# Process Improvement Project: SMFL Control Charts

May 26<sup>th</sup>, 2006 Katie McConky

#### Outline

- Project Overview
  Process Map
  Setting Specification Limits
- Future Needs

# **Project Overview**



#### **Process Map**



Click on each item to view details....

## What is Complete?

SPC Queries in MESA	Operation
SPC6SC_PAD	Pad Oxide
SPC6SC_CNT	Particle Count for Step 3
SPC6SC_FO	Field Oxide
SPC6SC_GOX	Gate Oxide
SPC6SC_KOX	KOOI Oxide
SPC6SC_LTO	Low Temperature Oxide
SPC6SC_MTL	Metal 1
SPC6SC_NI1	1500 A Nitride
SPC6SC_NI2	3500 A Nitride
SPC6SC_NRS	Test Measurement
SPC6SC_POL	Poly
SPC6SC_PRS	Test Measurement
SPC6SC_WO	Well Oxide
SPC6SC_WRS	Test Measurement
SPC6SC_2CD	2um CD Line Width
SPC6SC_350	Well Oxide?
SPC6SCPORS	DE01?

## What is Complete?

Process	Product	Step #	Recipe Desc	Required Action	Query Name in Mesa	Query Save Location	Power Point and Run File Location	Powerpoint Name	Document Name in MESA
PAD OXIDE	SMFL-CMOS SUB-CMOS 150 ADV-CMOS 150	4, 16 4, 17 4,10,39	500A oxideOX05 500A oxideOX05 500A oxideOX05	Make all 7 steps use the same control chart	SPC6SC_PAD SPC6SC_PAD SPC6SC_PAD	QGPL.SC6PAD QGPL.SC6PAD QGPL.SC6PAD	J:\QA\SCMOS150 J:\QA\SCMOS150 J:\QA\SCMOS150	pad.pps pad.pps pad.pps	SPC6SCPAD SPC6SCPAD SPC6SCPAD
PARTICLE COUNT	SMFL-CMOS SUB-CMOS 150 ADV-CMOS 150	3 3 3	particle count particle count particle count	Make all 3 products use the same control chart	SPC6SC_CNT SPC6SC_CNT SPC6SC_CNT	QGPL.PTCL_SC6 QGPL.PTCL_SC6 QGPL.PTCL_SC6	J:\QA\SCMOS150 J:\QA\SCMOS150 J:\QA\SCMOS150	ptcl.pps ptcl.pps ptcl.pps	SPC6SCPTCL SPC6SCPTCL SPC6SCPTCL
FIELD OXIDE GROWTH	SMFL-CMOS SUB-CMOS 150 ADV-CMOS 150	25 26 	163, 5hr Soak 950C 406, 65min Soak 1100C 	Make separate query and control chart for each product	SPC_FIELDO SPC6SC_FO 	QGPL.FIELDOX QGPL.SC6FLDOX 	J:\QA J:\QA\SCMOS150 	FIELD_OX.pps FIELDOX.pps 	SPC_FIELD_OX SPC6SCFIELDOX 
GATE OXIDE	SMFL-CMOS SUB-CMOS 150 ADV-CMOS 150	35 35 33	474, 310AOX06 215, 150AOX06 213, 100AOX06	Each product needs its own chart b/c unique recipes	SPC_GATEOX SPC6SC_GOX SPCADV_GOX	GATE_OX SC6GATEOX SPCADVGOX	J:\QA J:\QA\SCMOS150 J:\QA\ADVCMOS	Gate_ox.pps gateox.pps gate_ox.pps	SPC6SCGATEOX SPC_GATE_OX SPCADVGOX
KOOI OXIDE	SMFL-CMOS SUB-CMOS 150 ADV-CMOS 150	28 29 	311, 40 min wet O2 9000 311, 40 min wet O2 9000 	Add SMFL-CMOS to Sub- CMOS Query	SPC6SC_KOX SPC6SC_KOX 	SC6KOOIOX SC6KOOIOX	J:\QA\SCMOS150 J:\QA\SCMOS150 	KOOlox KOOlox 	SPC6SCKOOI SPC6SCKOOI 
	SMFL-CMOS SUB-CMOS 150	51, 61 50, 61	A6-FAC 0.4 TEOS A6-FAC 0.4 TEOS	SMFL-CMOS needs SPC in Instrs for both steps. SUB- CMOS needs SPC in Instrs for	SPC6SCLTO SPC6SCLTO	SC6CLTOX SC6CLTOX	J:\QA\SCMOS150 J:\QA\SCMOS150	LTO.pps LTO.pps	SPC6SCLTO SPC6SCLTO
LIO	ADV-CMOS 150 ADV-CMOS 150	62 18	A6-FAC 0.4 TEOS A6-FAC 0.6 TEOS	step 50. SMFL, SUB, and ADV step 62 can all be combined into one SPC. ADV CMOS step 18 needs its own SPC.	SPC6SCLTO SPCADV_LTO	SC6CLTOX SC6ADVLTO	J:\QA\SCMOS150 J:\QA\ADVCMOS	LTO.pps LTO.pps	SPC6SCLTO SPCADVLTO

# **Setting Spec/Control Limits**

#### Specification Limits:

- USL: The largest value acceptable where the device will still work.
- LSL: The smallest value acceptable where the device will still work.

#### Control Limits:

- UCL: Upper limit, that when exceeded indicates the process is out of control.
- LCL: Lower limit, that when exceeded indicates the process is out of control.

# **Setting Spec/Control Limits**



# **Setting Spec/Control Limits**

<u>Specifications Limits</u> are set by engineers Dependent on the process requirements • ie. Minimum oxide thickness required to mask an implant Discussions and simulations can help to decide <u>Control Limits</u> are a result of the process itself Control limits are usually 3 sigma from the target Ideally your control limits should be within your specification limits.

#### **Future Needs**

- Finish updating the remaining control charts
- Avoid using Step # in queries because adding or deleting steps from a process requires the queries to be updated.
  - Replace Step # with unique operation names
- Set specifications limits with more thought



## **Edit/Create Query**

- **1.** Decide what process steps to include in your query:
  - 1. Include all steps for all processes that use the same recipe
  - 2. PAD OXIDE EXAMPLE:

PROCESS	Step #	Recipe
SMFL-CMOS	4, 16	250
SUB-CMOS 150	4, 17	250
ADV-CMOS 150	4, 10, 39	250

2. Compare your needs to the current query:
1. Make changes to the existing query if necessary (See next slides for how to access the queries)



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F12=Cancel F19=Next group		

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Page Down through the list of queries until you reach queries starting with SPC:

SPC\_ are used for unique SMFL- CMOS

SPC6SC\_ are used for SUB-CMOS and all processes with identical operations

**SPCADV\_** are used for unique ADV-CMOS processes



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QuerySPC6SC_PADOptionDISPLAYLibraryQGPLCCSID65535
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#### **Running The Query**

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SPC6SC_LT0	SPC6SC_WRS SPC6SC_2CD SPC6SC_350 SPC6SCPORS STATUS TEST01 TOKUNAGA01 TOKUNAGA02 TOKUNAGA03	- VEGA - VEGA - VEGA - WILL - WOO - YAT - YAT - YAT - ZEN	After editing t appropriate question	ZENNER02 ZENNER03 the query, press '9' on the uery to run the query. This ry data to the query save
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# **Finding The Query Save Location**



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#### **Finding The Query Save Location**



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#### **Finding The Query Save Location**



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### **References in .HED File**

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#### **Run File**



#### **Power Point Show**





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### **Instruction Sets**

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ADV-CMOS-OX06-DRIVE 150	ADV-CMOS 0X06 DRY 0XIDE DRIVE	FULLER
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_ ADV-CMOS-OX08-ANNEAL 150	ADV-CMOS TEOS Densification	DJH2265
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