

INTRODUCTION

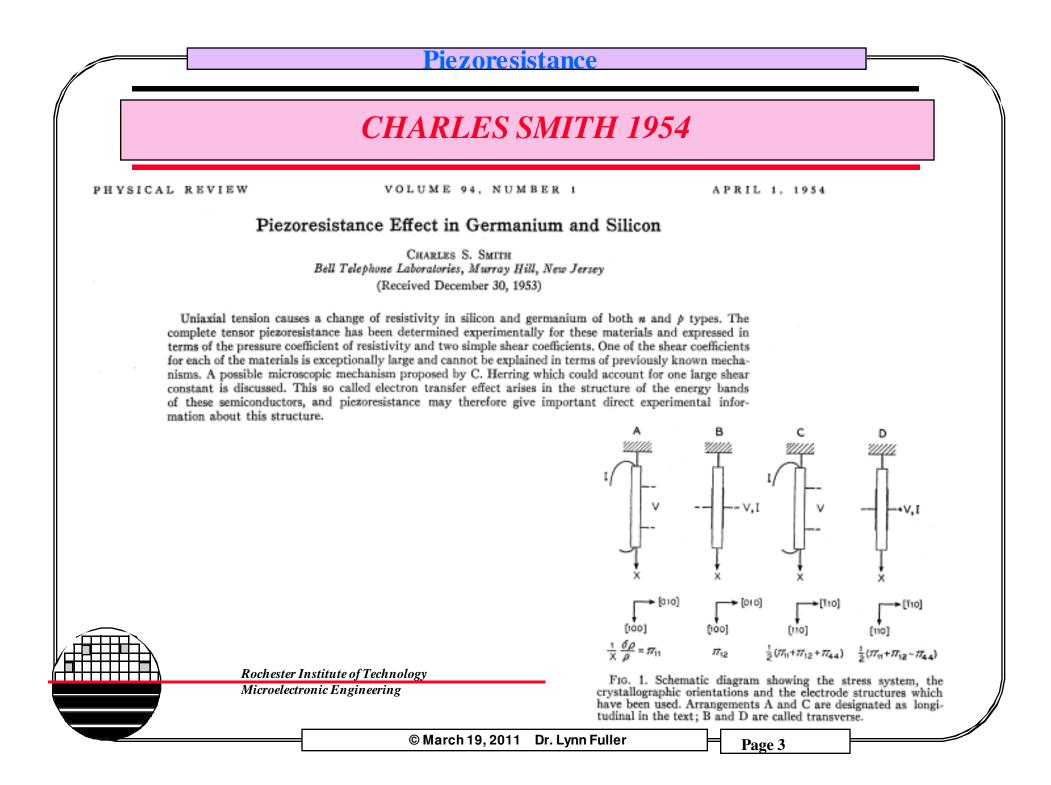
The piezoresistive effect was first reported in 1954 [1] and has been used in making sensors for years. The effect of strain on the mobility of electrons and holes in semiconductors is important in today's sensors and transistors.

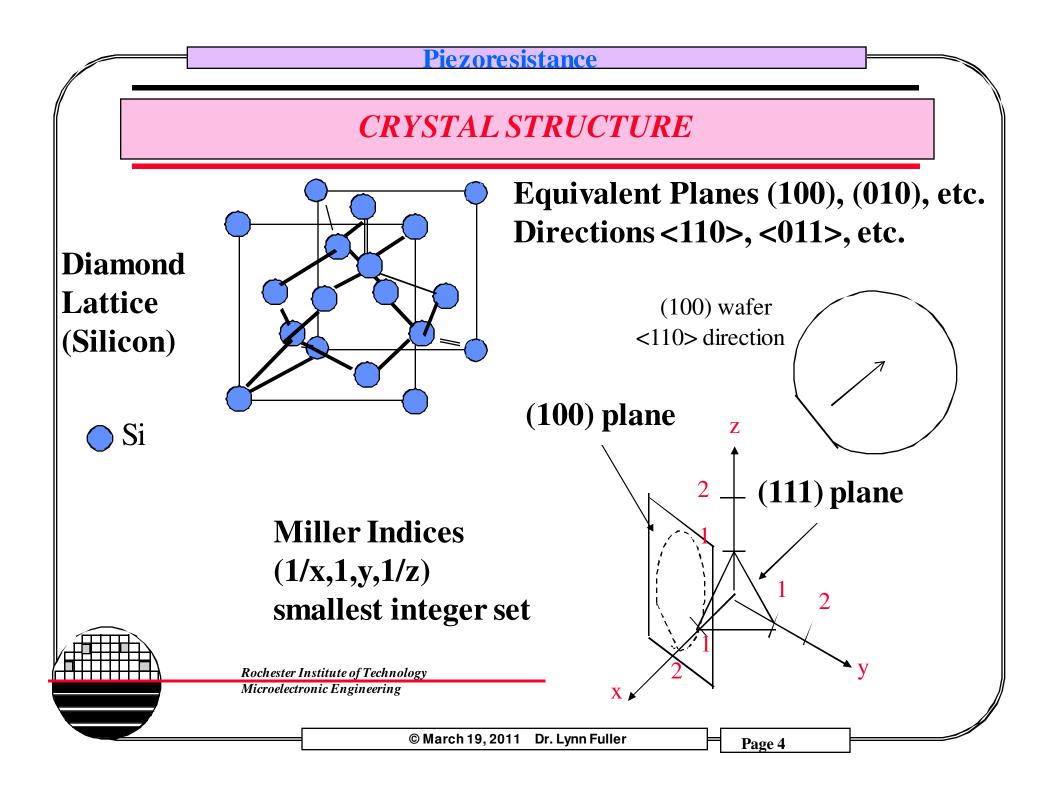


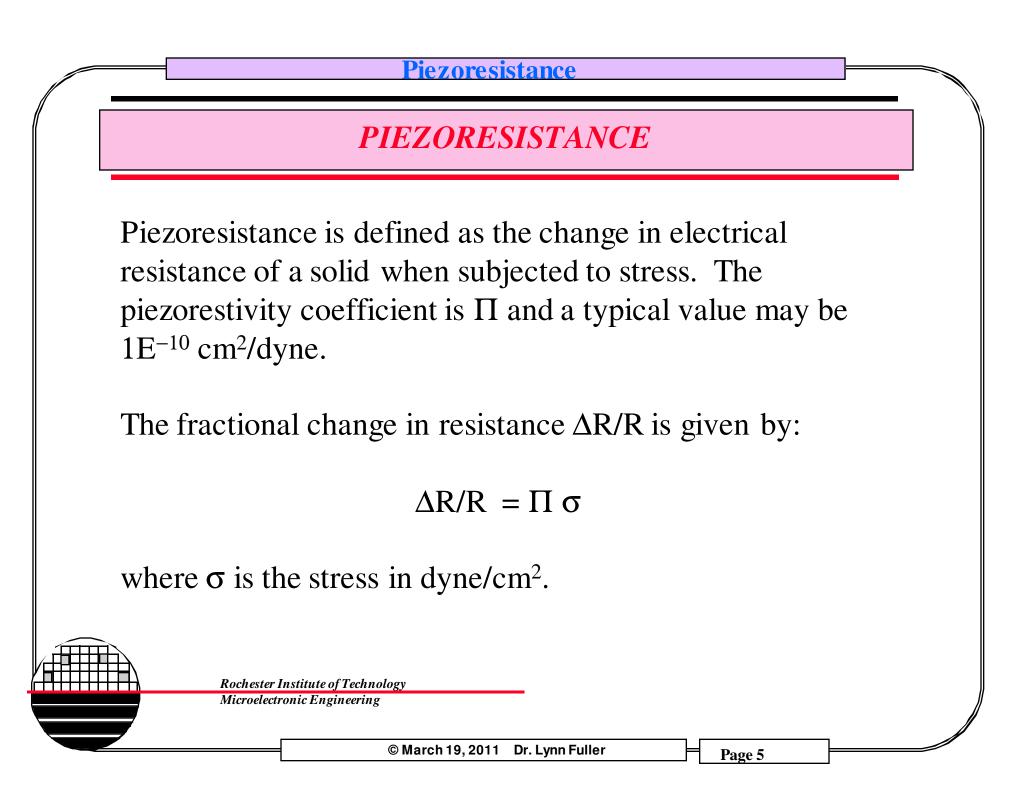
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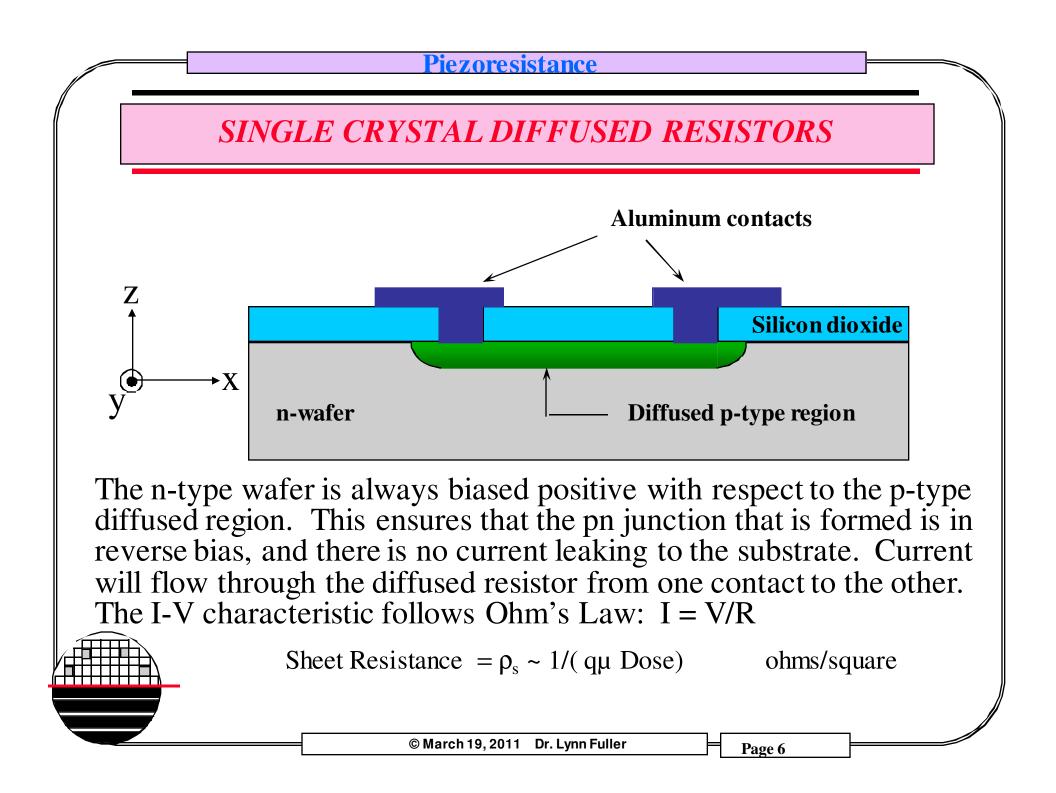
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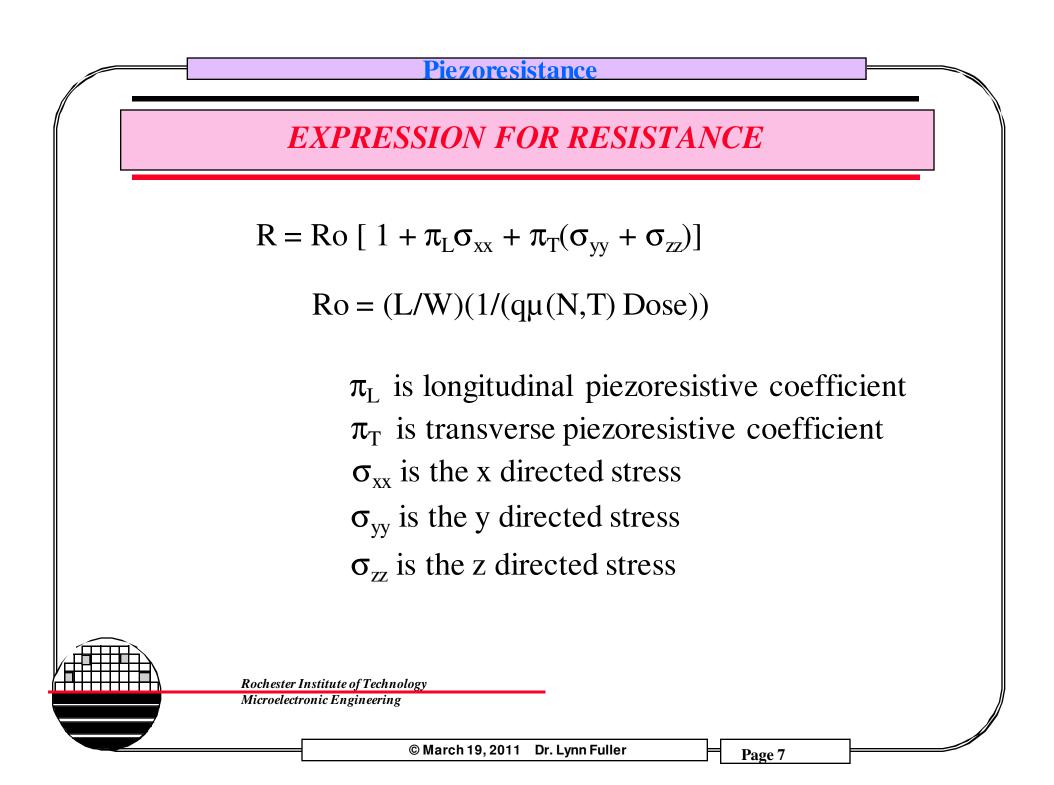
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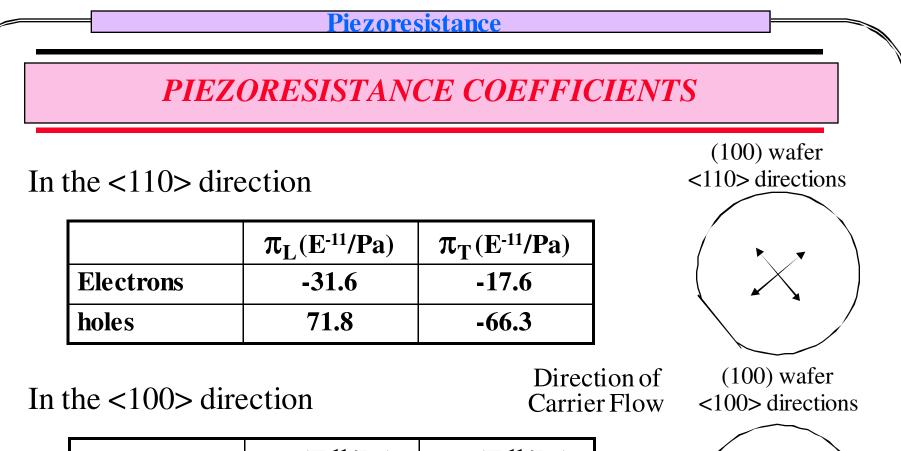








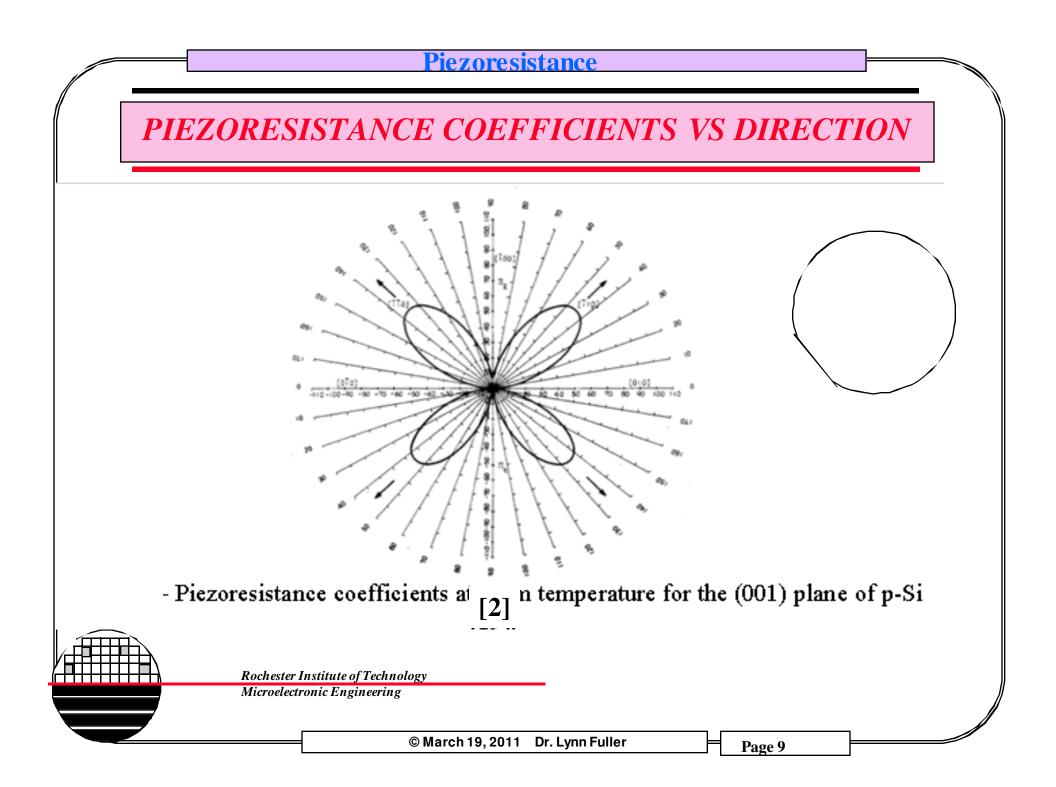




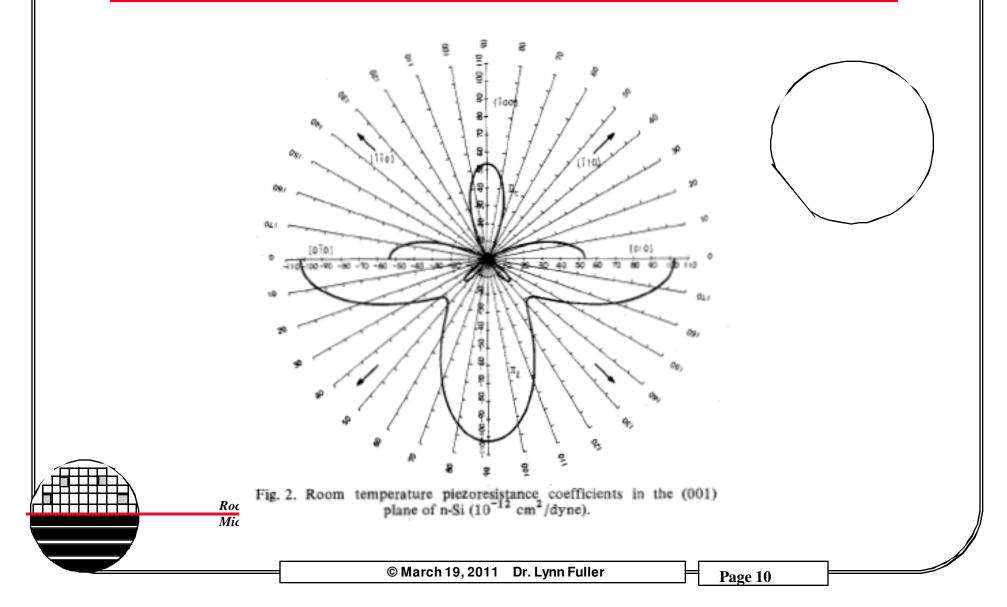
	$\pi_{L}(E^{-11}/Pa)$	$\pi_{\mathrm{T}}(\mathrm{E}^{-11}/\mathrm{Pa})$
Electrons	-102	53.4
holes	6.6	-1.1

Tensile strain in (100) silicon increases mobility for electrons for flow in <110> direction Compressive strain in (100) silicon increases mobility for holes for flow in <110> direction

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PIEZORESISTANCE COEFFICIENTS VS DIRECTION



EXAMPLE: PIEZORESISTANCE

Example: Find the maximum stress in a simple polysilicon cantilever with the following parameters. Ymax = 1 μ m, b=4 μ m, h=2 μ m, L=100 μ m

 $\sigma_{x=0} = 5.6e7$ newton/m2 = 5.6e8 dyne/cm2

From example in mem_mech.ppt

Continue Example: What is the change in resistance given $\Pi = 1e-10 \text{ cm}^2/\text{dyne}$

 $\Delta R/R = \Pi \sigma = (1e-10 \text{ cm}2/\text{dyne})(5.6e8 \text{ dyne/cm}2)$ = 5.6%

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SUMMARY FOR MOBILITY / STRAIN

1. Mobility is affected by strain in semiconductors. Mobility can be increased or decreased depending on the type of strain (tensile, compressive) and the direction of strain relative to crystal orientation and current flow.

For (100) wafers and current flow in <110> direction:

2. Tensile strain n-type silicon enhances mobility of electrons. Tensile strain transverse to current flow enhances mobility of electrons.

3. Compressive strain in the direction of current flow in p-type silicon enhances mobility of holes. Tensile strain transverse to current flow enhances mobility of holes.



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REFERENCES

1. Charles S. Smith, "Piezoresistance Effect in Germanium and Silicon," Physical Review, Vol 94, No.1, April 1, 1954.

2. Y. Kanda, "A graphical representation of the piezoresistance coefficients in silicon," *Electron Devices, IEEE Transactions on*, vol. 29, no. 1, pp. 64-70, 1982.

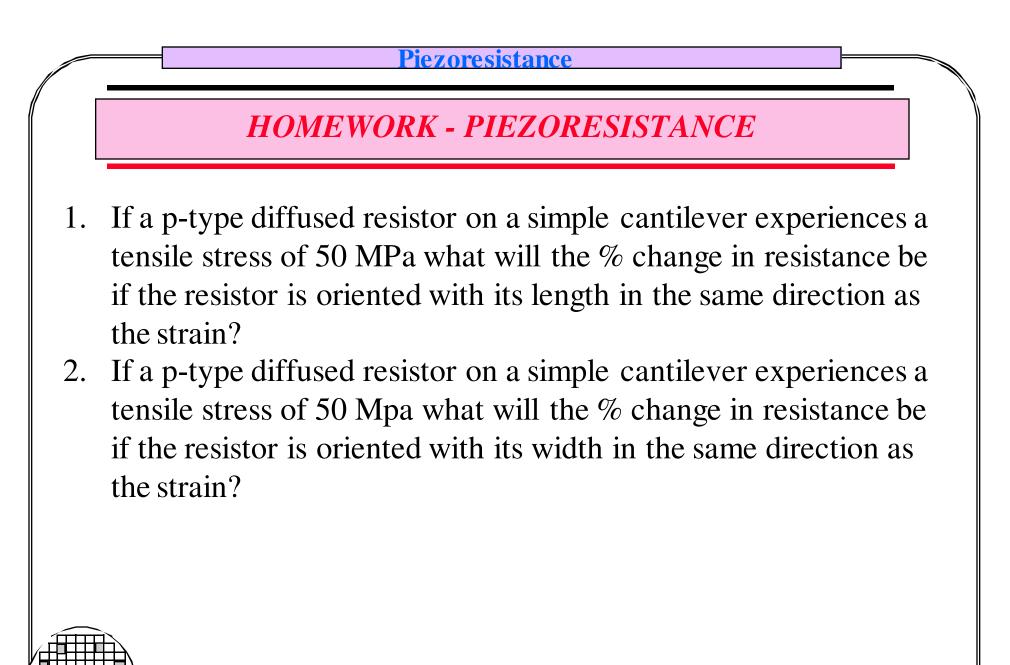
 C. Mazure, and I. Cayrefourcq, "Status of device mobility enhancement through strained silicon engineering." pp. 1-6.
A. A. Barlian, W. T. Park, J. R. Mallon *et al.*, "Review: Semiconductor Piezoresistance for Microsystems," *Proceedings of the IEEE*, vol. 97, no. 3, pp. 513-552, 2009.



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