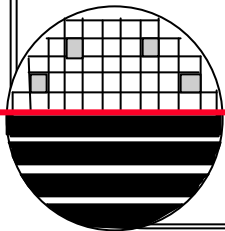


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MICROELECTRONIC ENGINEERING**

Resist Exposure and Development (Development Rate Monitor)

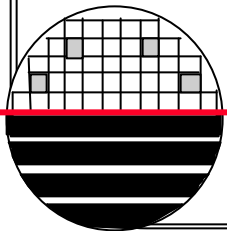
Dr. Lynn Fuller

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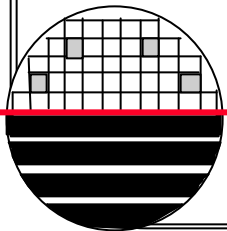
OUTLINE

Introduction
Modeling of Exposure in Photoresist
ABC Parameters
Modeling of Development
Measurement of Development Rate
Signal vs Time
Thickness vs Time
Thickness vs Exposure
Gamma
Development Rate Monitor
References
Homework



INTRODUCTION

Measurement of the development rate of photoresist versus time gives a host of information about the resist, developer, reflections from the substrate and system parameters such as gamma.



MODELING OF EXPOSURE IN PHOTORESIST

Complex Index of Refraction of Photoresist

$$n^{\wedge} = n - jK$$

$K = a l / 4 p$ is the extinction coefficient

n = real part of the index of refraction

λ = wavelength

The absorption constant a

$$a = A m(z,t) + B$$

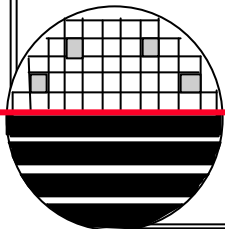
where m is the relative amount of photoactive inhibitor at position z and time t

A is the exposure dependent parameter

B is the exposure independent parameter

An optical sensitivity parameter C relates the destruction of inhibitor local irradiance, I

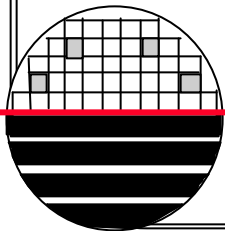
$$\frac{dm(z,t)}{dt} = -I(z,t) m(z,t) C$$



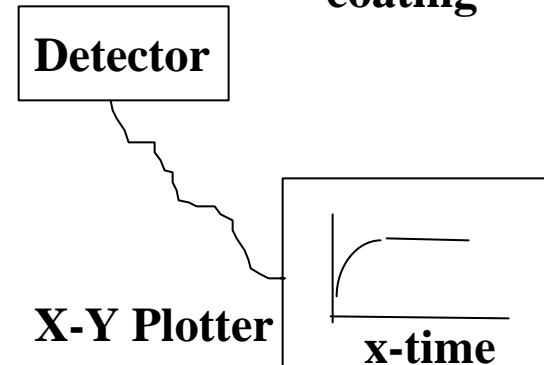
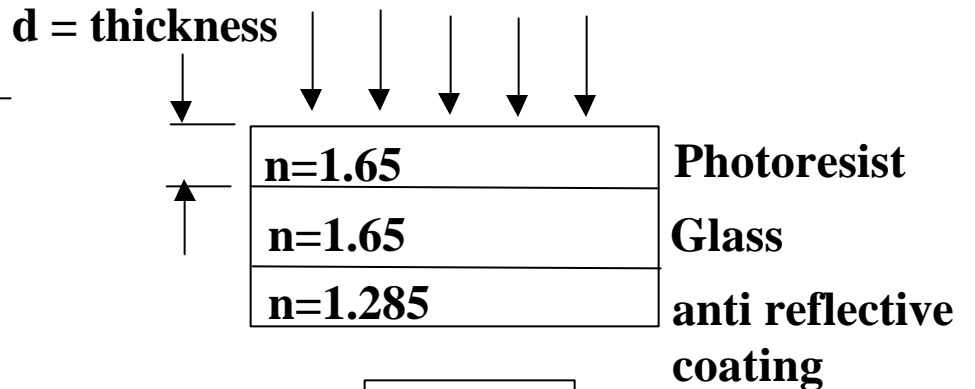
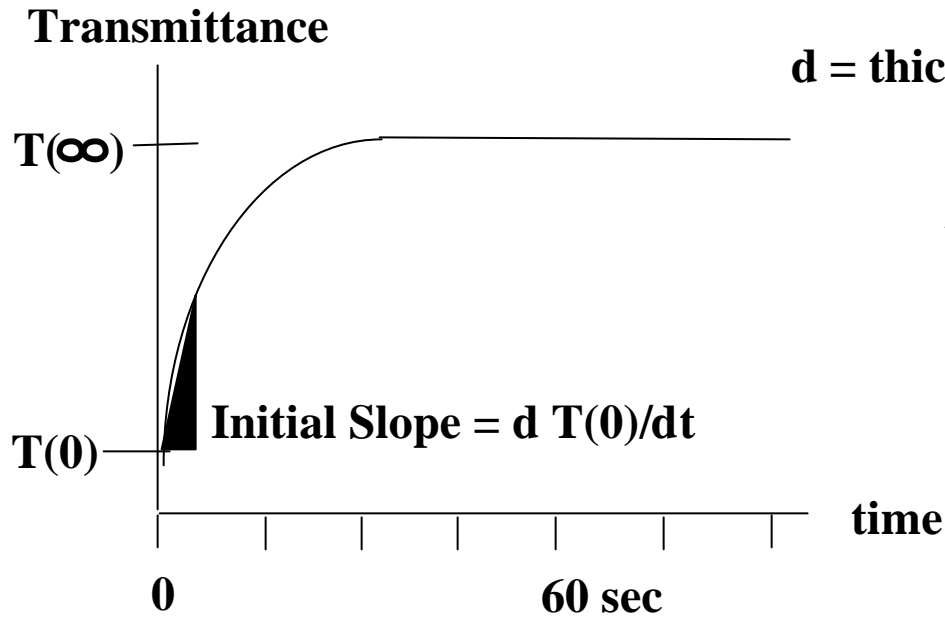
A,B,C PARAMETERS FOR EXPOSURE

A,B,C Exposure Parameters for AZ 1350J

l	A (μm^{-1})	B (μm^{-1})	C (cm^2/mj)	n
436nm	0.54	0.06	0.014	1.68
405nm	0.86	0.07	0.018	1.70
365nm	0.74	0.20	0.012	1.72



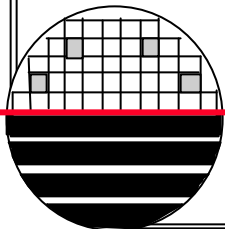
MEASUREMENT OF A,B,C, PARAMETERS



$$A = 1/d \ln [T(\infty)/T(0)]$$

$$B = -1/d \ln T(\infty)$$

$$C = \{A+B/AI_0 T(0) (1-T(0))\} dT(0)/dt$$

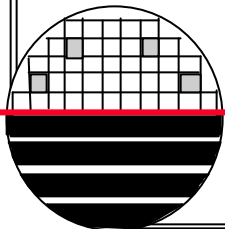
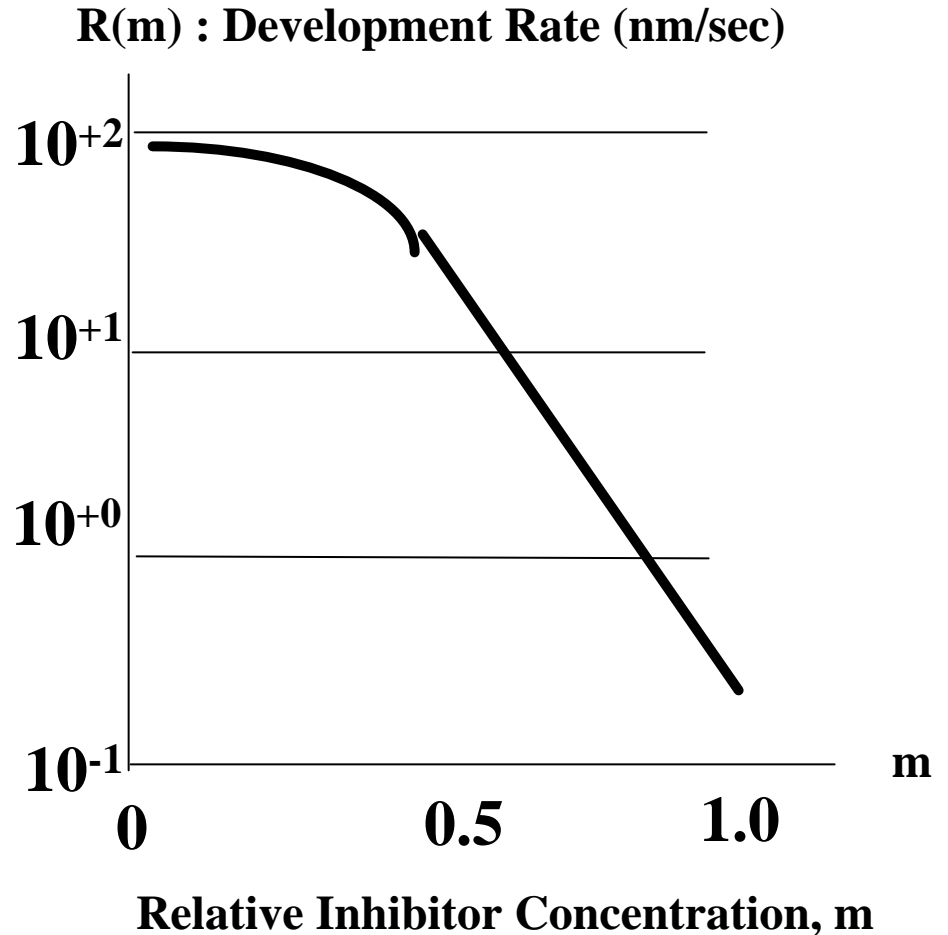


MODELING OF DEVELOPMENT IN POSITIVE RESIST

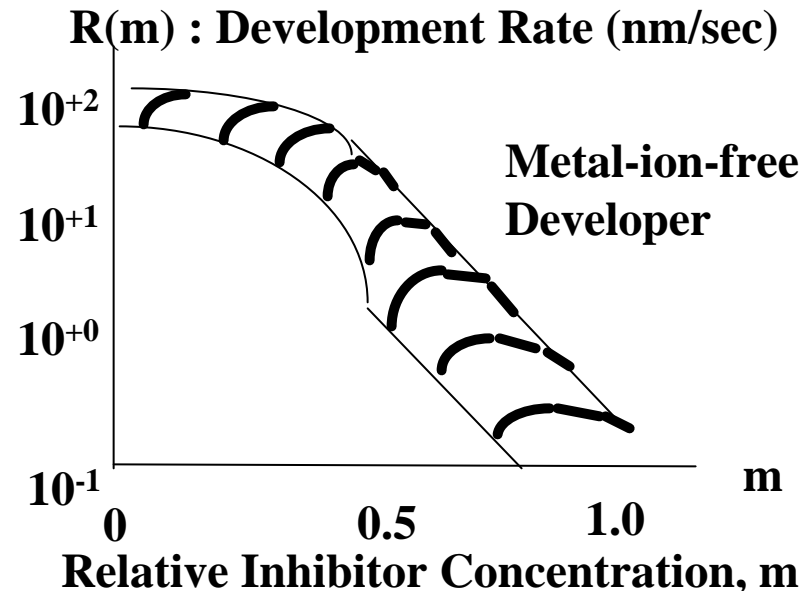
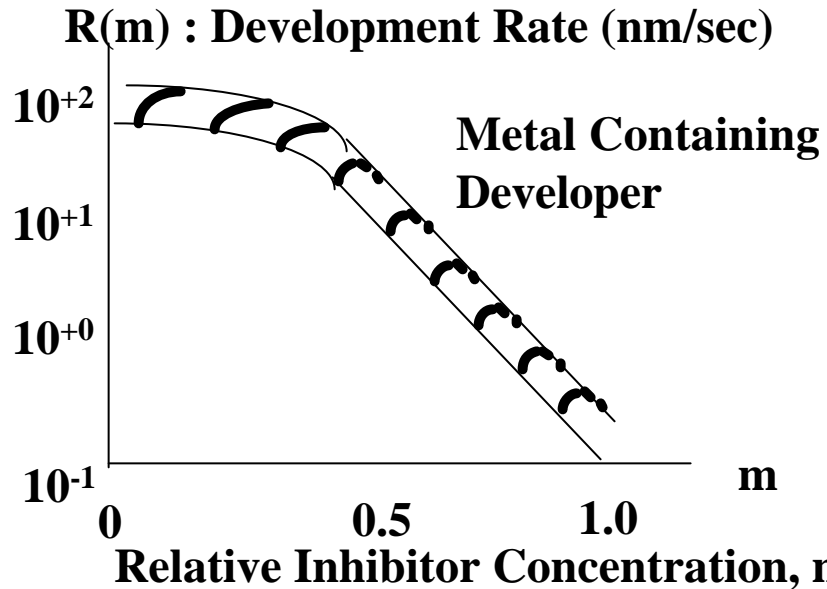
Kim's Model:

$$R(m) = \frac{1}{\frac{1 - m e^{-R_s(1-m)}}{R1} + \frac{m e^{-R_s(1-m)}}{R2}}$$

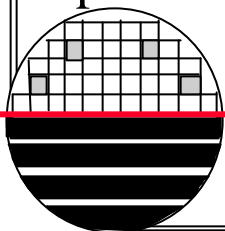
where R1 is rate of fully exposed resist
 R2 is rate of unexposed resist
 Rs is sensitivity of development rate to changes in m



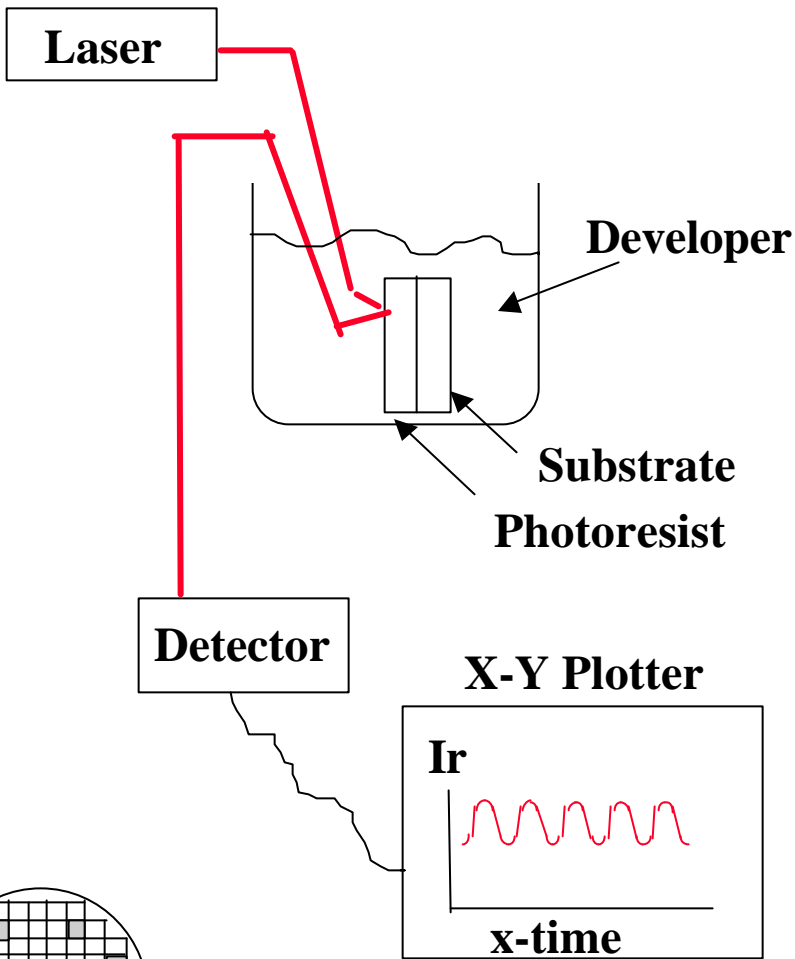
SURFACE RATE RETARDATION EFFECT



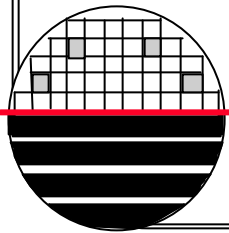
This retardation may be described by a multiplicative factor which varies linearly with exposure parameter m . The value of the factor with $m=0$ is called $R5$ and with $m=1$ is called $R6$. The depth dependence factor is exponential with characteristic delay distance $R4$. Metal-ion-free developer uses additional parameters $R7, R8, R, R10$.



MEASUREMENT OF DEVELOPMENT RATE



Convert the data I_r vs t into X_{pr} vs t
The slope dX_{pr}/dt is the development rate.



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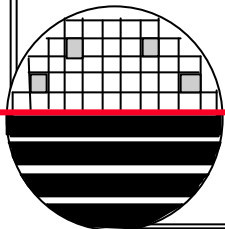
ORIGIN OF THE SIGNAL

Light of wavelength
 λ

Detector

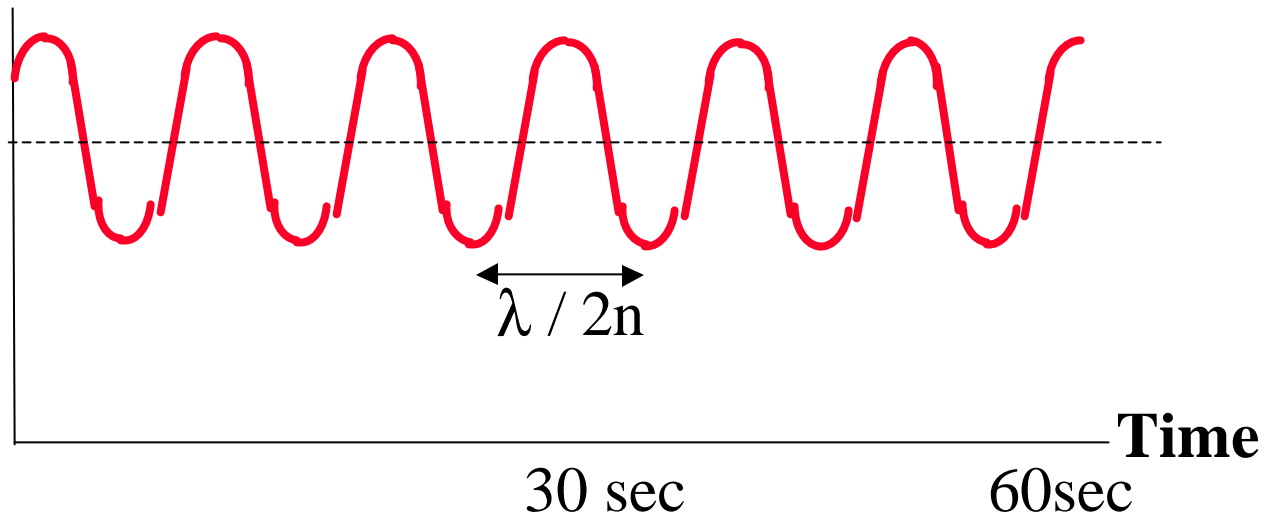


The reflected light intensity is the combination of the light reflected from the surface of the resist and from the surface of the substrate. If the resist thickness is a multiple of $\lambda/2n$ then the light reflected from the substrate will be in phase with the light reflected from the surface of the resist. The combination will be a signal maximum. As the resist is developed it becomes thinner giving signal minima and maxima until the resist is gone.

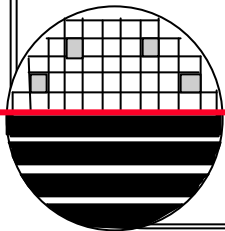


SIGNAL VERSUS TIME

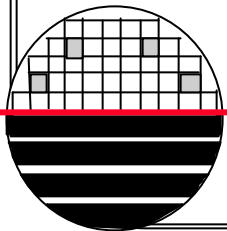
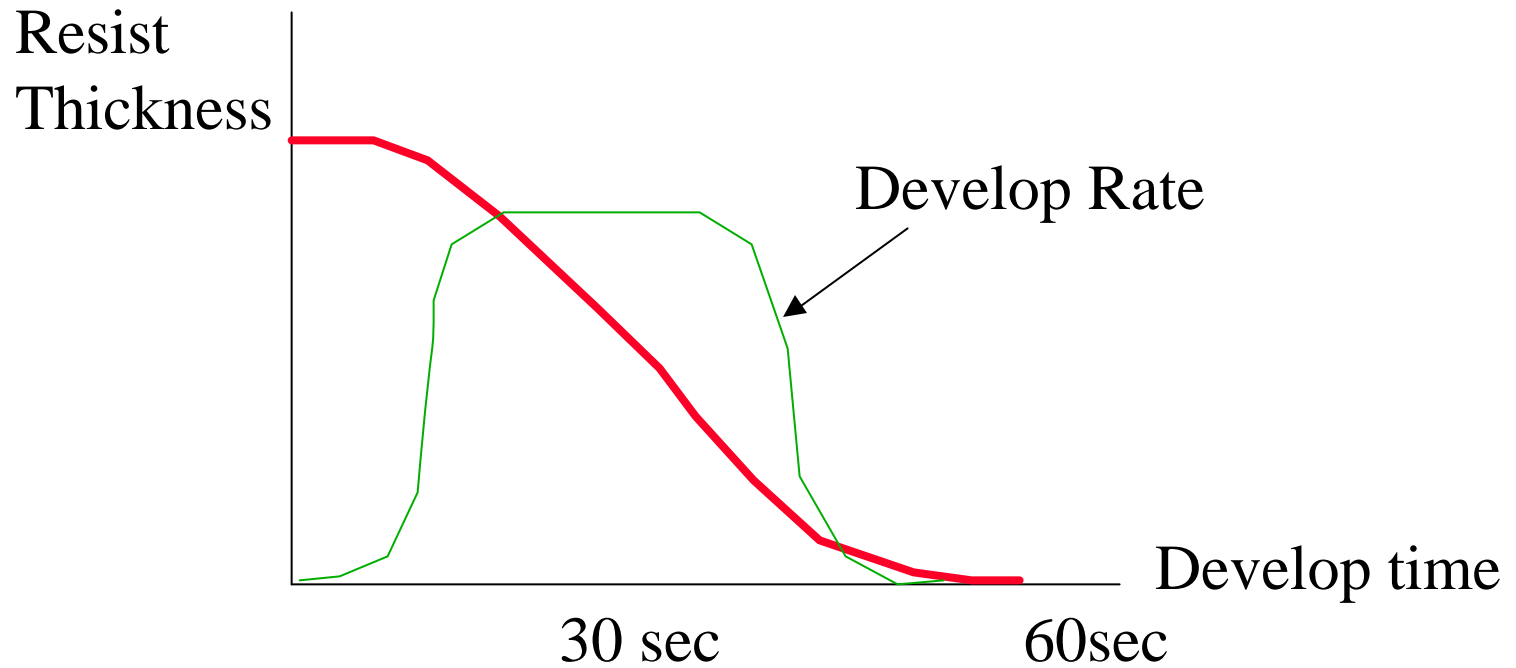
Ir



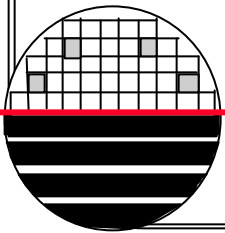
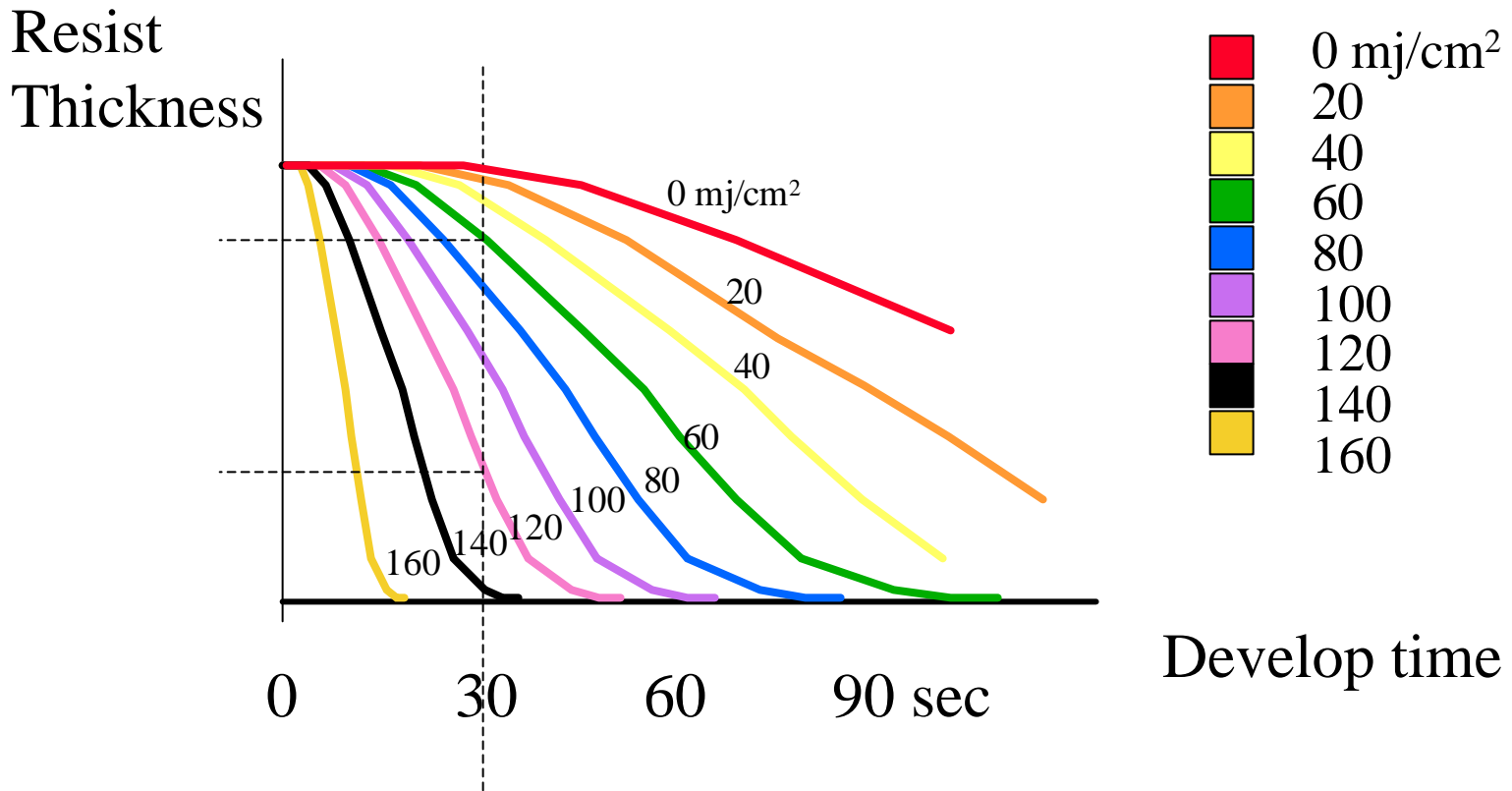
Example: $\lambda = 6328\text{\AA}$ and total of ~ 6 cycles each representing $\lambda/2n$ and $n = 1.68$ so starting resist thickness = $1.13\ \mu\text{m}$



THICKNESS AND RATE VERSUS TIME

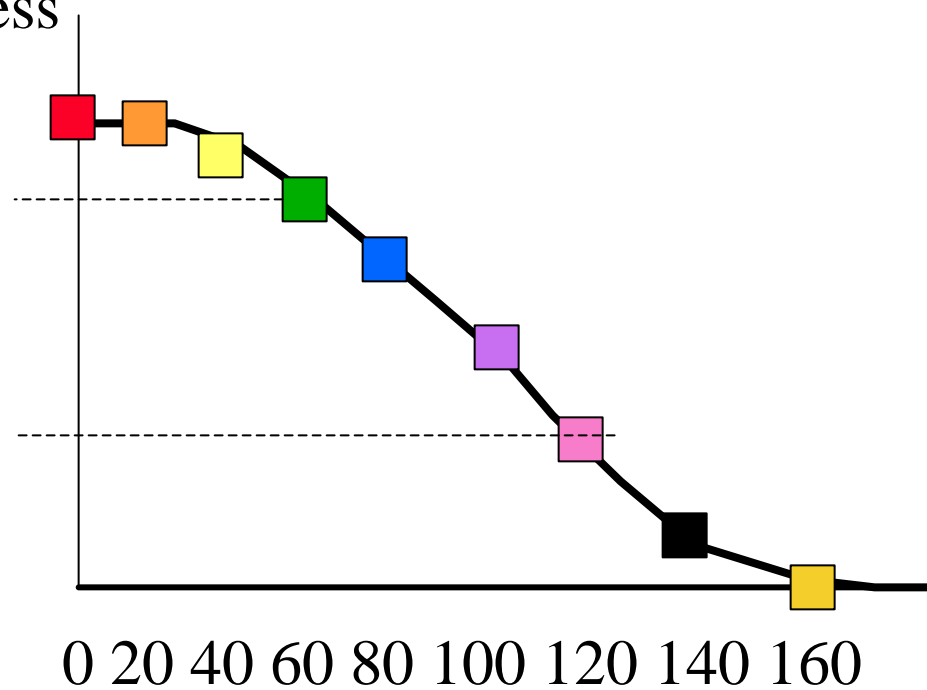


THICKNESS VERSUS TIME FOR DIFFERENT EXPOSURE

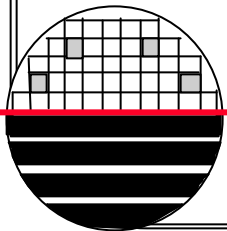


THICKNESS VERSUS EXPOSURE

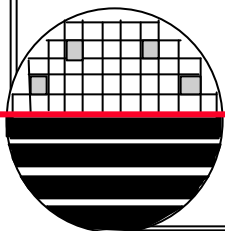
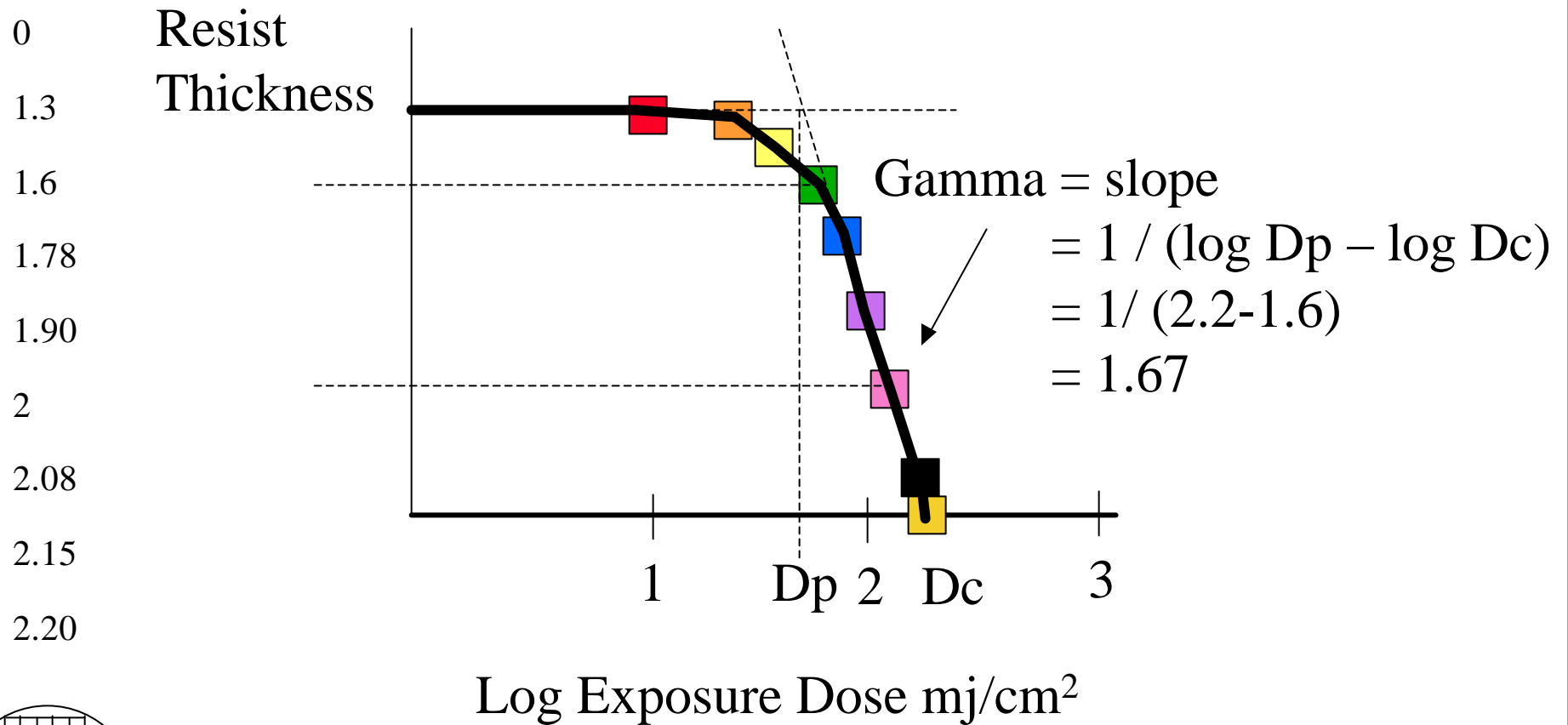
Resist
Thickness



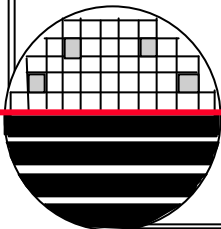
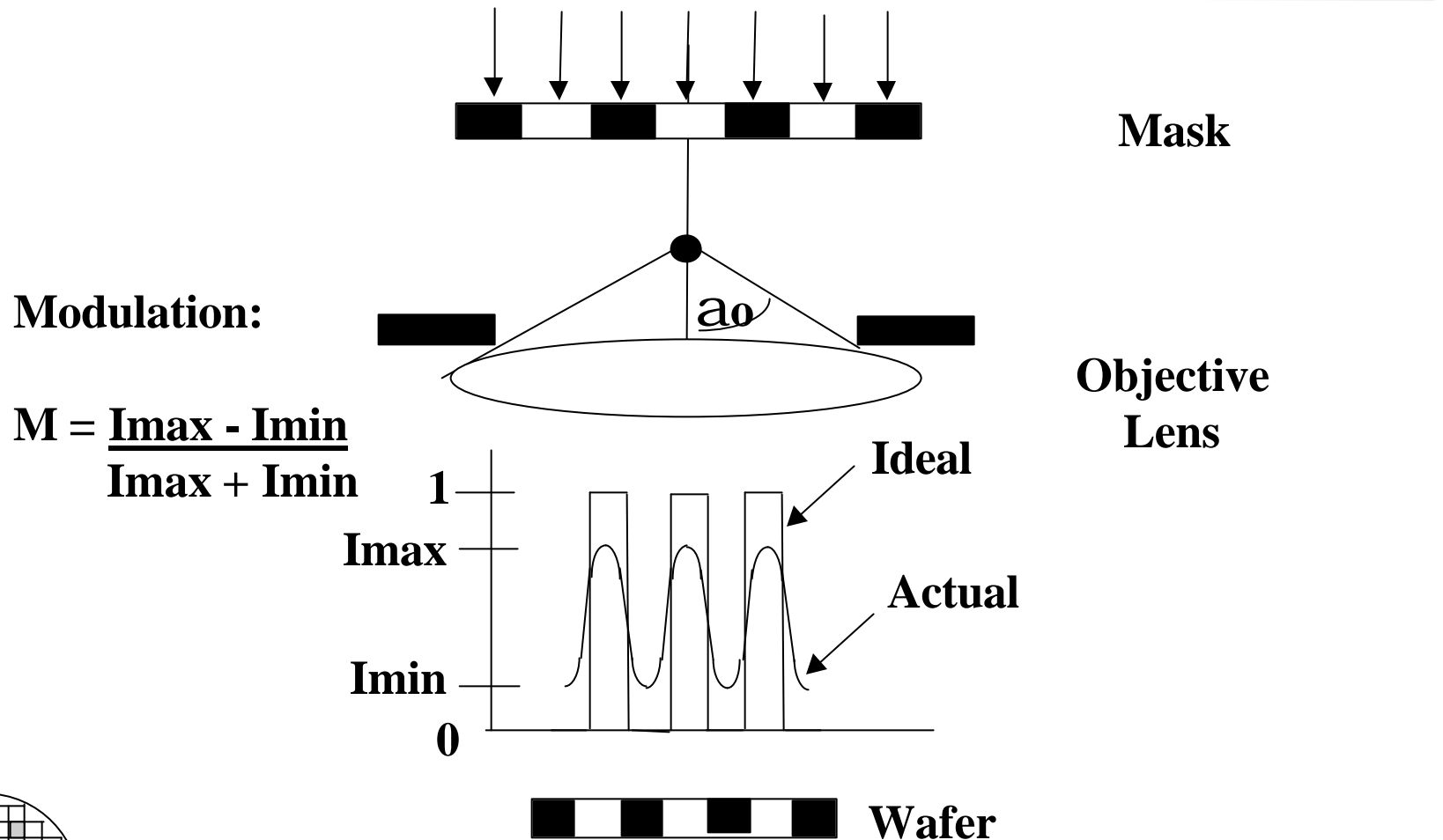
Exposure Dose
mj/cm²



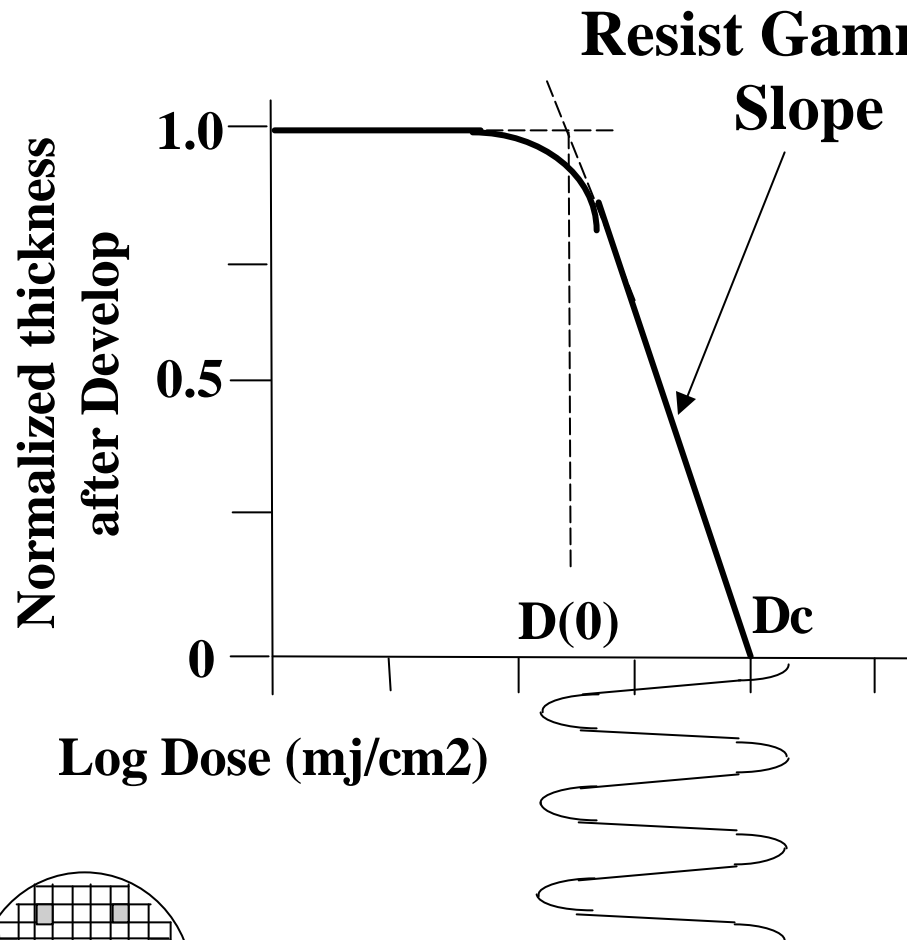
THICKNESS VERSUS LOG EXPOSURE - GAMMA



MODULATION

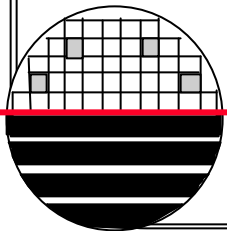


RELATIONSHIP BETWEEN MODULATION AND GAMMA



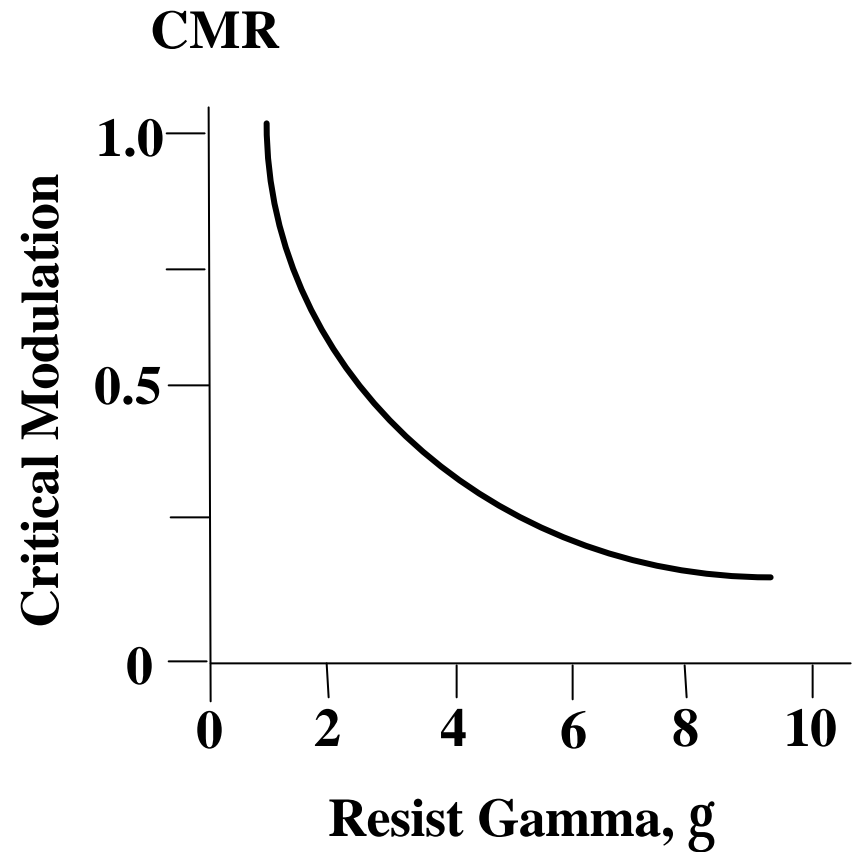
**D_c is the dose to clear
 $D(0)$ is the max dose for
unexposed areas**

**The higher the slope or
contrast, gamma, then the
smaller the difference needs
to be between exposure in
areas to be cleared and areas
to leave resist. That is the
required aerial image
modulation is smaller.**

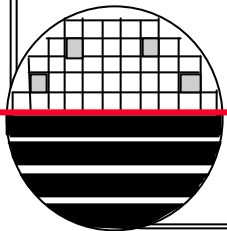


CRITICAL MODULATION FOR RESIST

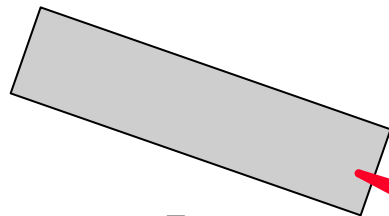
The higher the contrast, gamma, the smaller the required aerial image modulation.



$$CMR = (10^{1/g} - 1) / (10^{1/g} + 1)$$

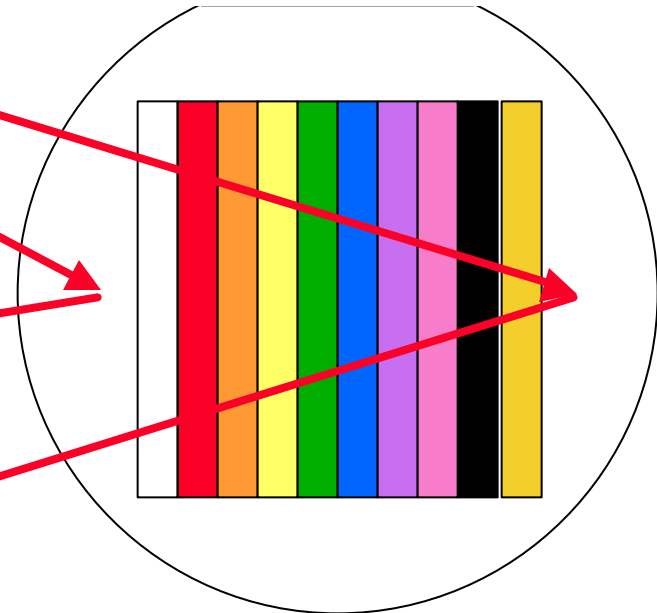


DEVELOPMENT RATE MONITOR (DRM)

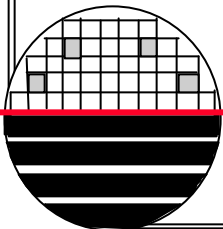


Laser

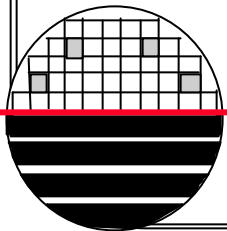
Photo
Diode
Array



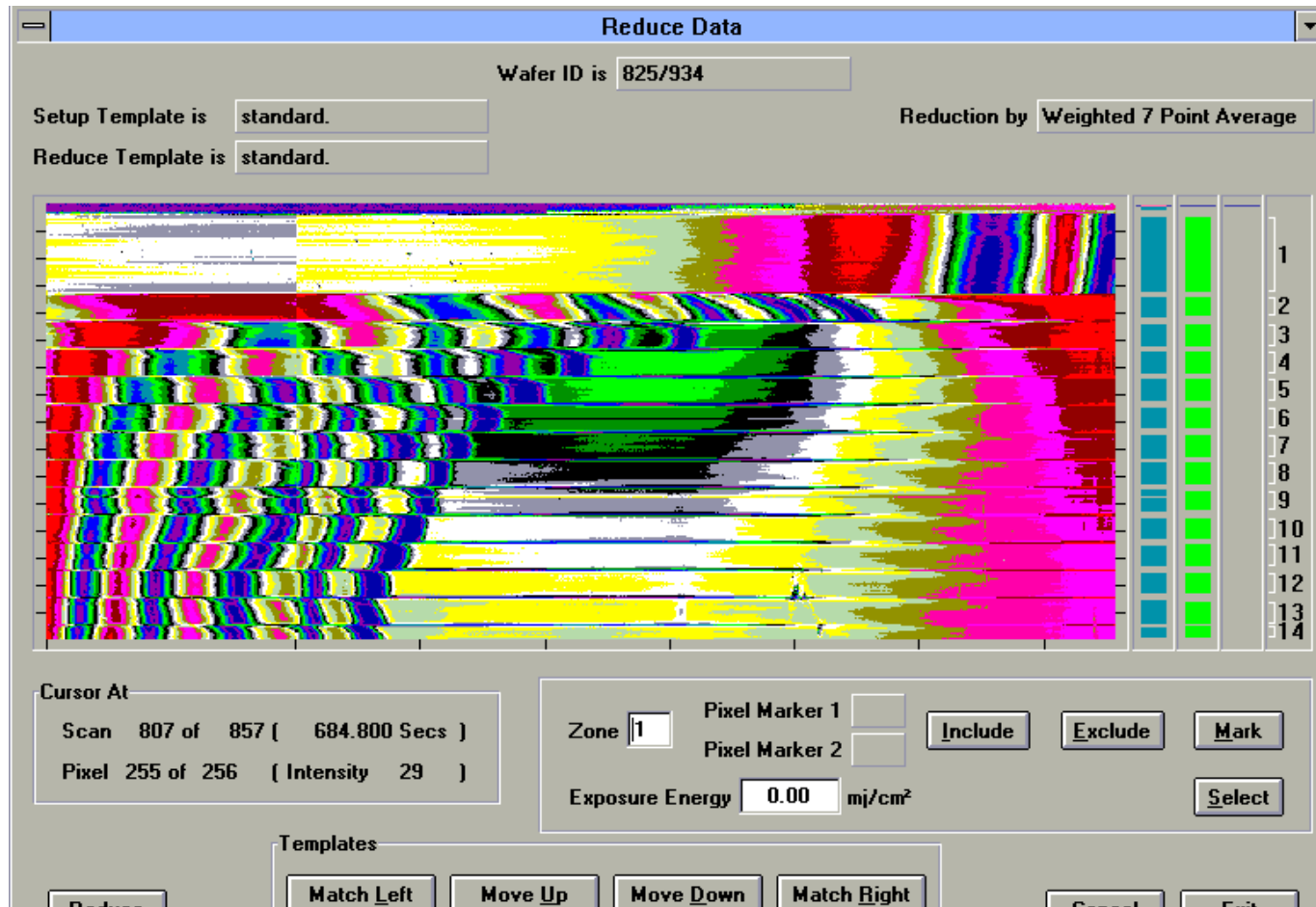
Wafer with different exposures
(special stepper job)



PERKIN ELMER DRM

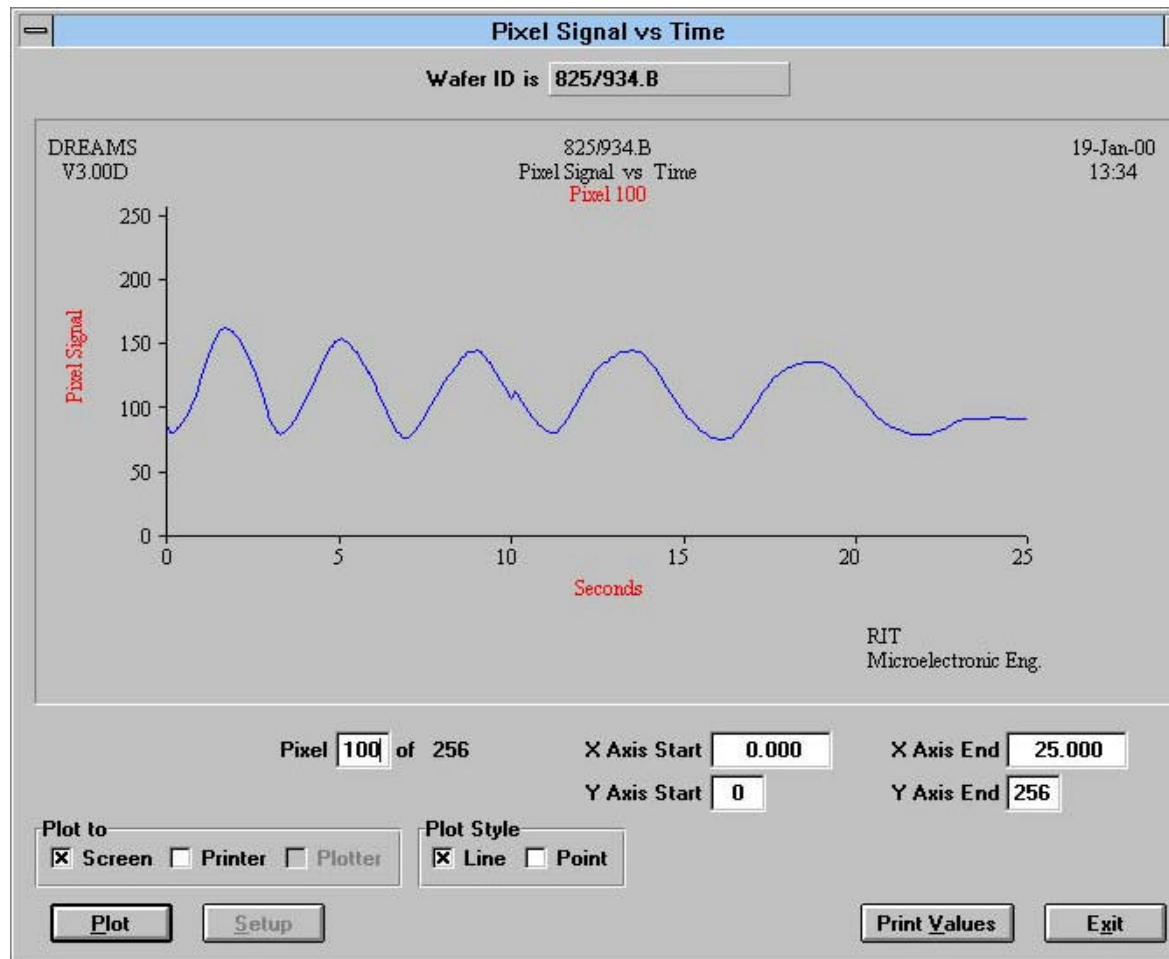


RAW DATA FROM 256 PHOTO DIODES



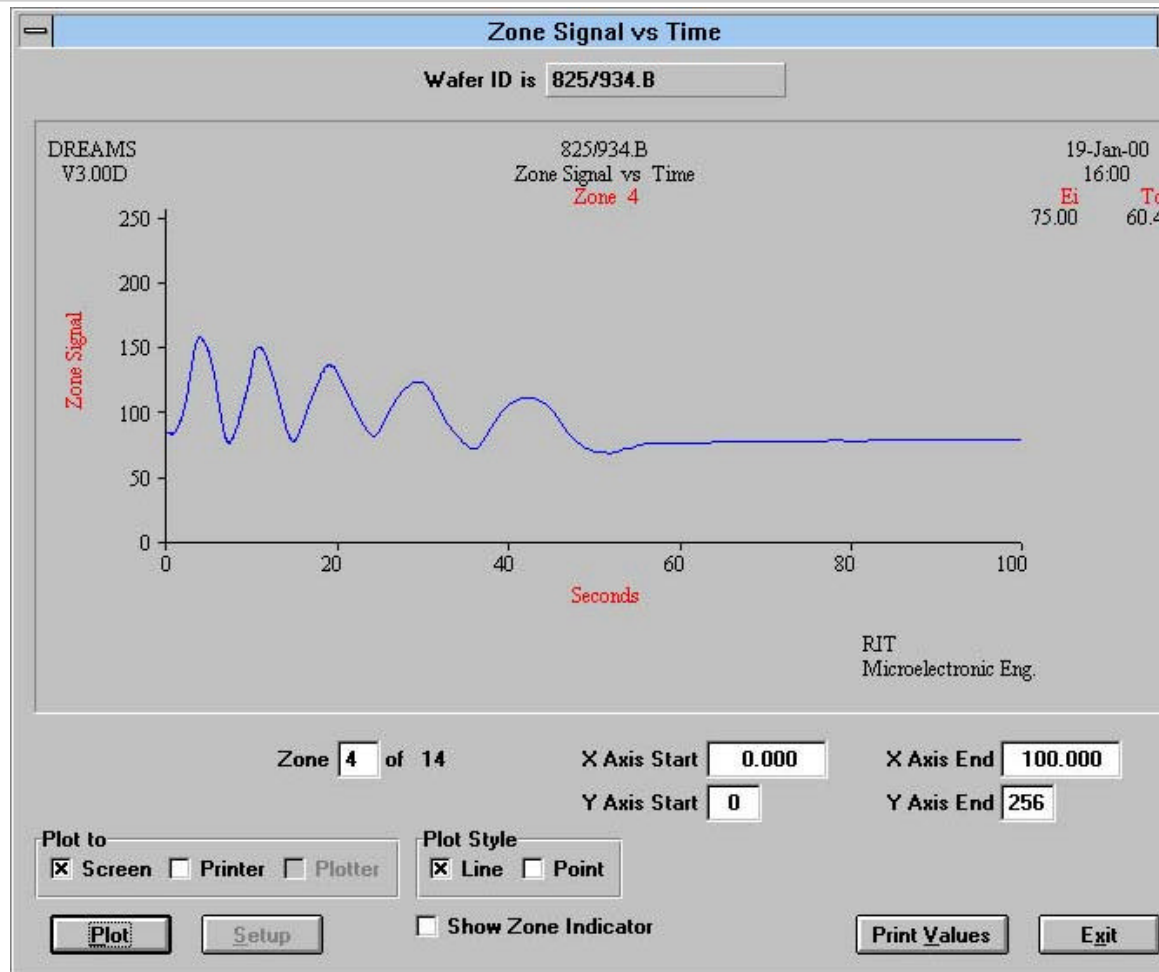
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PIXEL #100 SIGNAL VERSUS TIME



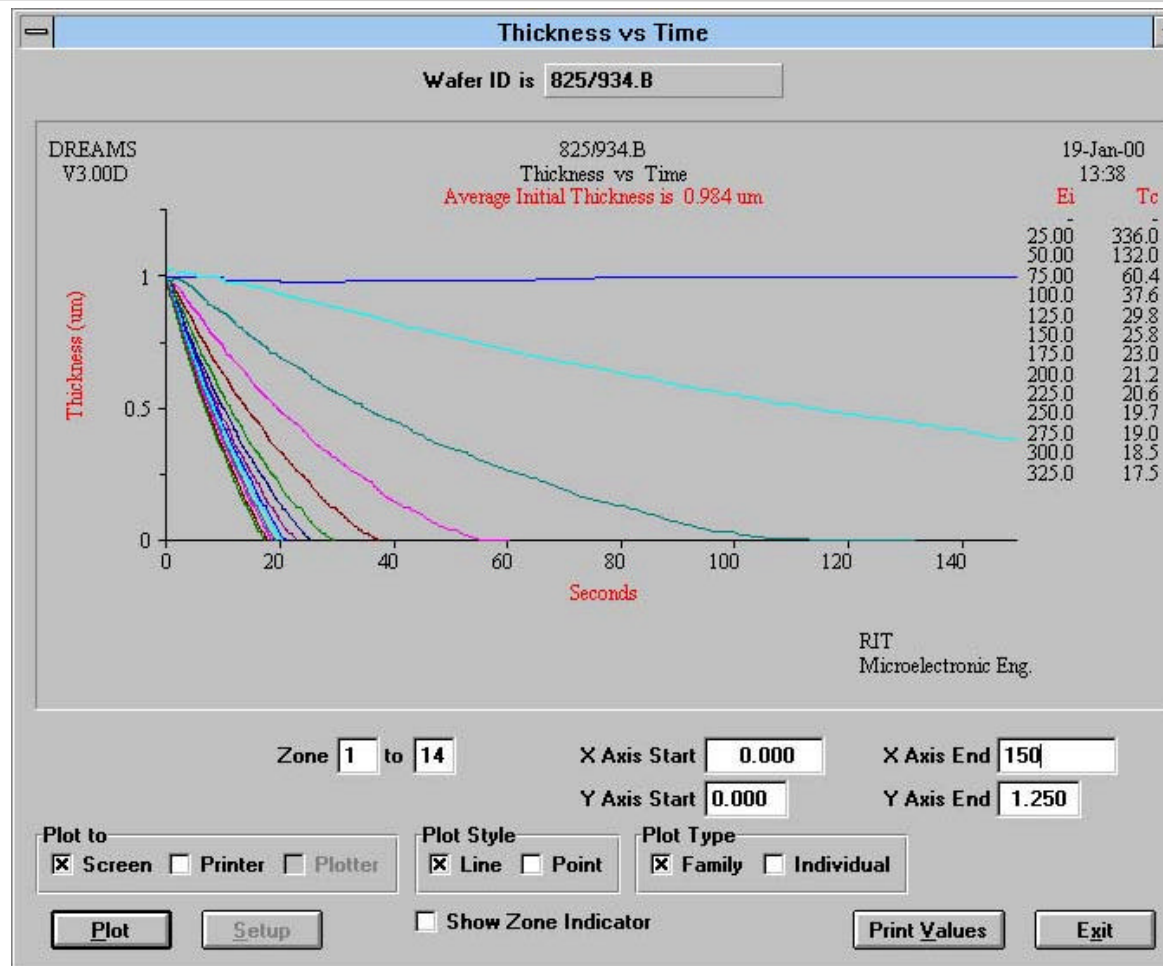
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ZONE THICKNESS VERSUS TIME



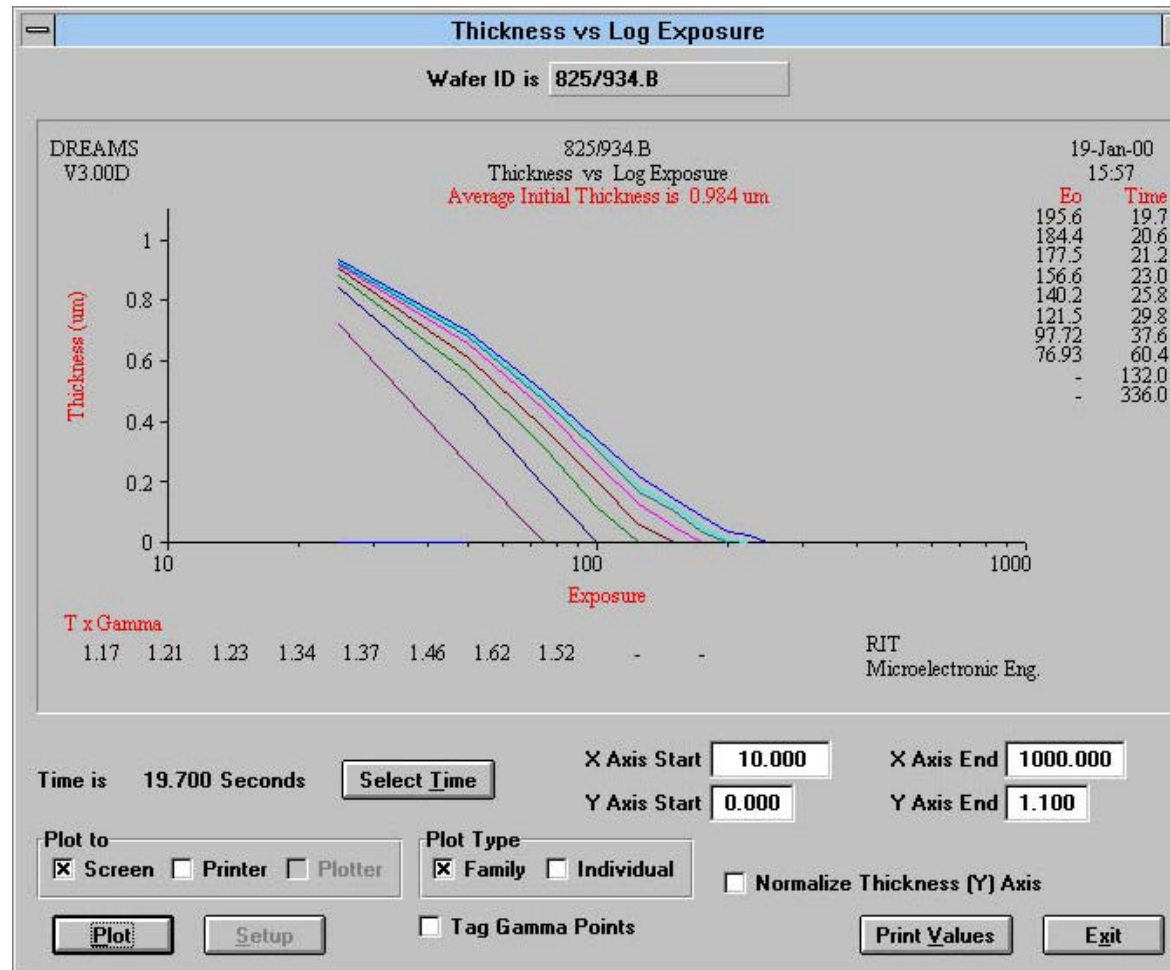
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THICKNESS VERSUS TIME FAMILY



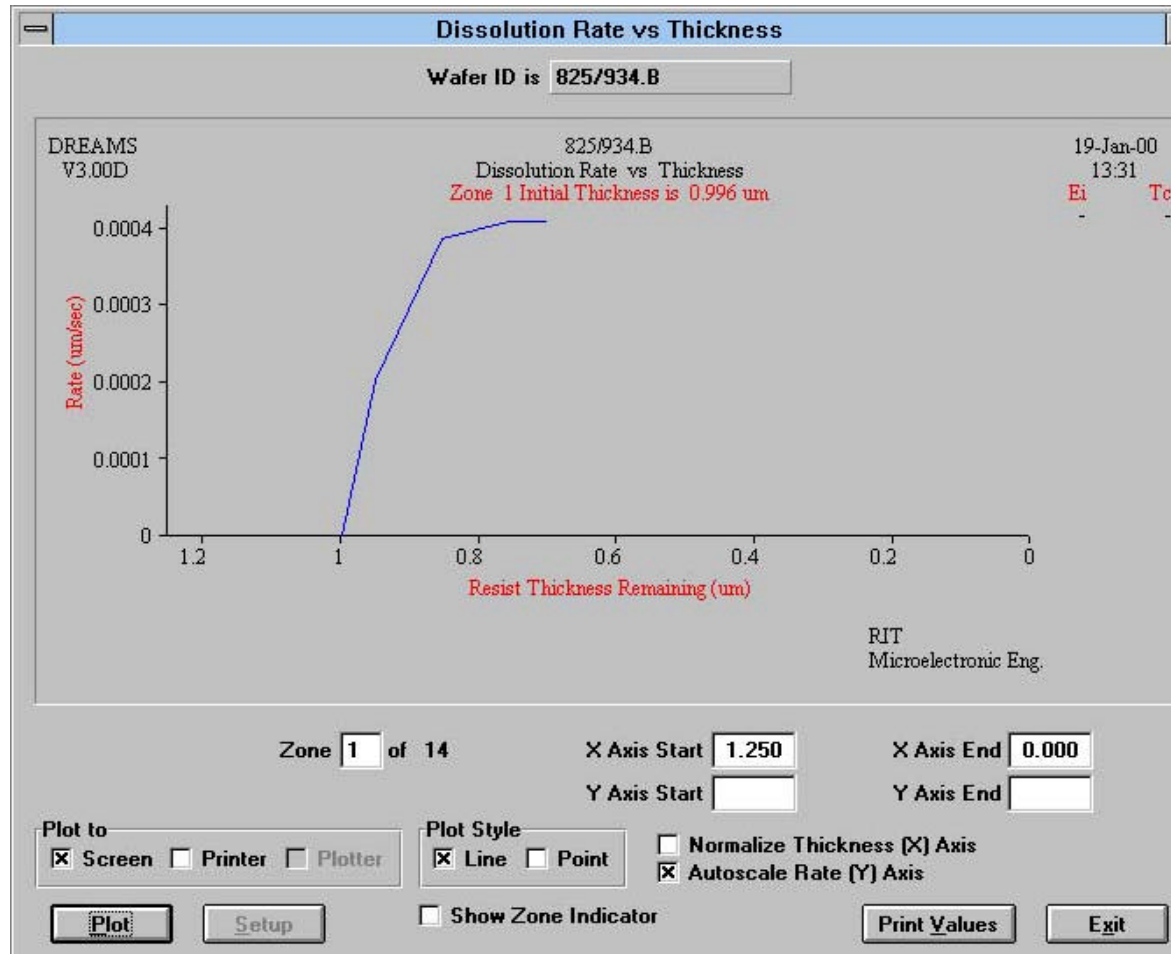
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THICKNESS VERSUS LOG DOSE



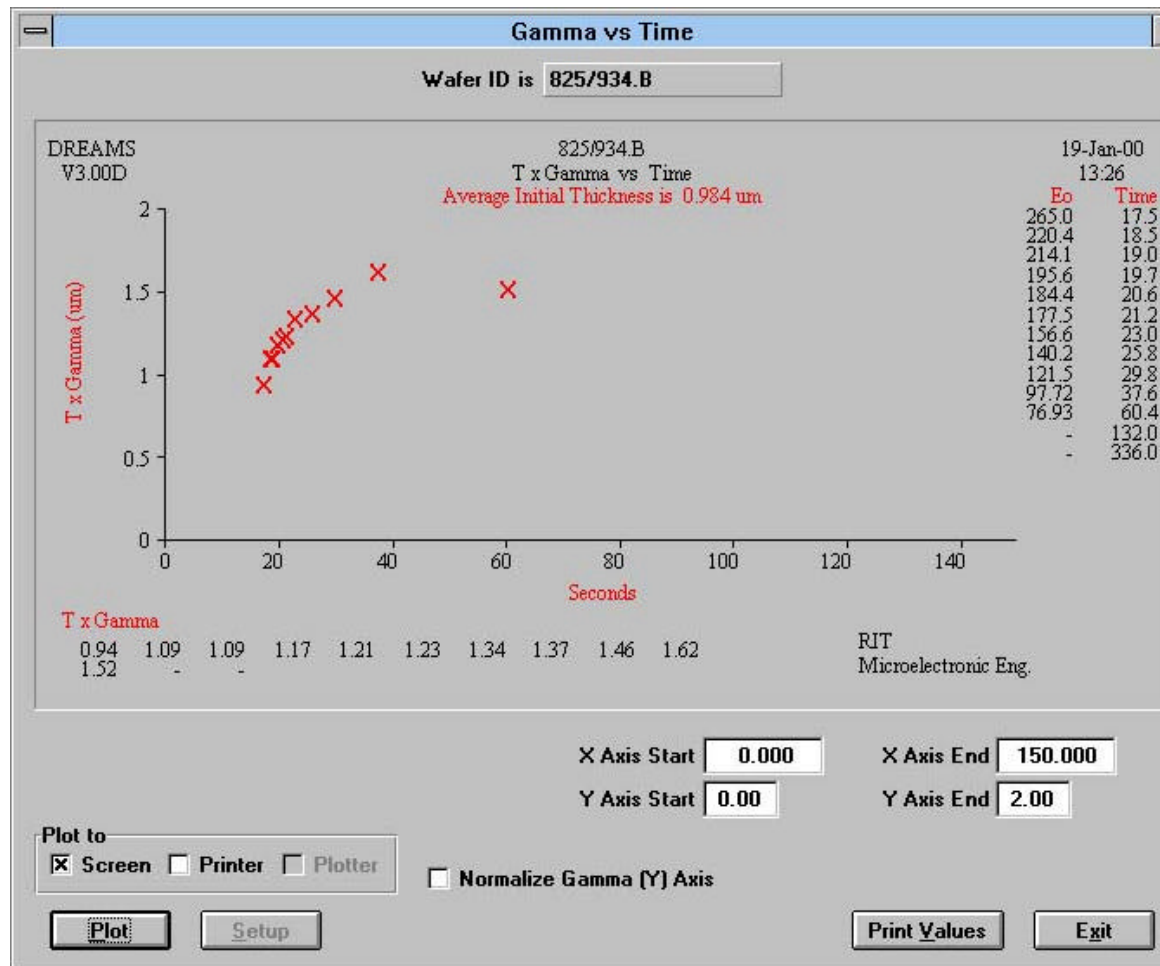
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DISSOLUTION RATE VERSUS THICKNESS



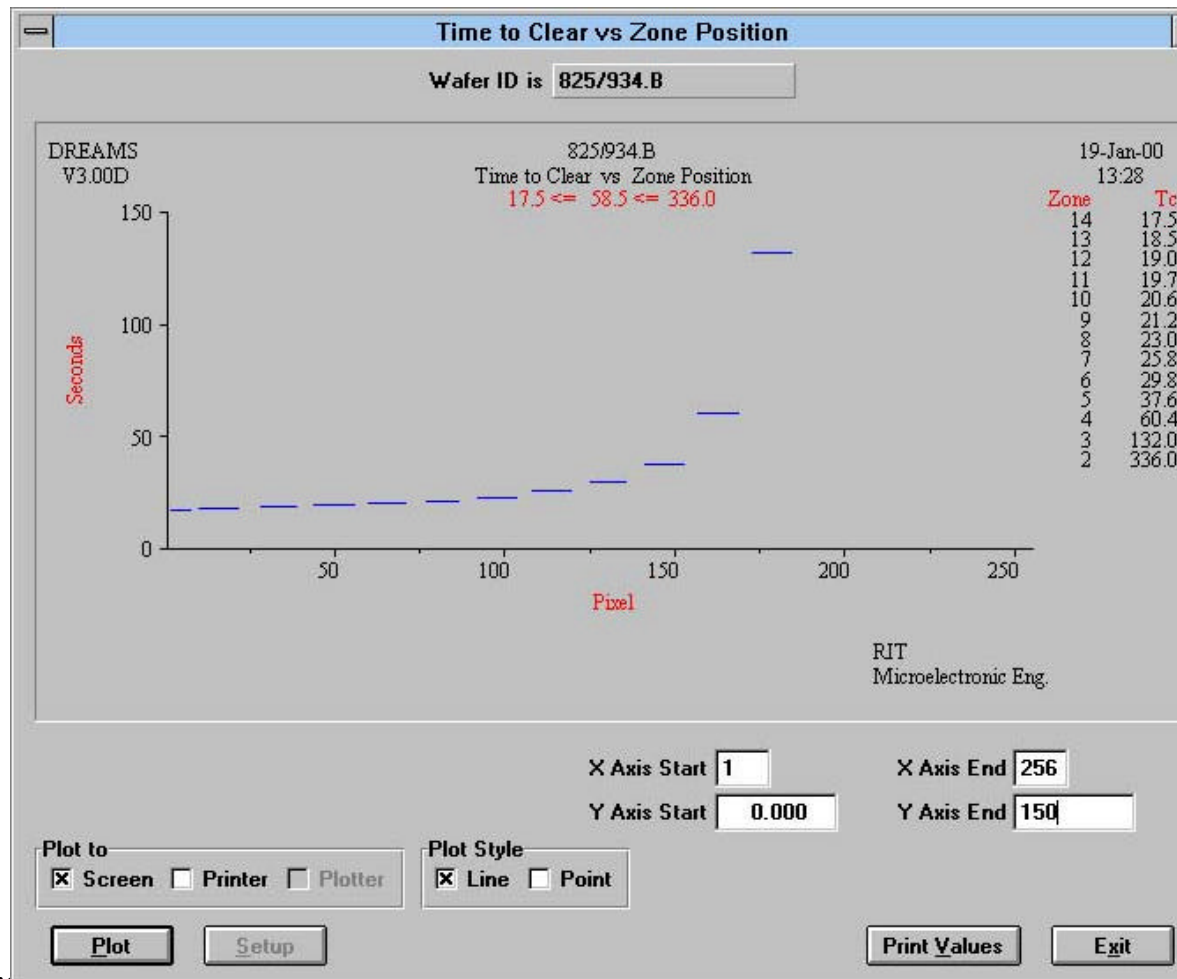
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THICKNESS TIMES GAMMA VERSUS TIME



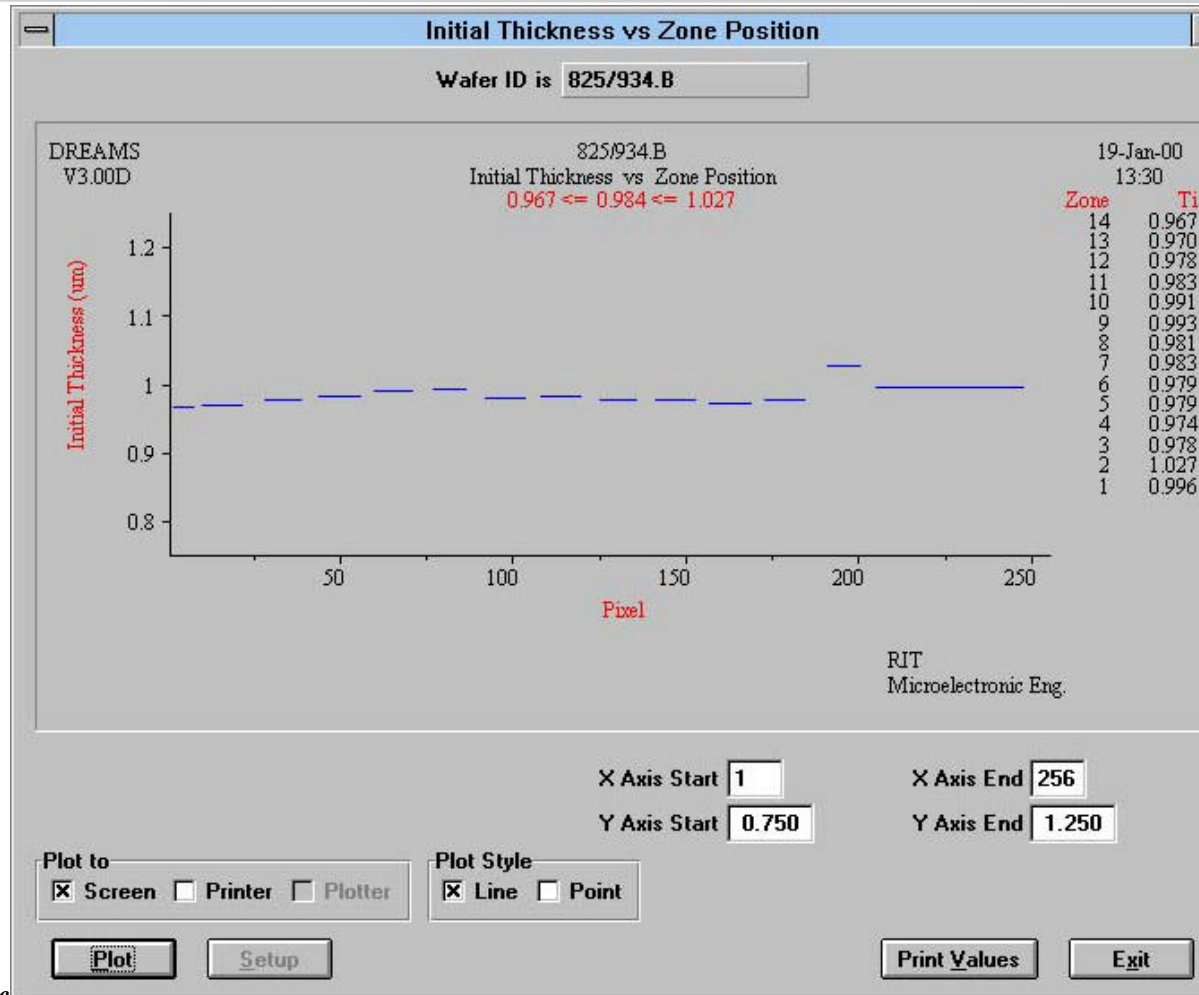
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TIME TO CLEAR VERSUS ZONE POSITION



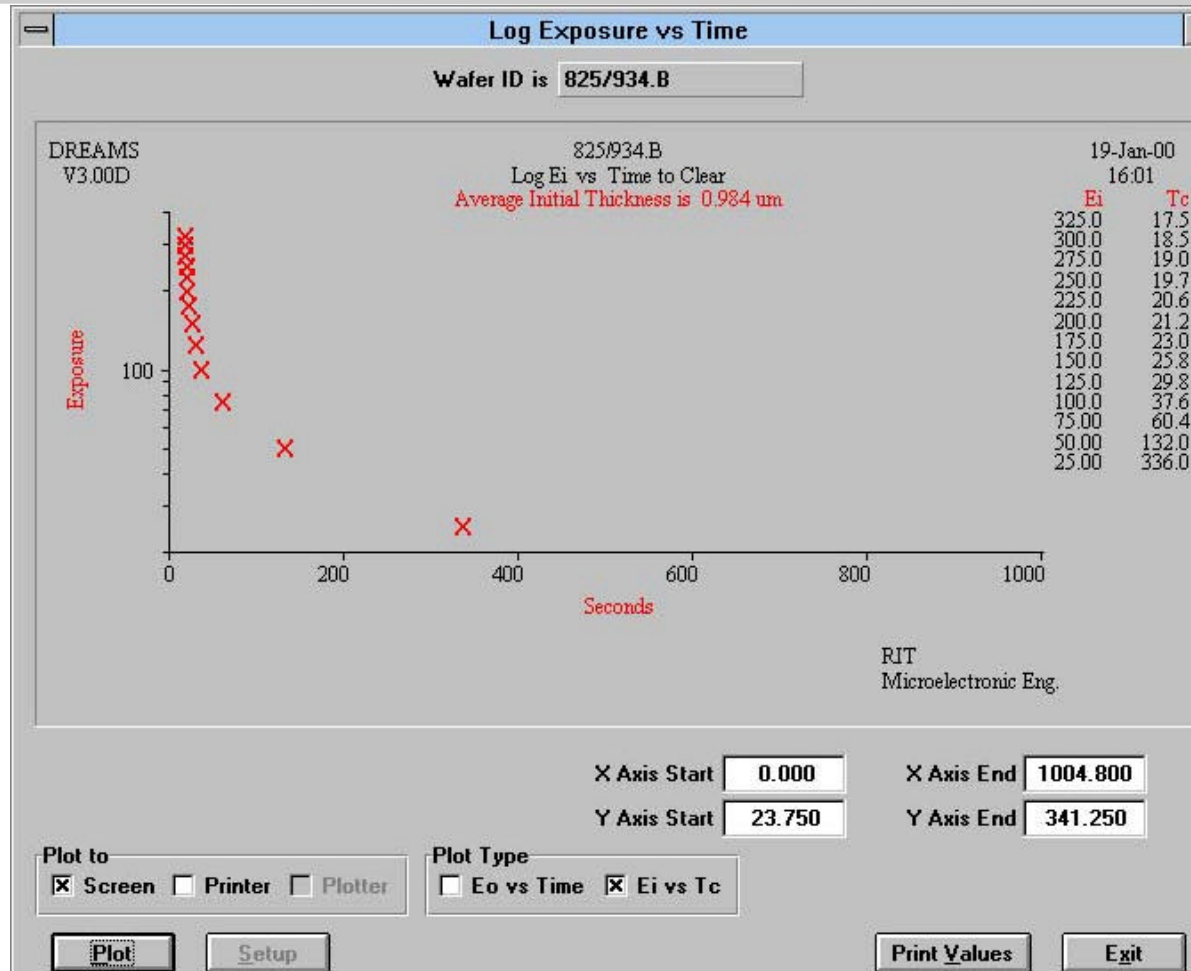
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INITIAL THICKNESS VERSUS ZONE POSITION



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LOG EXPOSURE VERSUS TIME



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REFERENCES

1. Introduction to Microlithography, Second Edition, Edited by Larry F. Thompson, C.Grant Willson and Murrae J. Bowden, ACS Professional Reference Book, American Chemical Society, Washington, DC 1994.
2. “Resist Modeling and Profile Simulation”, A.R.Neureuther, W.G.Oldham, Solid State Technology, May 1985.
3. “A General Simulator for VLSI Lithography and Etching Processes: Part I - Application to Projection Lithography”, W. G. Oldham, S.N. Nandgaokar, A.R.Neureuther, M.O’Toole, IEEE Transactions on Electron Devices, Vol. ED-26, No.4, April 1979.
4. “A General Simulator for VLSI Lithography and Etching Processes: Part II - Application to Deposition and Etching”, W. G. Oldham, A.R.Neureuther, C. Sung, John L. Reynolds, S.N. Nandgaonkar, IEEE Transactions on Electron Devices, Vol. ED-27, No.8, August 1980.
5. “Optical Lithography”, Frederick H. Dill, IEEE Transactions on Electron Devices, Vol. ED-32, No.7. July 1975.
6. Microlithography, Sheats and Smith

HOMWORK - DRM

- What would the signal versus time plot look like for a situation in which there is significant standing wave effect?
- What would the development rate plot look like for a situation in which there is significant standing wave effect?

