ROCHESTER INSTITUTE OF TECHNOLOGY MICROELECTRONIC ENGINEERING

# Modeling of Microlithographic Processes

# Dr. Lynn Fuller

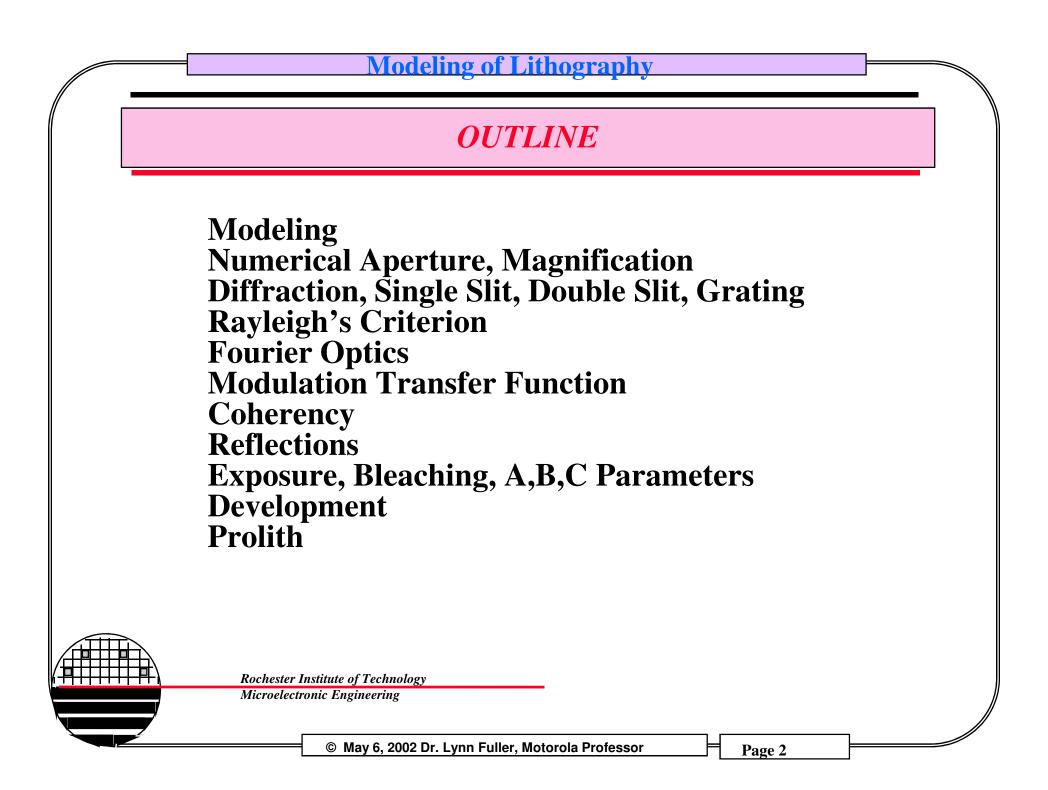
Motorola Professor Microelectronic Engineering Rochester Institute of Technology 82 Lomb Memorial Drive Rochester, NY 14623-5604 Tel (716) 475-2035 Fax (716) 475-5041 <u>LFFEEE@rit.edu</u> <u>http://www.microe.rit.edu</u>

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# **MODELING**

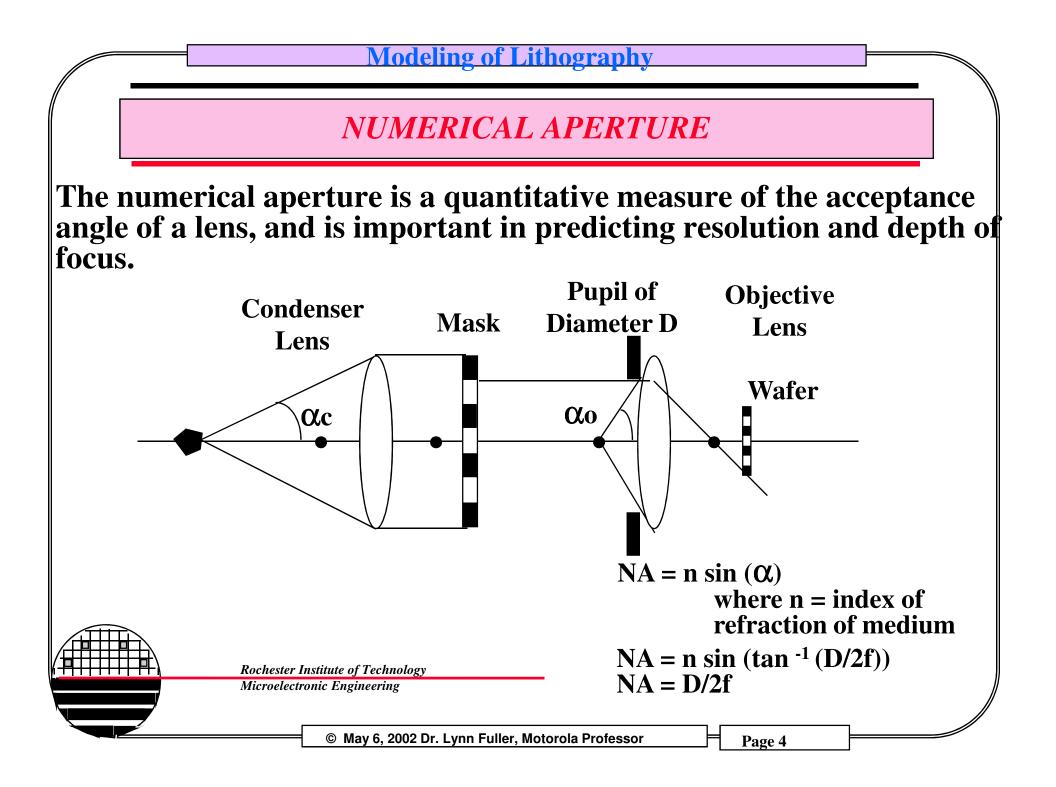
Modeling means to obtain mathematical expressions For the following:

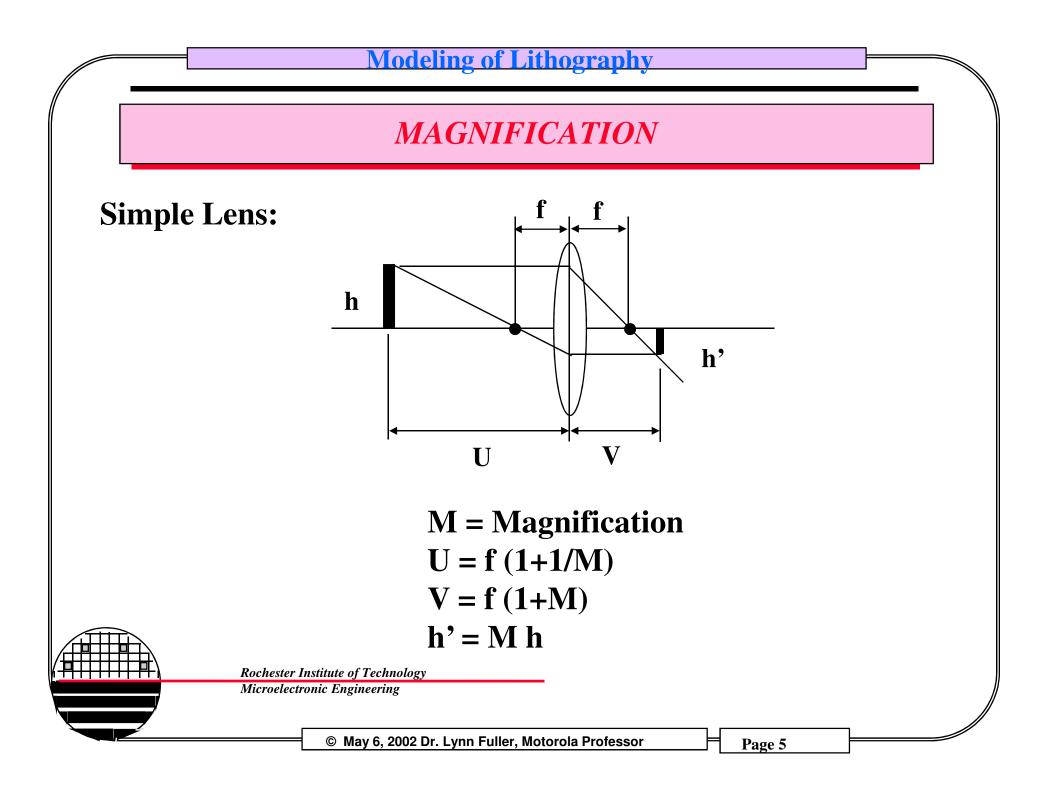
> AERIAL IMAGE - which depends on the parameters of the optical system, numerical aperture, coherency, wavelength REFLECTIONS - local irradiance depends on reflections from the multilayer substrate RESIST BLEACHING- resist bleaches during exposure which in turn changes parameters used

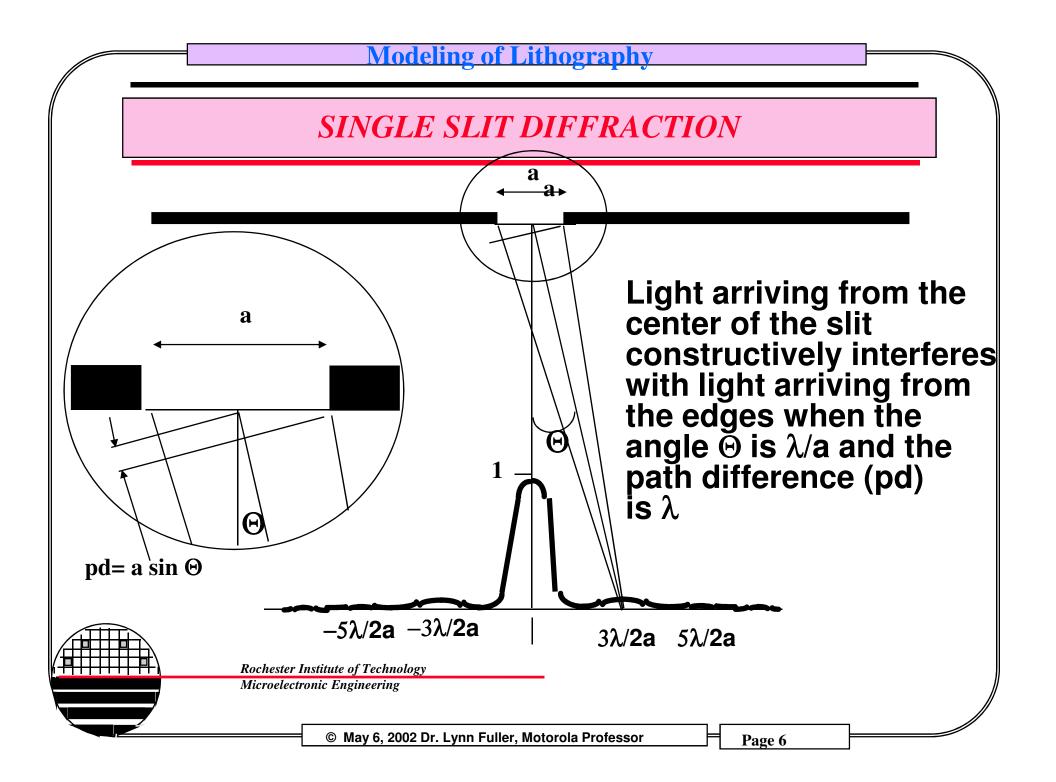
in the reflection of the degree.

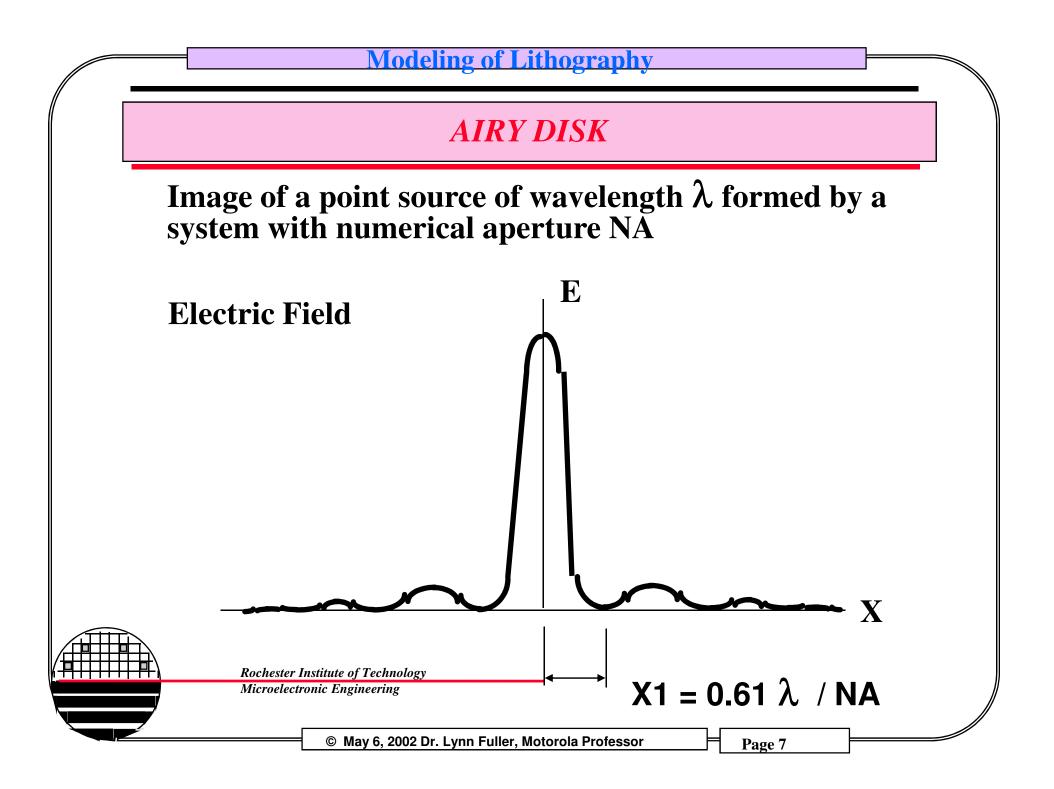
**RESIST DEVELOPMENT-** is a function of inhibitor concentration remaining after exposure.

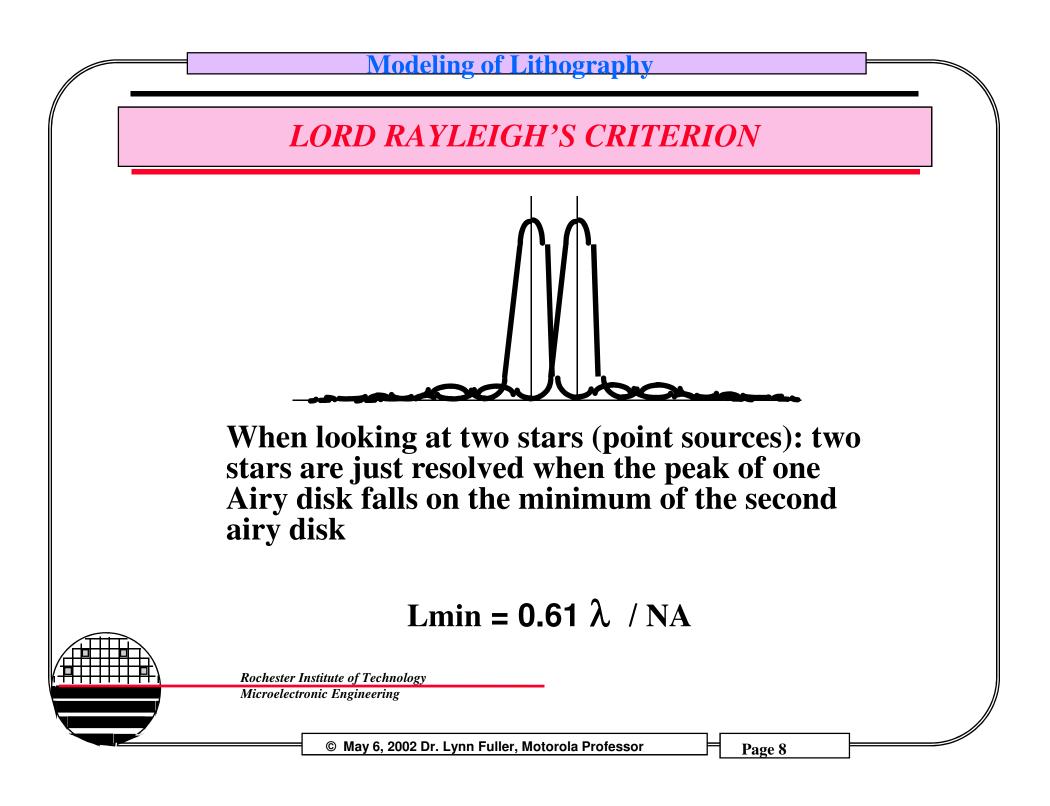
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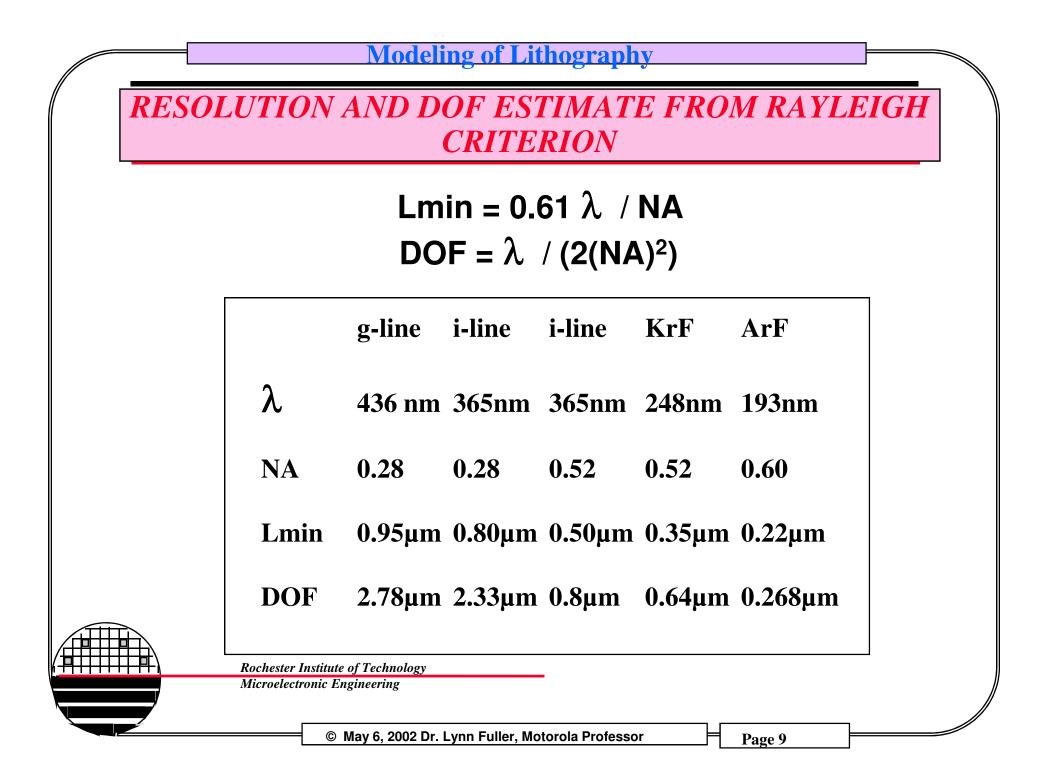


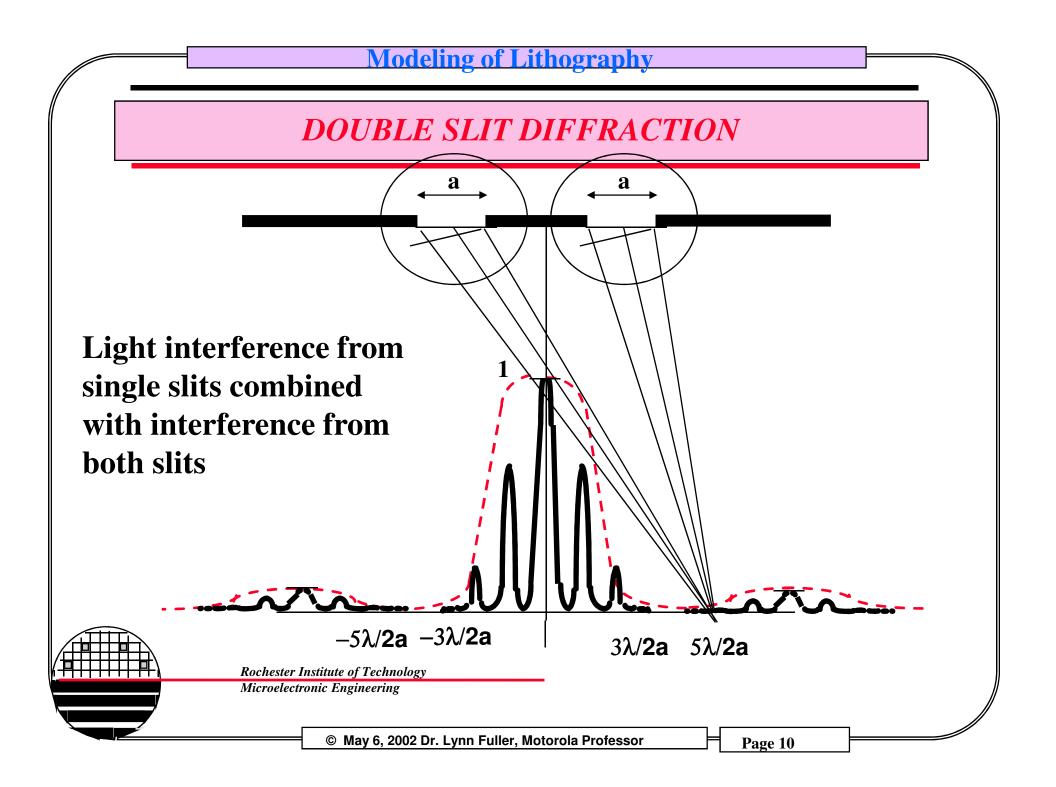


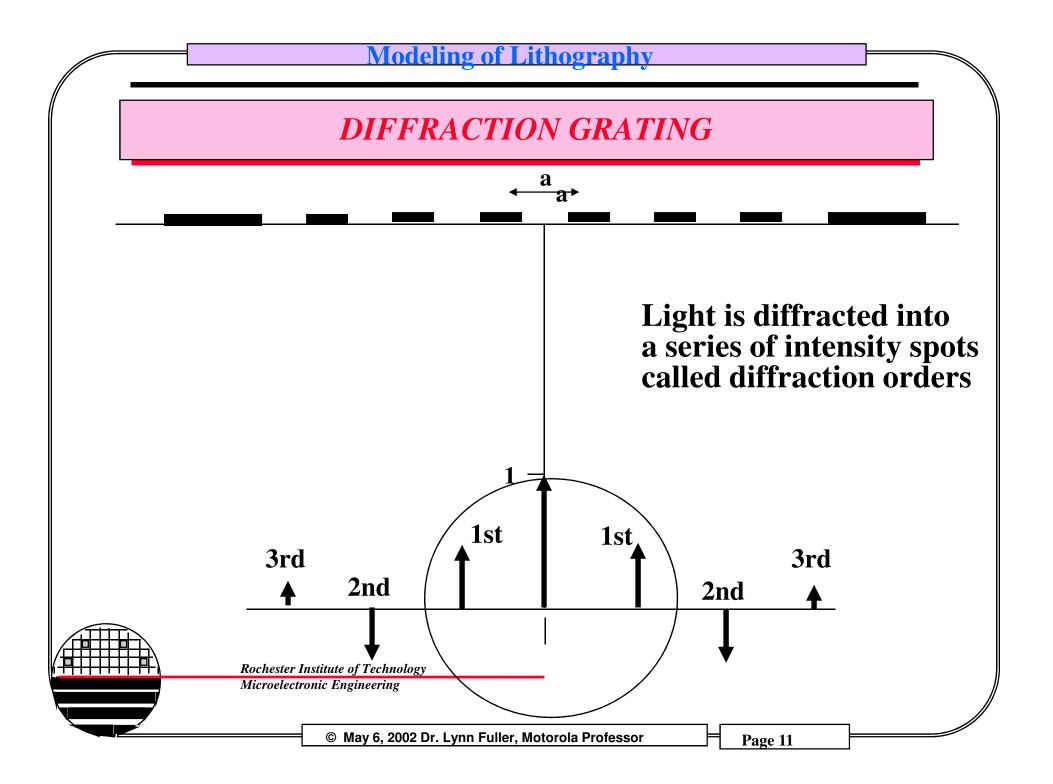


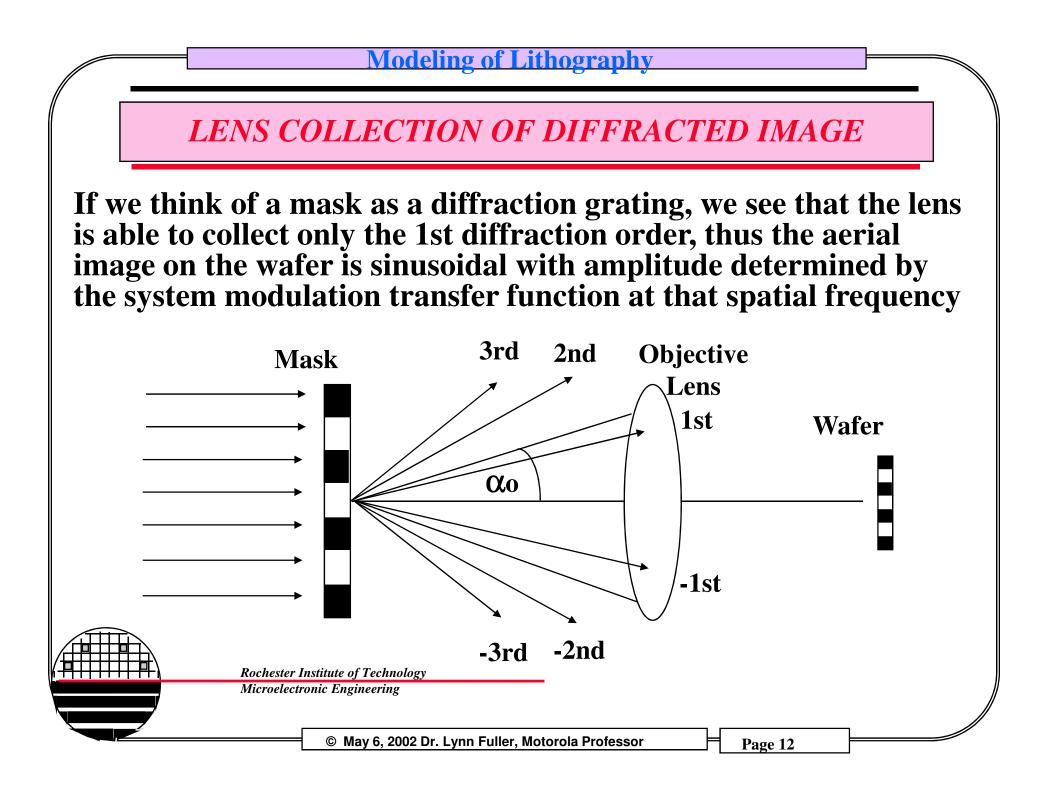


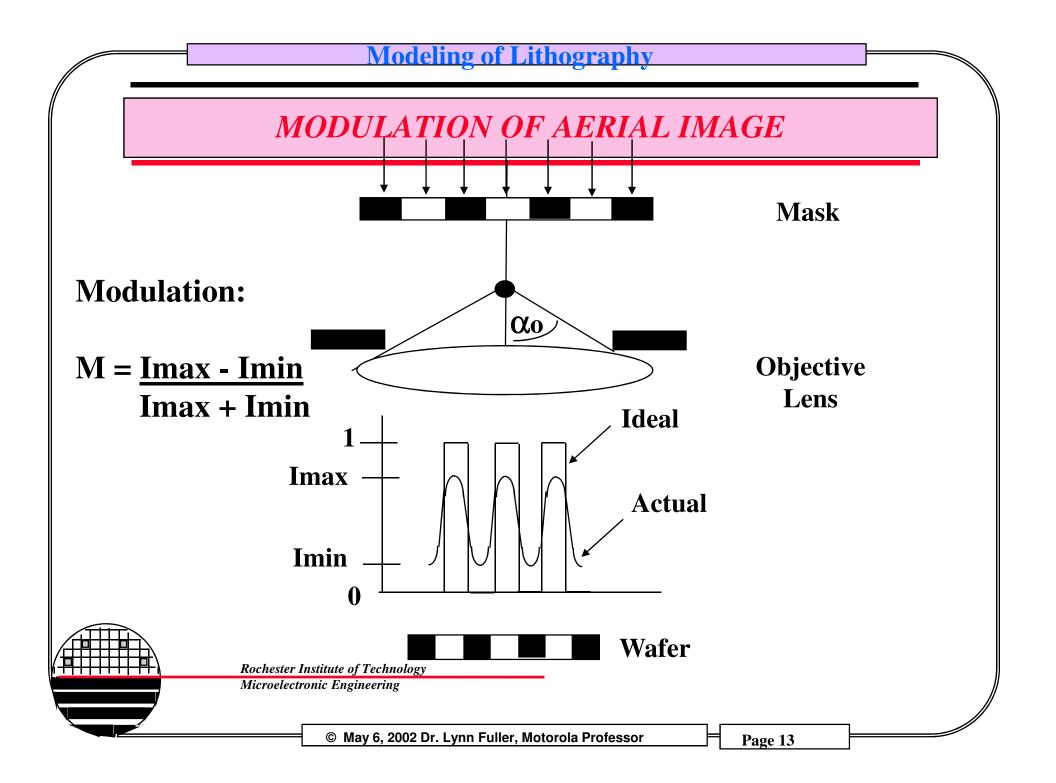


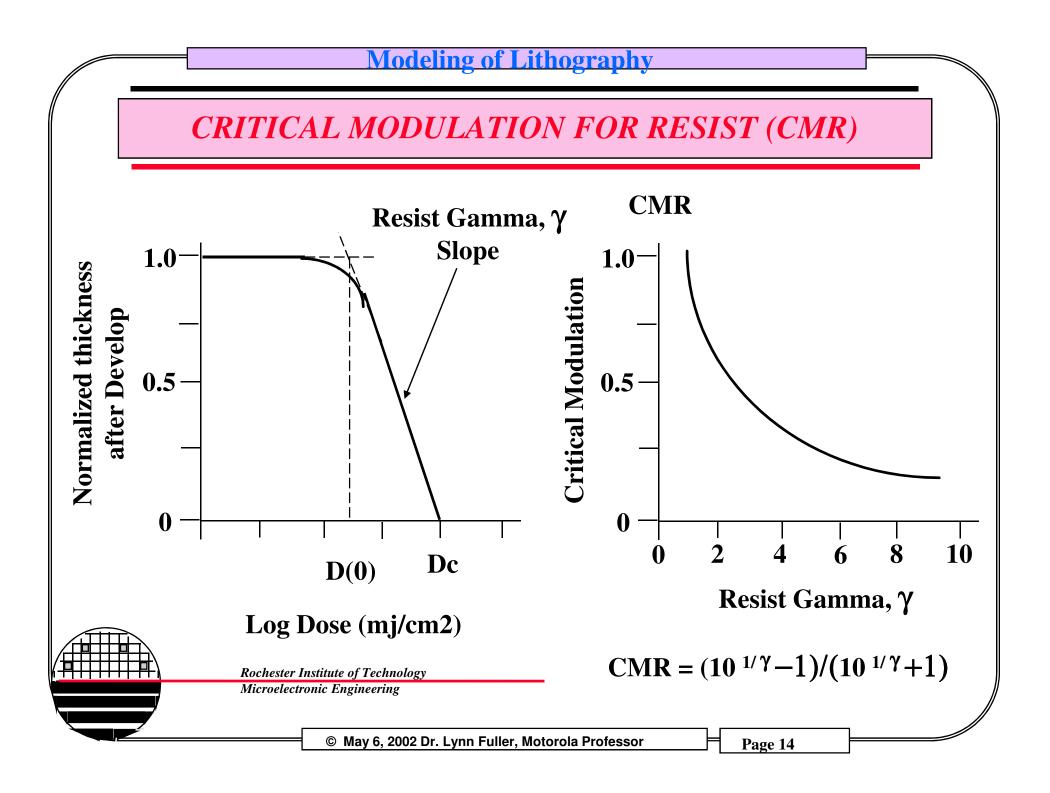


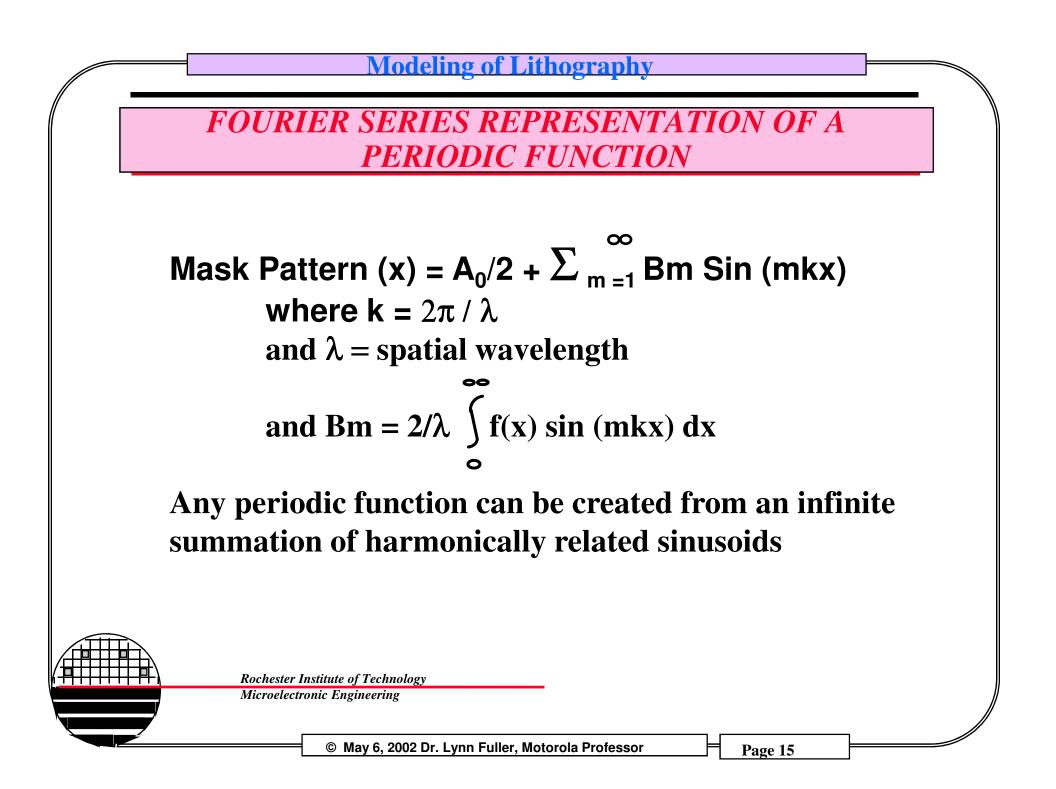


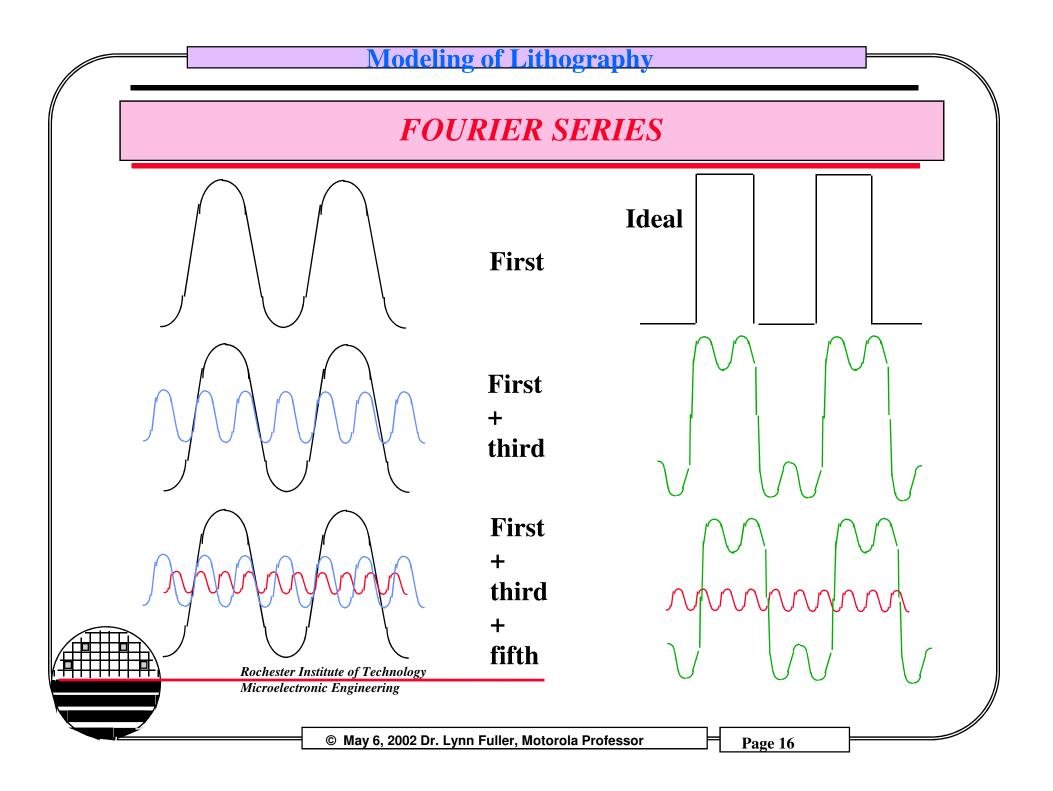


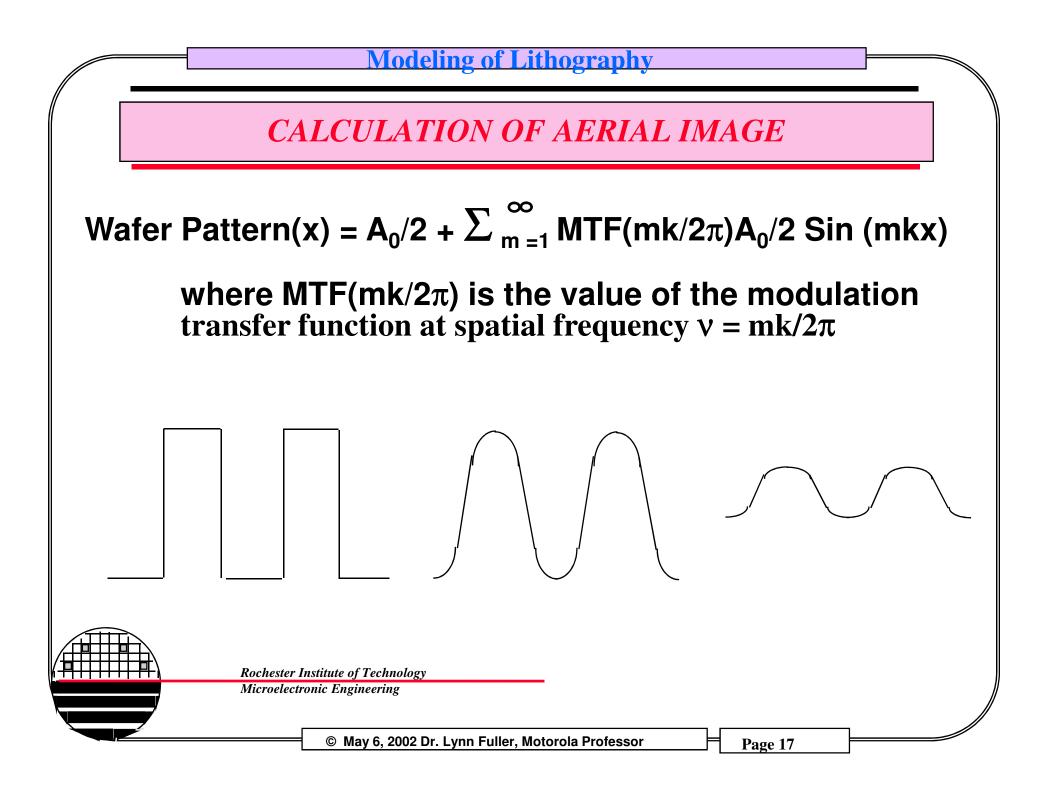


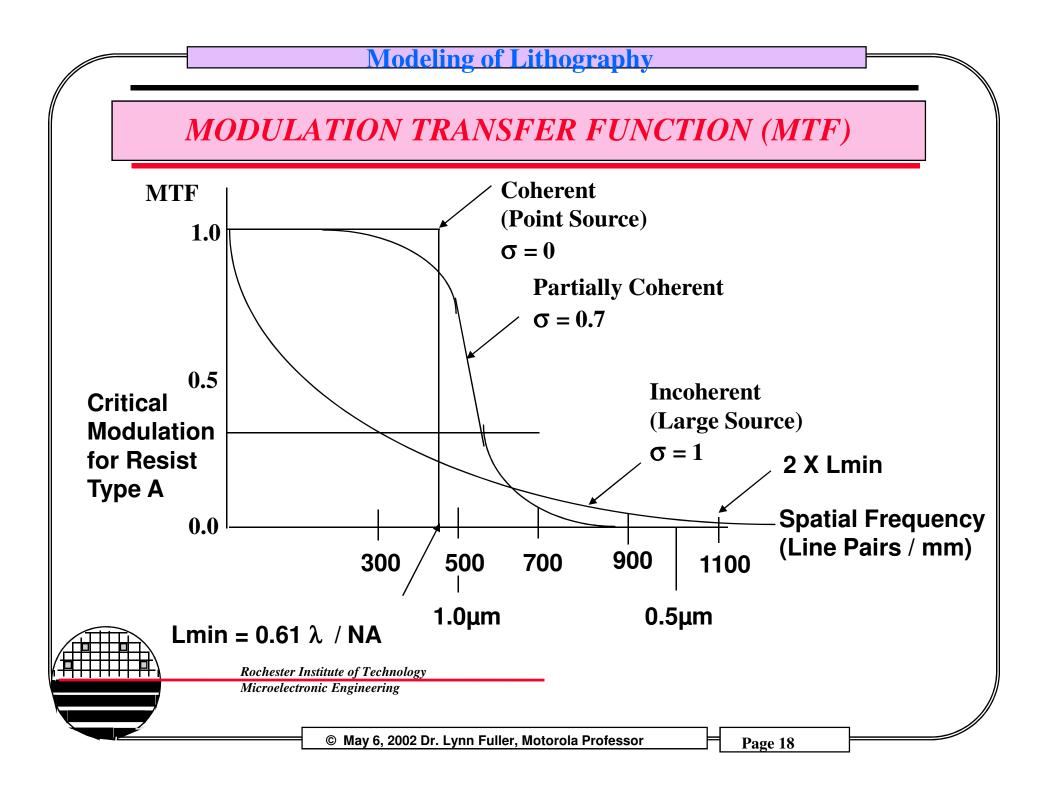


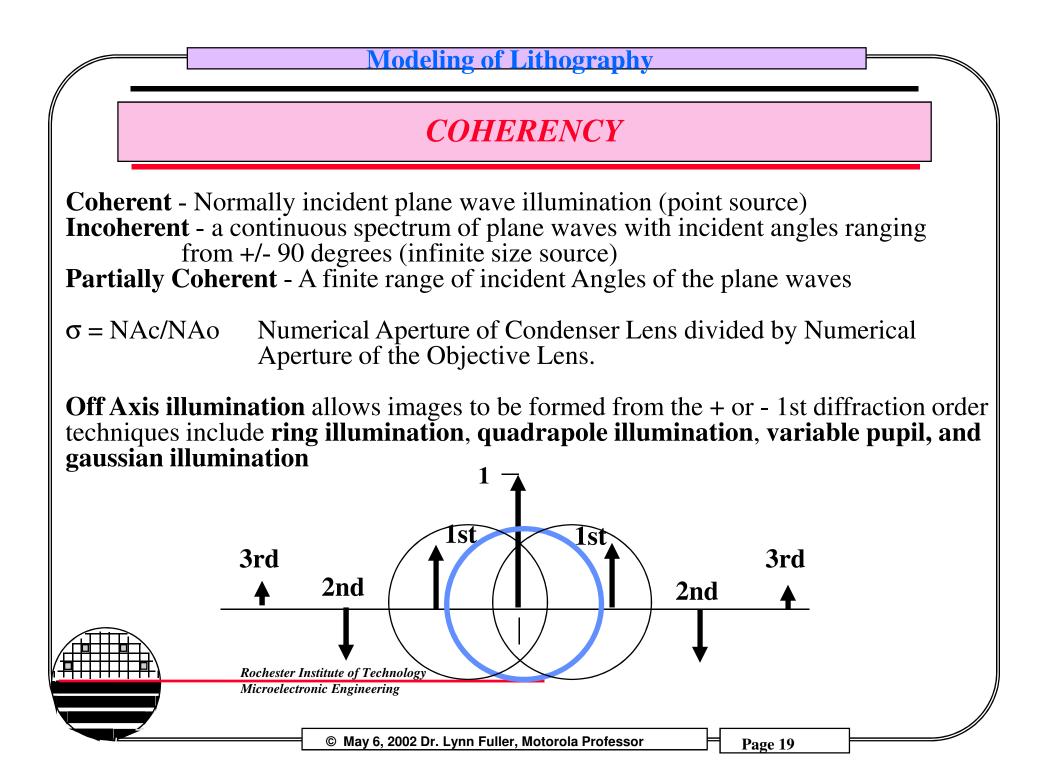


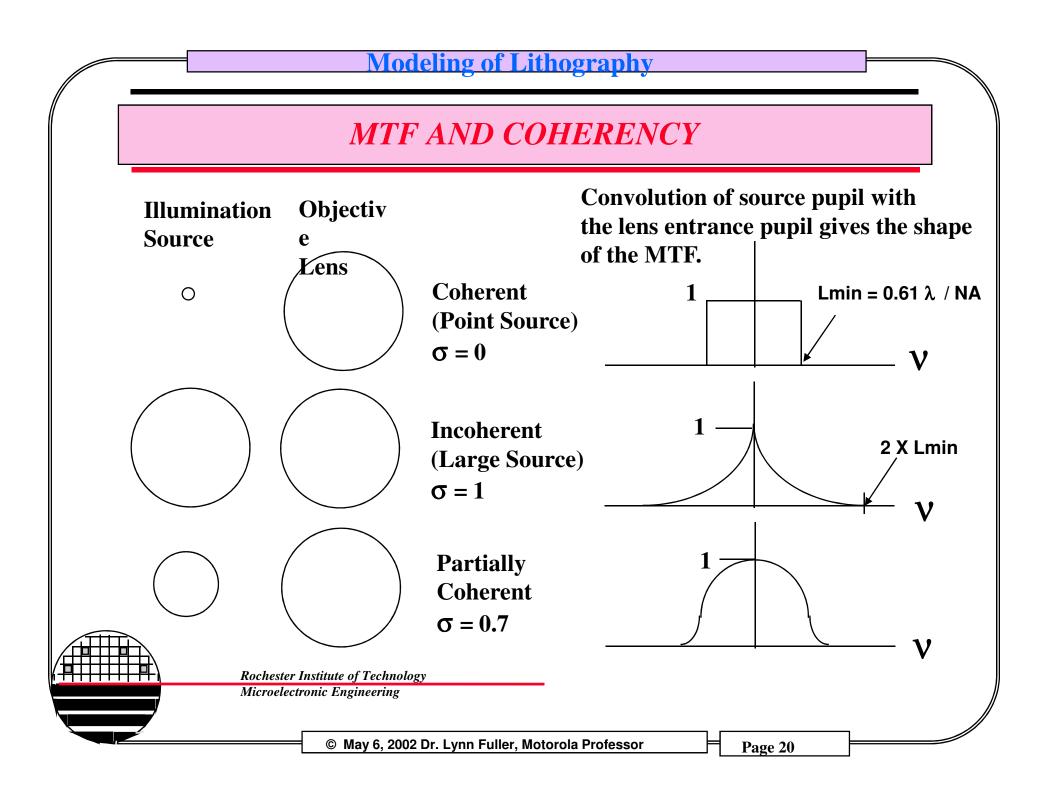


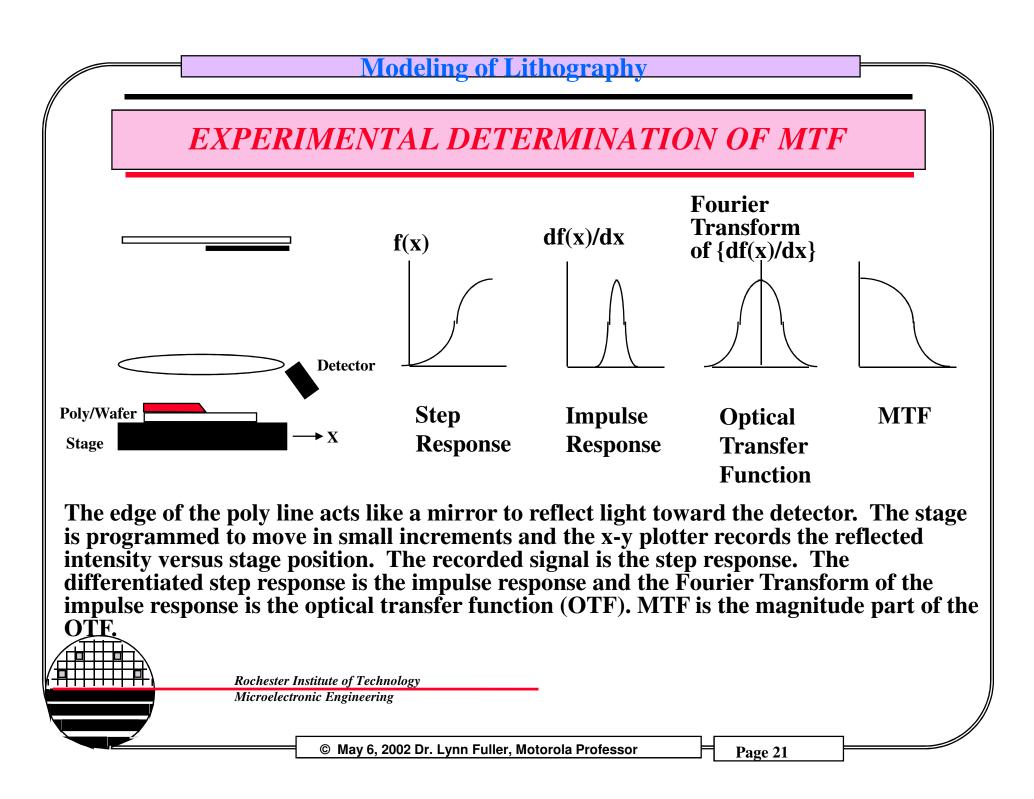






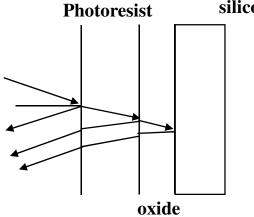






#### <u>Modeling of Lithography</u>

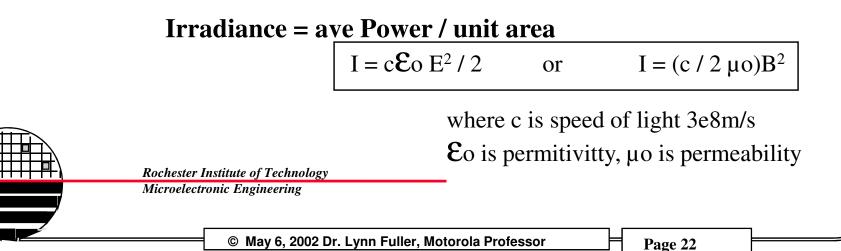
CULATION OF IRRADIANCE IN A SYSTEM WHERE THERE ARE MULTIPLE REFLECTING LAYERS

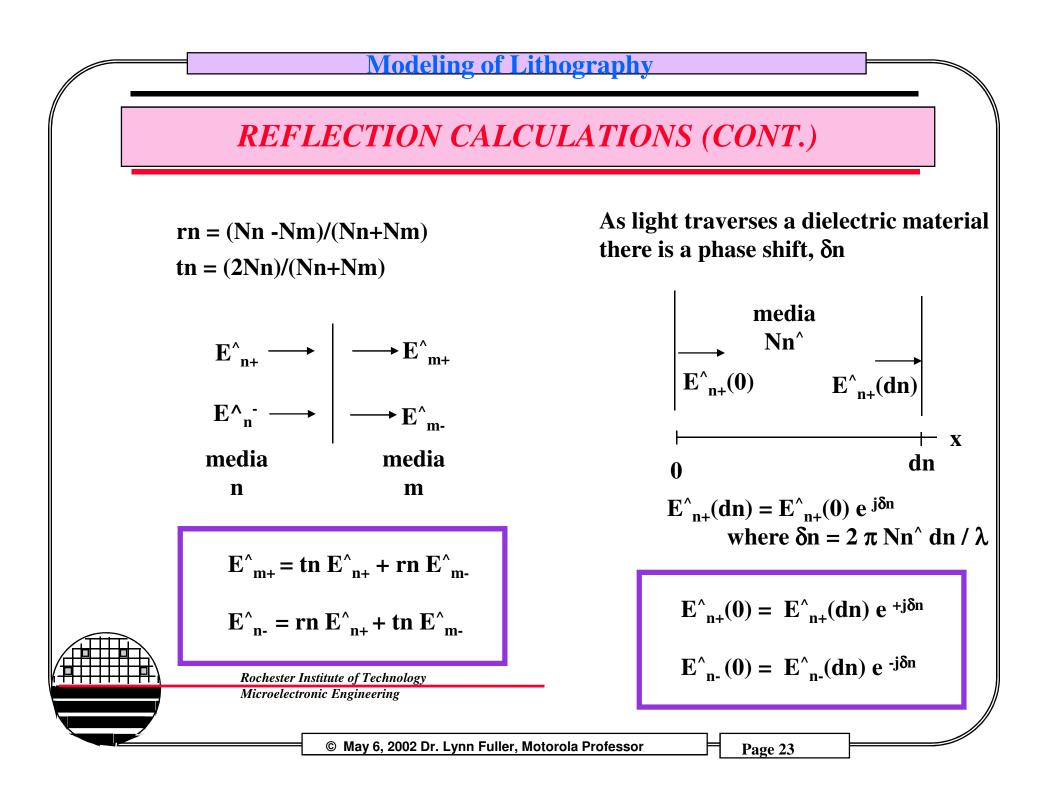


silicon

Light is an electromagnetic wave. The electric field is calculated from the irradiance value at the surface of the photoresist. Using the reflection and transmission coefficients for the boundary of two dielectrics a system of equations is built for a multi-layer substrate. The dielectric materials are described by their complex index of refraction.

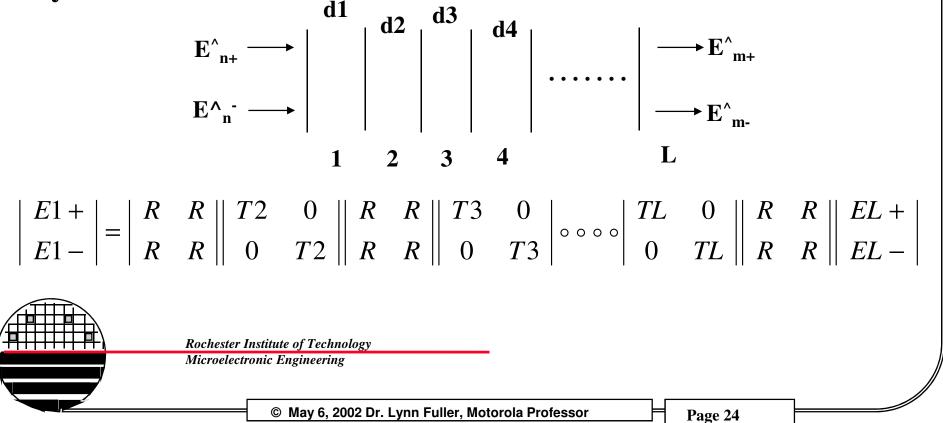
The relationship between Irradiance and electric or magnitc field is:

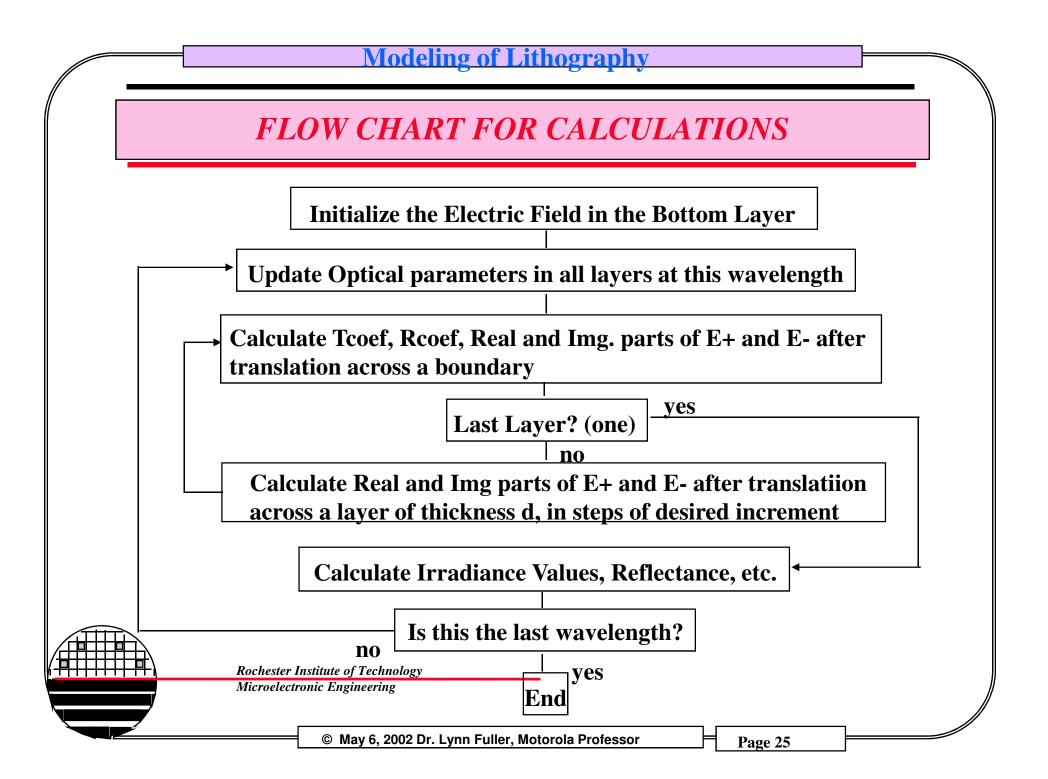


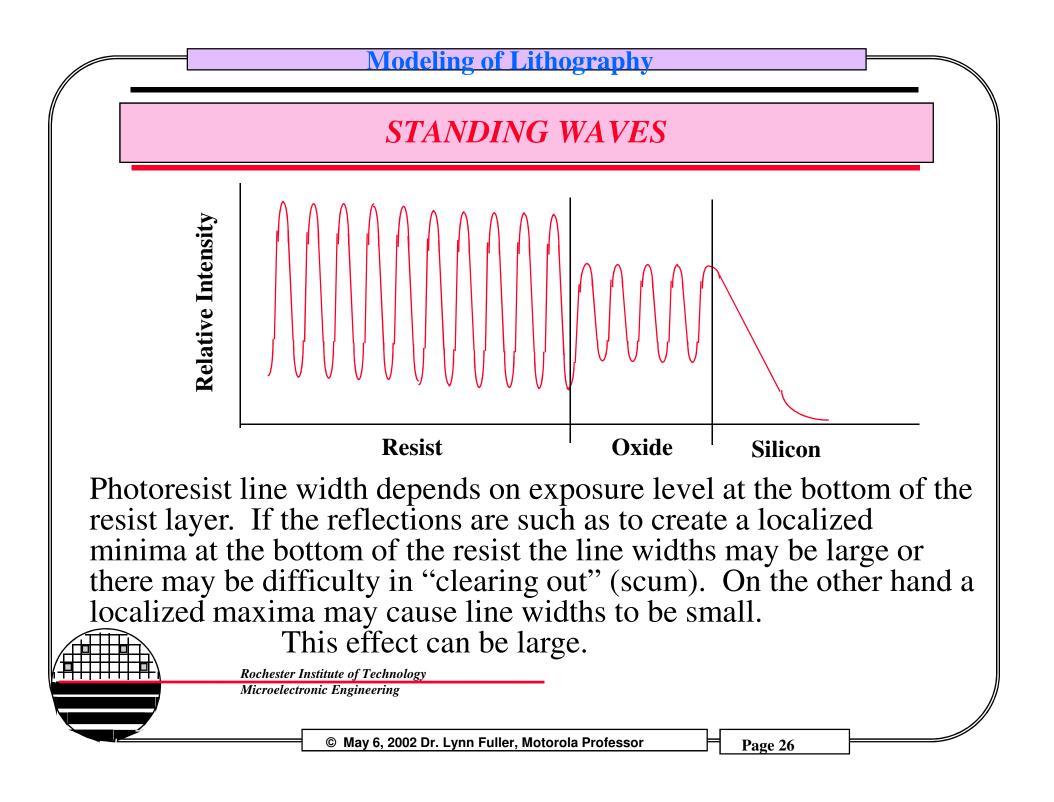


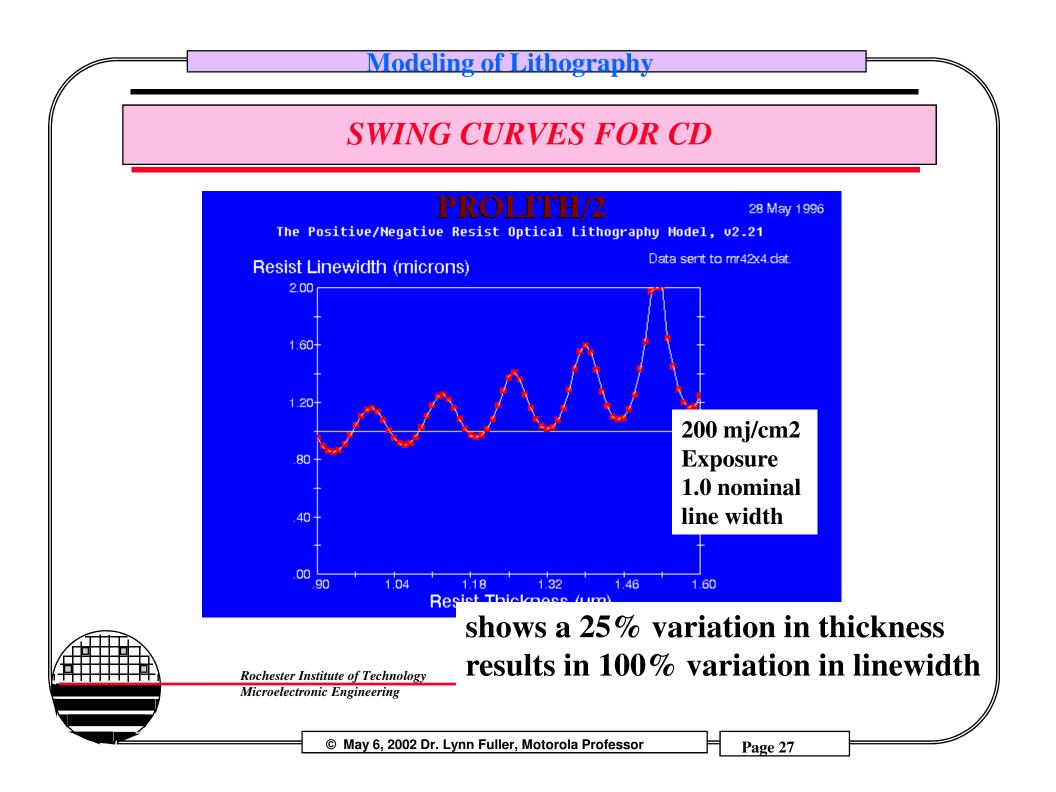
# **REFLECTION CALCULATIONS**

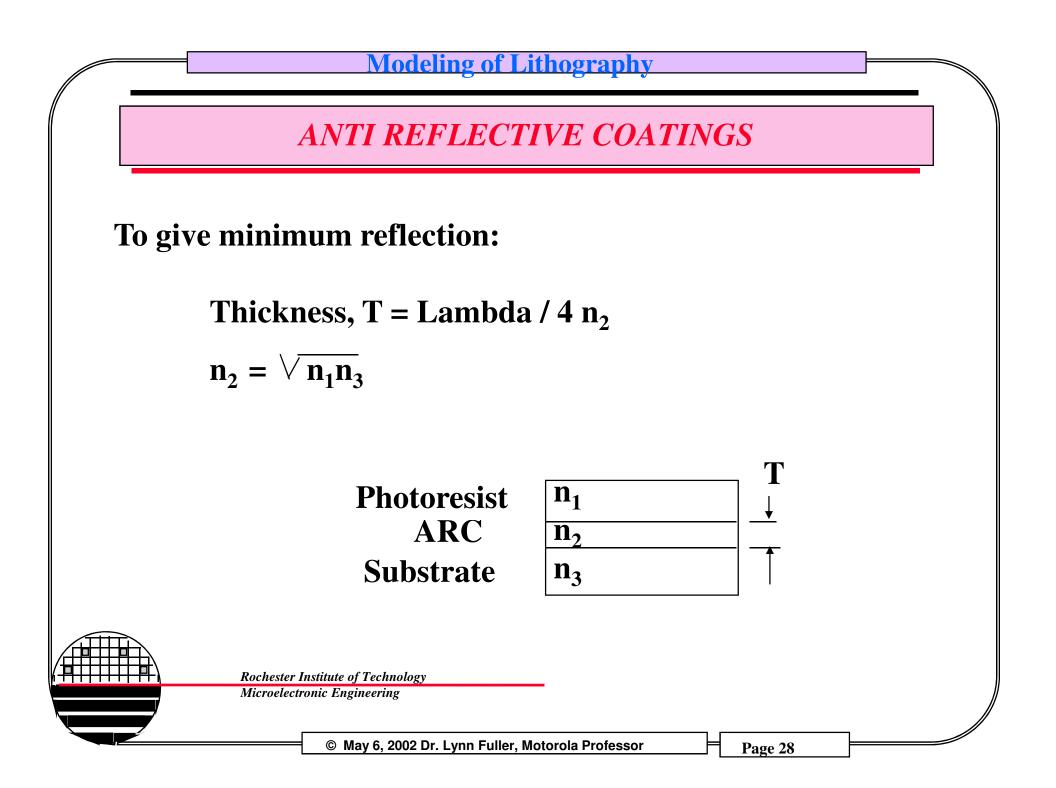
The two equations on the previous page are rearrange so input quantities are on the left and output quantities are on the right. The equations are converted to matrix format for simplicity. This allows for concise a representation of a system of any number of layers.

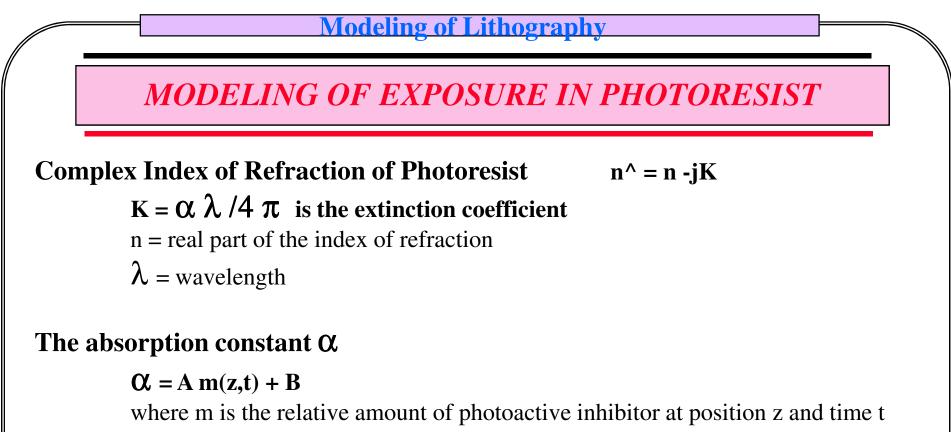








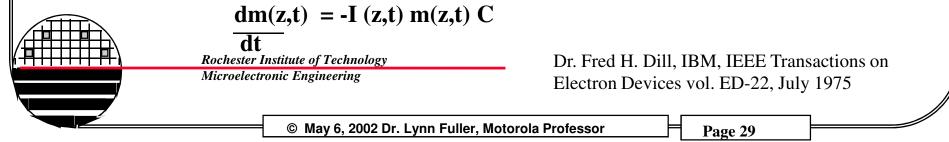




A is the exposure dependent parameter

B is the exposure independent parameter

An optical sensitivity parameter C relates the destruction of inhibitor local irradiance, I



# A,B,C PARAMETERS FOR EXPOSURE

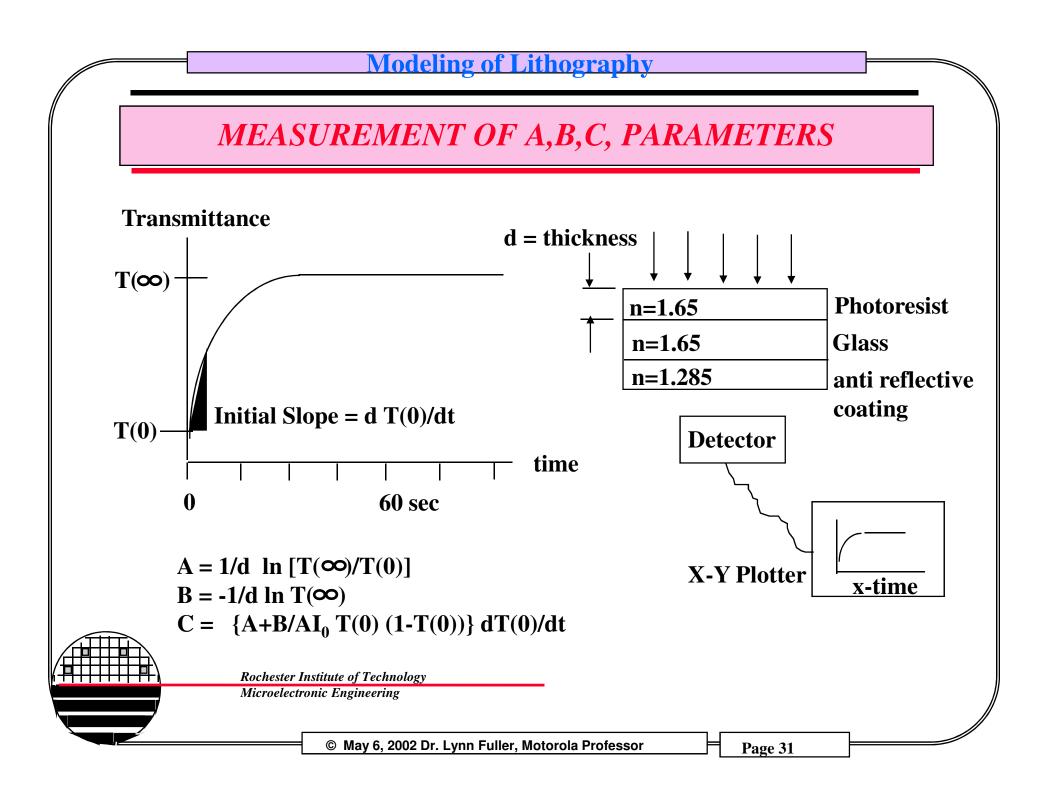
## A,B,C Exposure Parameters for AZ 1350J

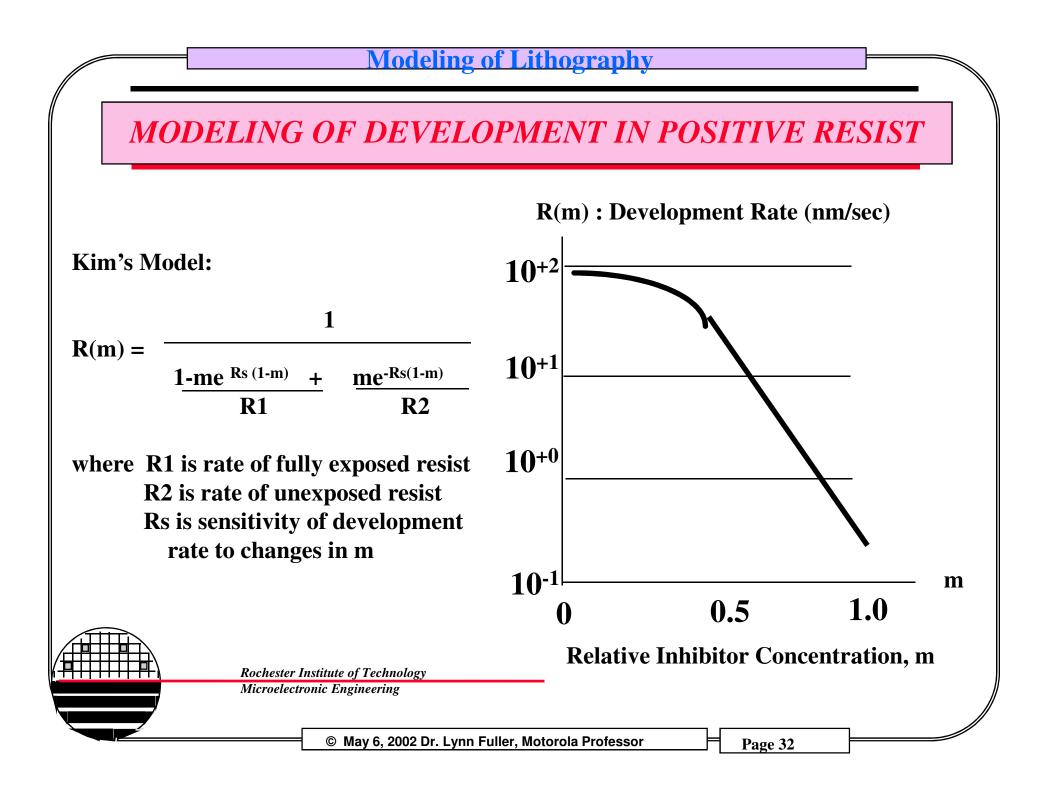
λ	A B	C C	n
	(μm <sup>-1</sup> ) (μm	n <sup>-1</sup> ) (cm <sup>2</sup> /mj	j)
436nm	0.54 0.	06 0.014	1.68
405nm	0.86 0.	07 0.018	1.70
365nm	<b>0.74 0.</b> <sup>2</sup>	20 0.012	1.72

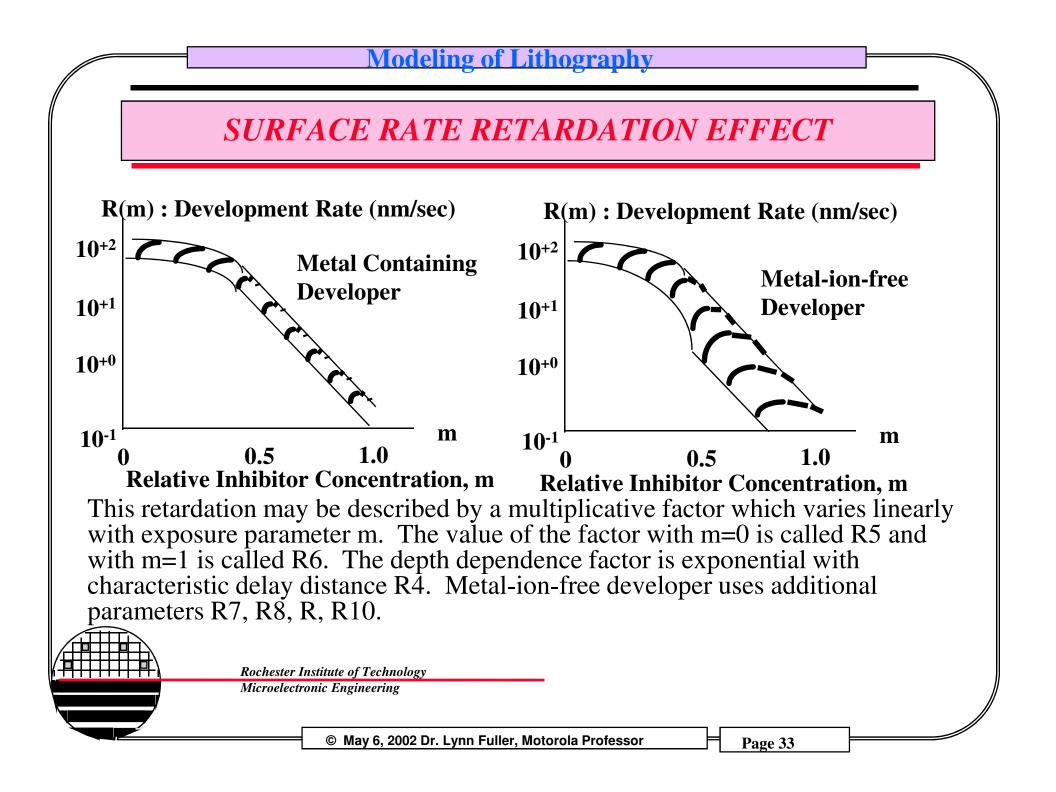
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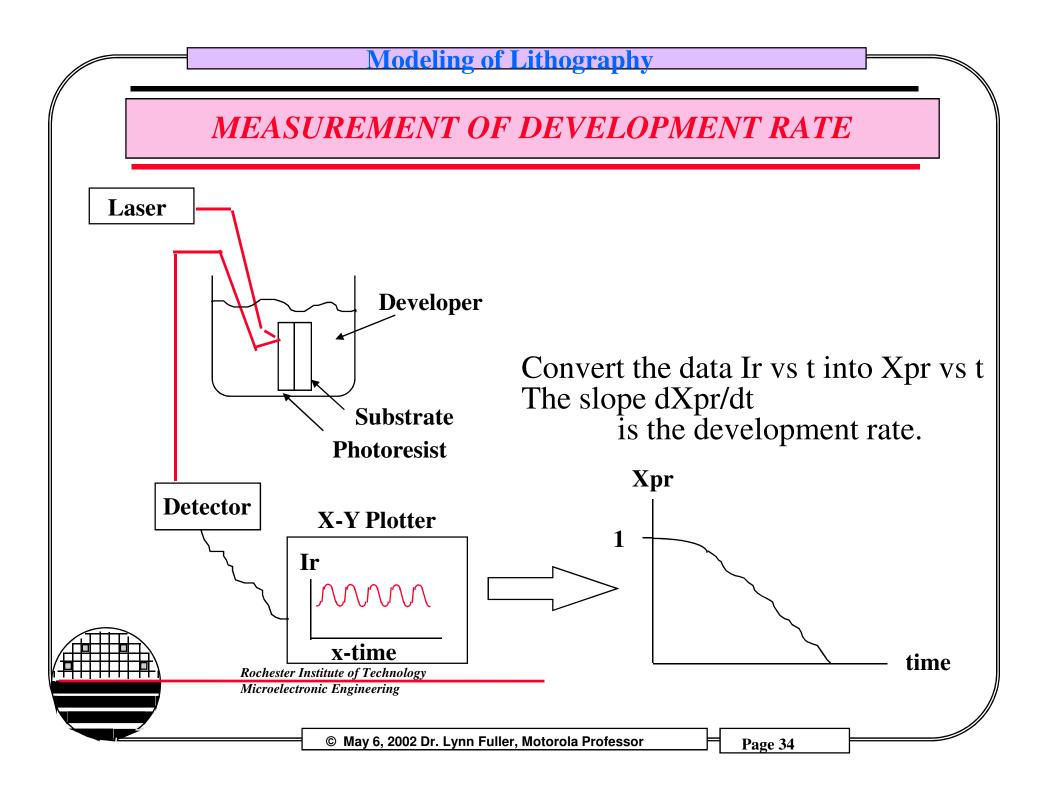
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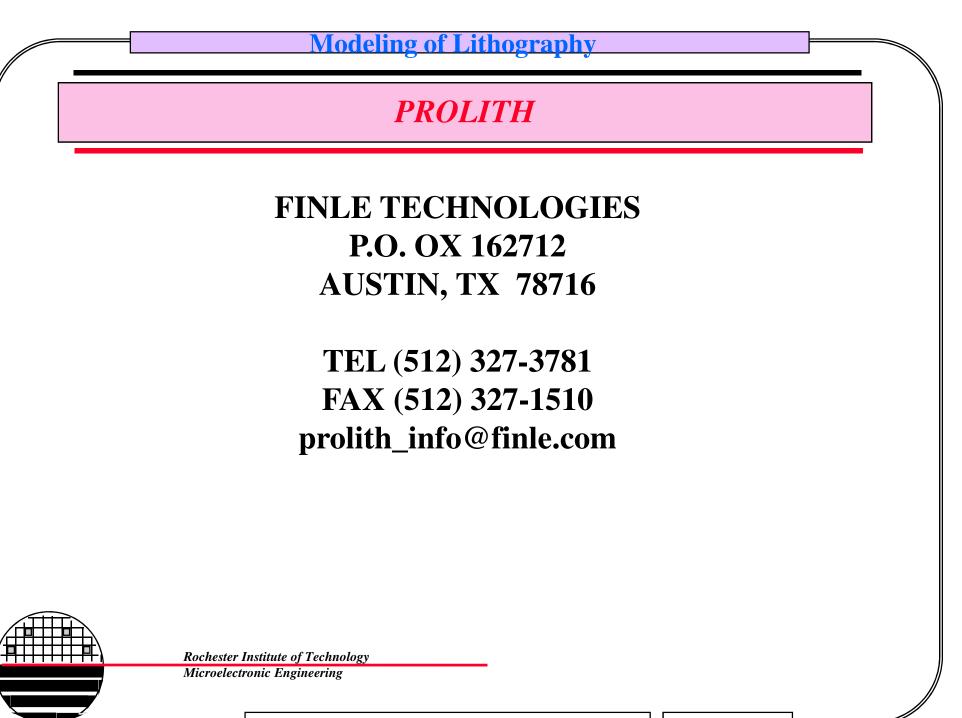
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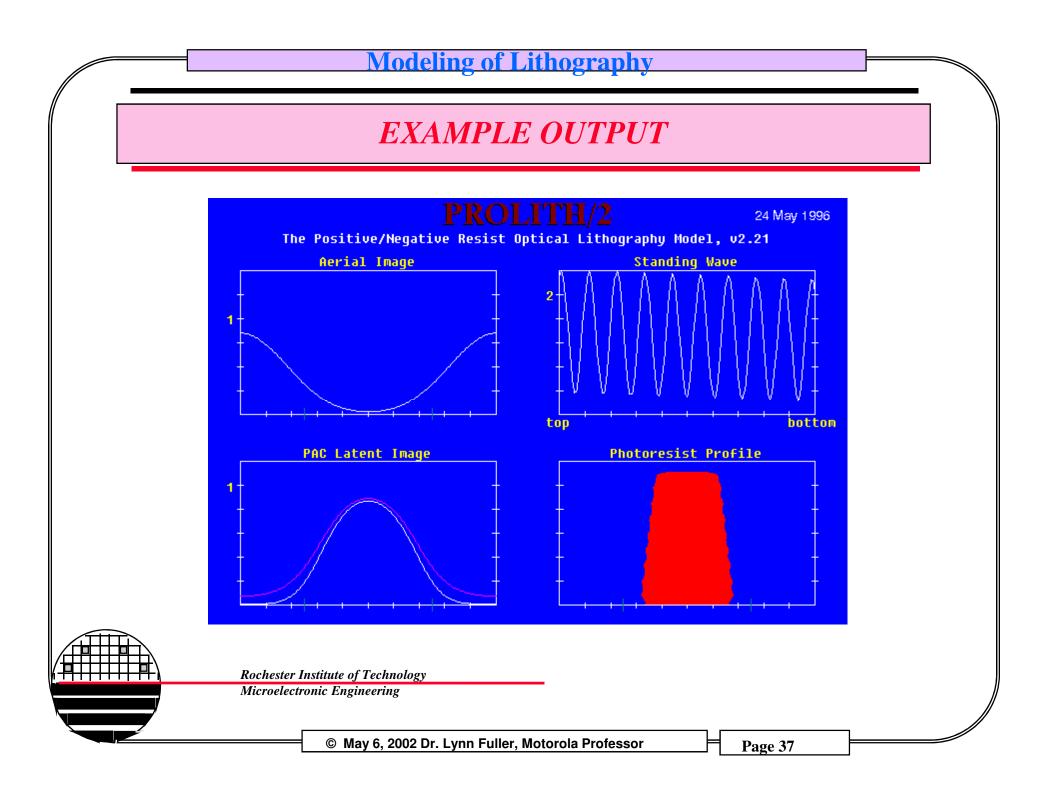
### **PROLITH INPUT PARAMETERS**

**Projection System:** Wavelength = 436 nmBandwidth = 0.0 nm Numerical Aperture = 0.29 Partial Coherence = 0.70Line width =  $1.00 \mu m$ Pitch =  $2.00 \,\mu m$ Mask Bias = 0.0Focal Position = 0.0**Image Flare = 0.0 Prebake Conditions: Hotplate** Bake Time = 1.0 min Temperature = 90 C Exposure Energy = 300 mJ/cm2

PEB Bake Time = 0.3 min PEB Temperature = 120 C Development Time = 120 sec Resist System: System 8 Thickness = 1.2  $\mu$ m Layer #1: Si Dioxide Thickness = 500nm Layer #2: Polysilicon Thickness = 600 nm Layer #3: Si Dioxide Thickness = 50 nm Substrate: Silicon

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**PRINTED OUTPUT VALUES** 

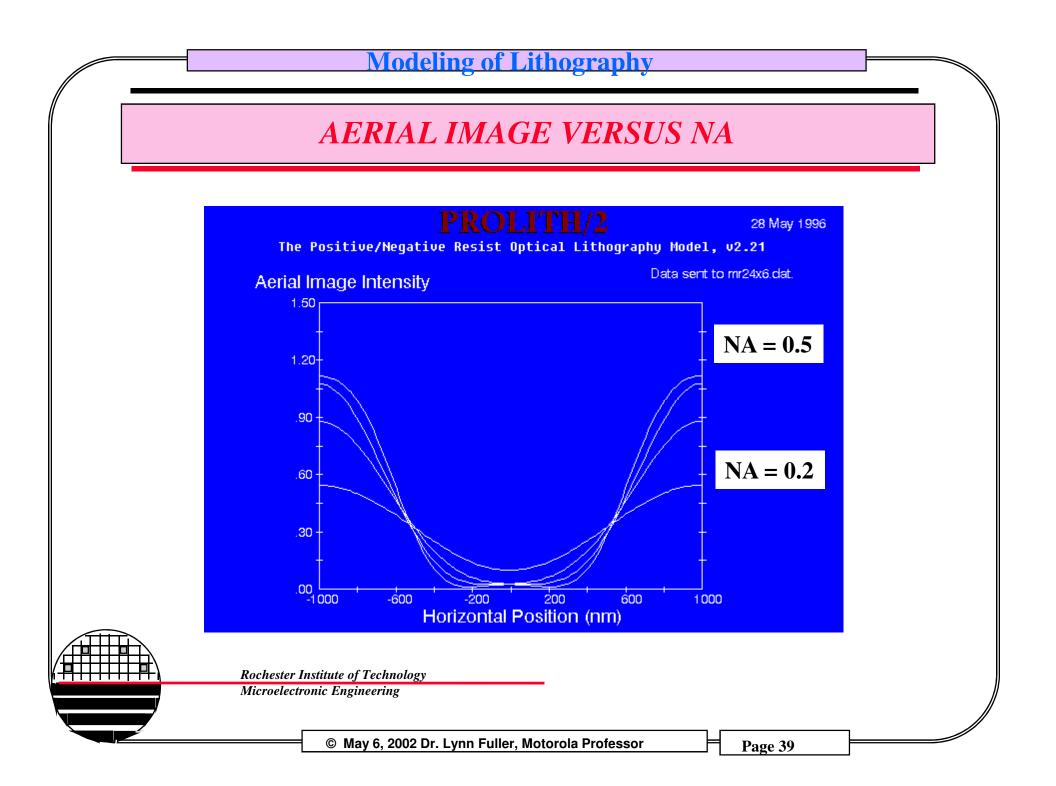
### **PROLITH/2** Single Run Results

Image Log-slope = 4.00951/micronsPAC Gradient = 1.61751/micronsResist Line width = 1.7129micronsSide wall Angle = 83.69degreesTime To Clear = 33.09secondsLine Resist Loss = 84.23nm

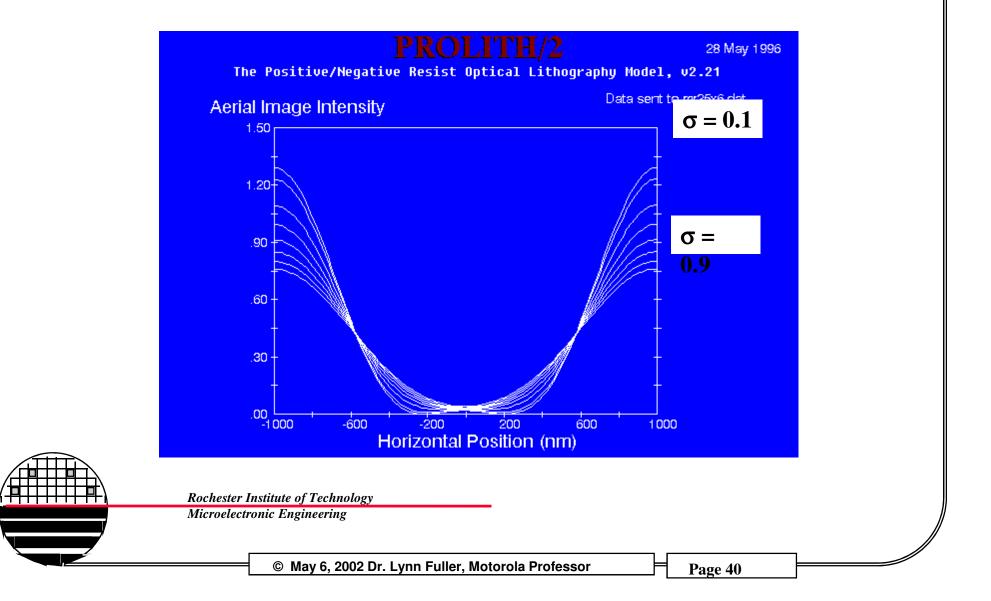
# PROLITH/2 Run Time = 0.18 minutes with a speed factor of 10

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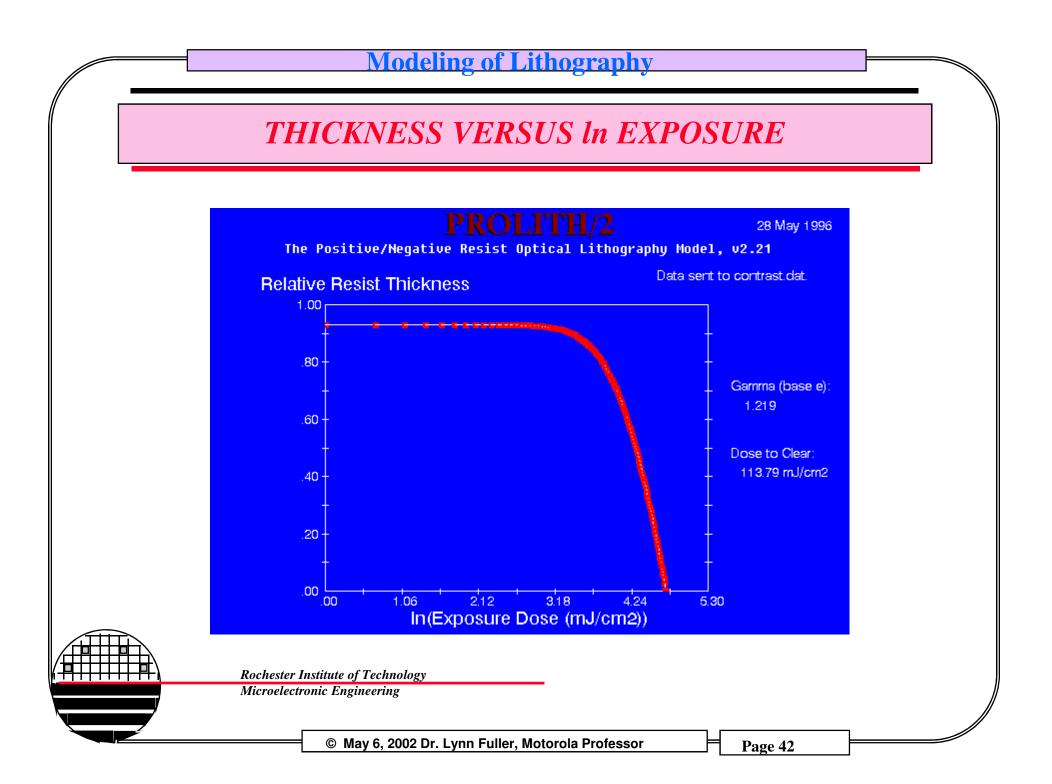


# AERIAL IMAGE VERSUS COHERENCY $\sigma$



### Modeling of Lithography SWING CURVE (DOSE TO CLEAR VS THICKNESS) 28 May 1996 The Positive/Negative Resist Optical Lithography Model, v2.21 Data sent to mr42x8.dat. Dose to Clear Eo (mJ/cm2) 250 200. 150. 100. 50. 0. 1.10 1.20 1.30 1.40 1.00 1.50 **Resist Thickness (um) Rochester Institute of Technology** Microelectronic Engineering

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## **REFERENCES**

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