ROCHESTER INSTITUTE OF TEHNOLOGY MICROELECTRONIC ENGINEERING

Diode Sensors Theory

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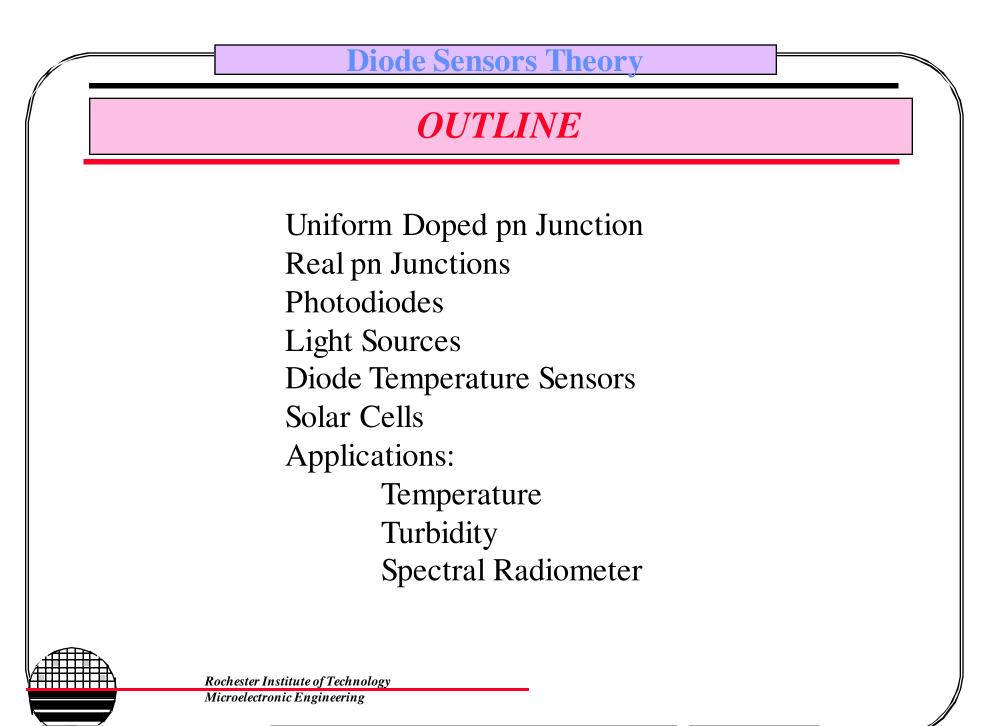


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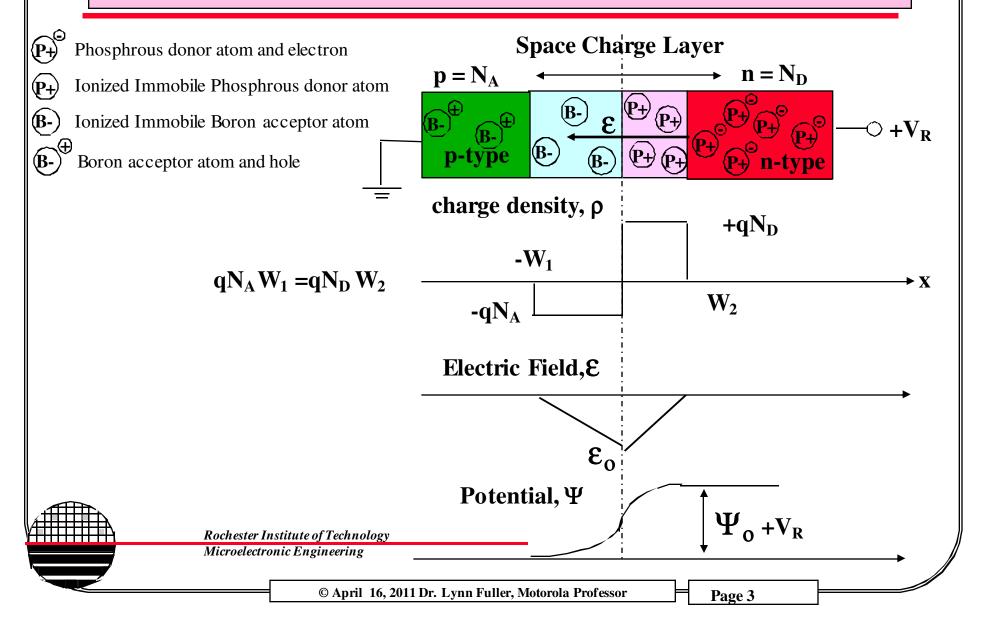
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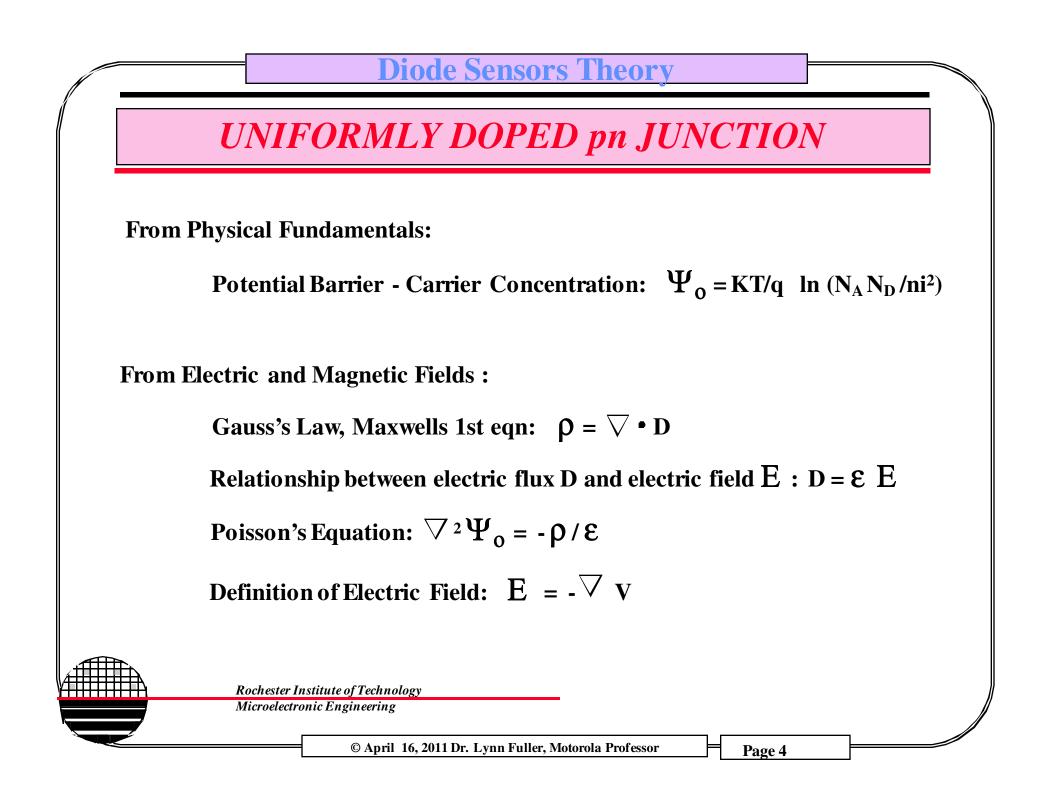
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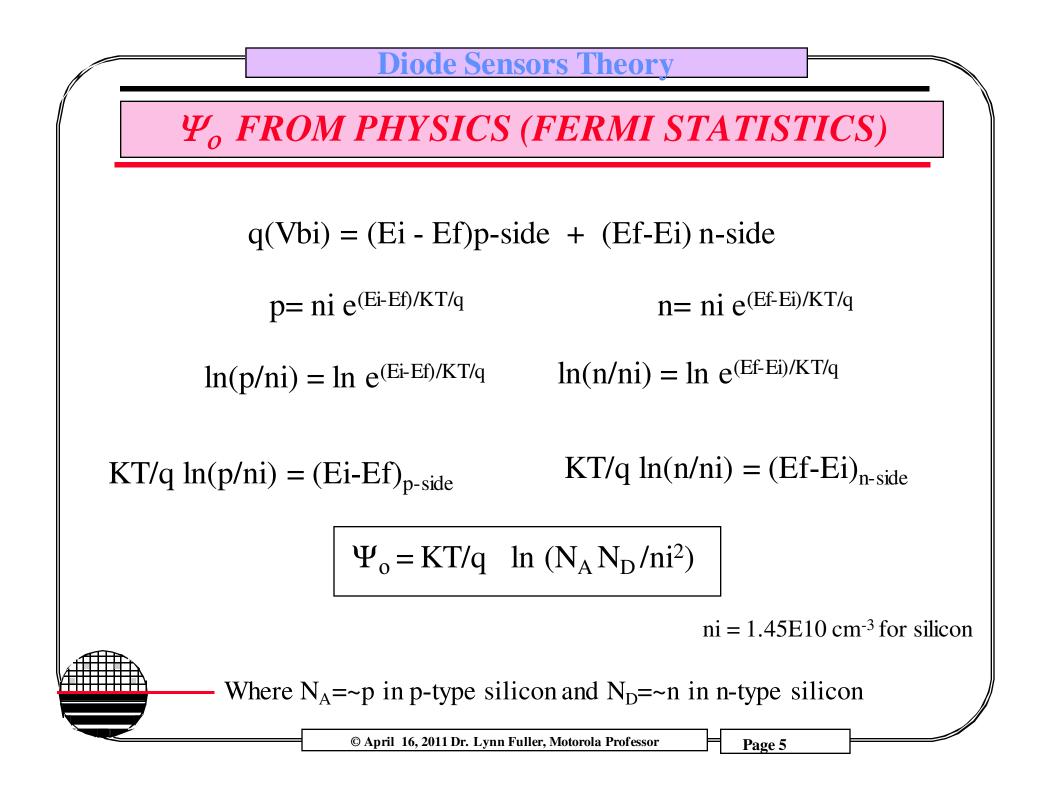


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UNIFORMLY DOPED PN JUNCTION







UNIFORMLY DOPED PN JUNCTION

Built in Voltage: $\Psi_0 = KT/q \ln (N_A N_D/ni^2)$

 $ni = 1.45E10 \text{ cm}^{-3}$

Width of Space Charge Layer, W: with reverse bias of V_R volts

W = (W₁ + W₂) = [(2ε/q) (
$$\Psi_0 + V_R$$
) (1/N_A+ 1/N_D)]^{1/2}

W₁ width on p-side

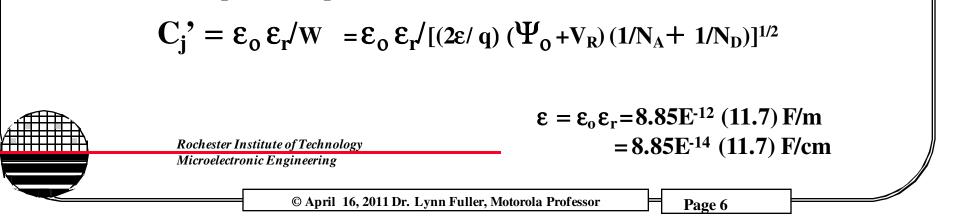
W₂ width on n-side

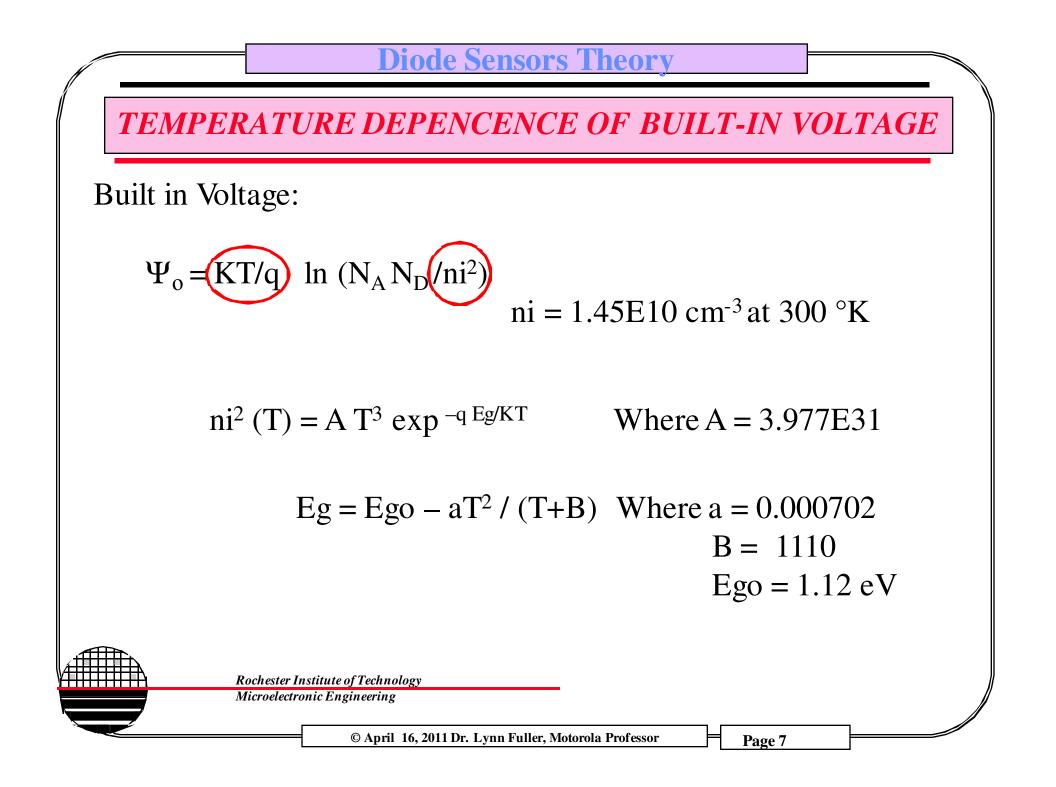
 $W_1 = W [N_D/(N_A + N_D)]$ $W_2 = W [N_A/(N_A + N_D)]$

Maximum Electric Field:

$$E_{o} = - [(2q/\epsilon) (\Psi_{o} + V_{R}) (N_{A} N_{D} / (N_{A} + N_{D}))]^{1/2}$$

Junction Capacitance per unit area:





Diode Sensors Theory EXAMPLE CALCULATIONS					
	ROCHESTER INSTITUTE OF TECHNOLOGY MICROELECTRONIC ENGINEERING	PN.XLS 4/16/2011			
Width of space charge layer depends on the doping on both sides and the	CALCULATIONS FOR PN JUNCTION (ELECTROSTATICS) DR. LYNN FULLER To use this spreadsheed change the values in the white boxes. The rest of the sheet is protected and should not be changed unless you are sure of the consequences. The calculated results are shown in the purple boxes. <u>CONSTANTS</u> VARIABLES K 1.38E-23 J/K q 1.60E-19 Coul Temp 300 K				
applied reverse bias voltage and temperature.	Ego 1.12 eV zo 8.85E-14 F/cm Nd = 1.00E+16 cm-3 zr 11.7 Na = 5.00E+14 cm-3 ni 1.45E+10 cm-3 Breakdown E 3.00E+05 V/cm Vr = 0 Volts CALCULATIONS:	Reverse Bias Voltage			
Rochester Institute Microelectronic Eng	Eg = Ego - (aT^2/(T+B) ni^2 = A T^3 e^(-Eg/KT/q) KT/q = Vbi = (KT/q) ln (NaNd/ni2) W = [(2z/q)(Vbi+Vr)(1/Na +1/Nd)]^0.5 W1 = W[Nd/(Na+Nd)] W2 = W[Nd/(Na+Nd)] Eo = -[(2q/eoer)(Vbi+Va)(NaNd/(Na+Nd)]^0.5	1.075 eV 9.84E+20 cm-6 0.0259 Volts 0.58 Volts 1.25 μm 1.19 μm -9.23E+03 V/cm 8.26E-09 F/cm2			

EXAMPLE

Example: If the doping concentrations are Na=1E15 and Nd=3E15 cm⁻³ and the reverse bias voltage is 0, then find the built in voltage, width of the space charge layer, width on the n-side, width on the p-side, electric field maximum and junction capacitance. Repeat for reverse bias of 10, 40, and 100 volts.

$$\Psi_{o} = Vbi = KT/q \quad \ln (N_{A}N_{D}/ni^{2}) =$$

$$W = (W_{1} + W_{2}) = [(2\epsilon/q)(\Psi_{o} + V_{R})(1/N_{A} + 1/N_{D})]^{1/2} =$$

$$W1 =$$

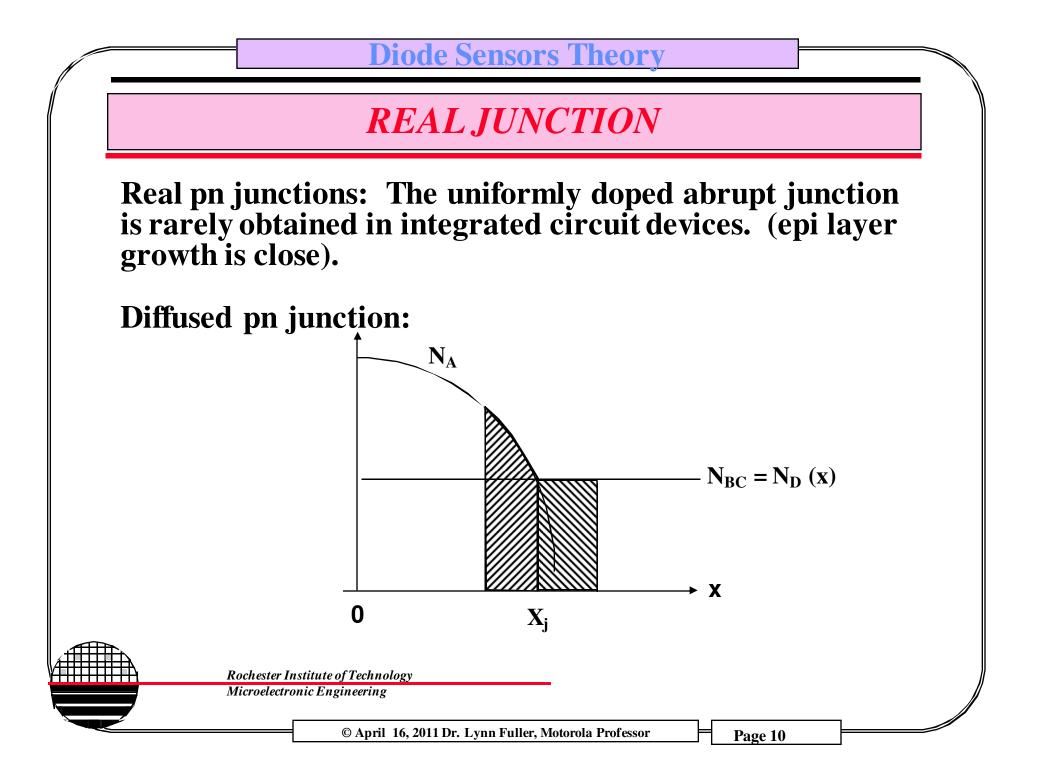
$$W2 =$$

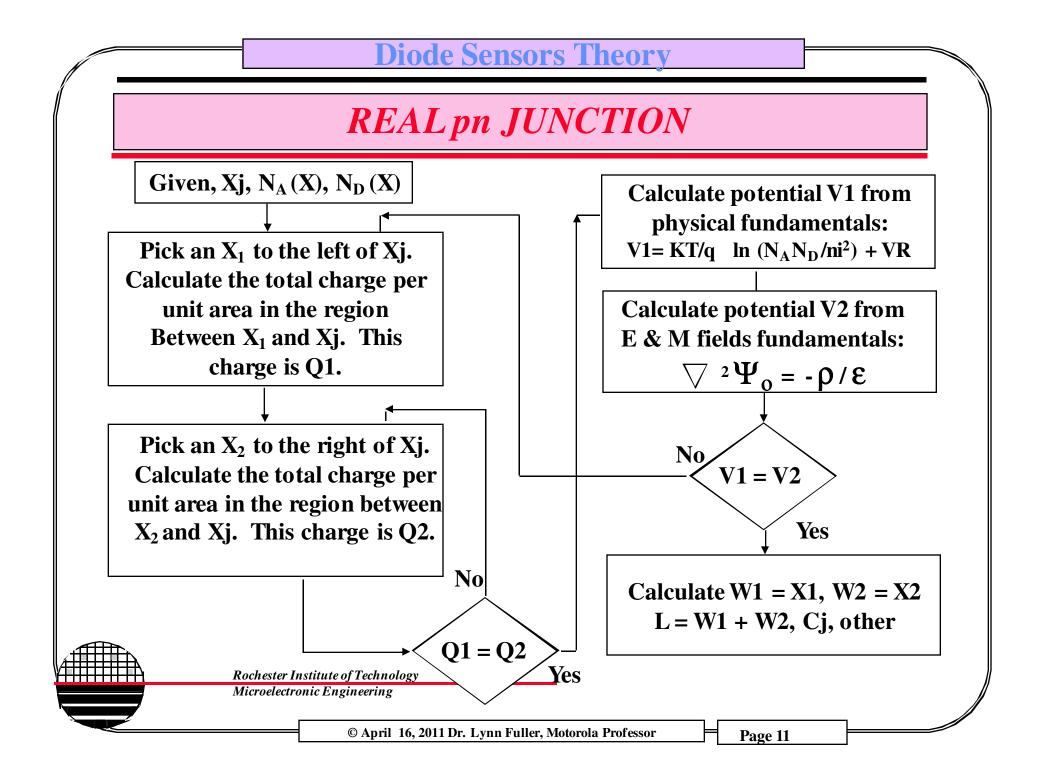
$$Emax =$$

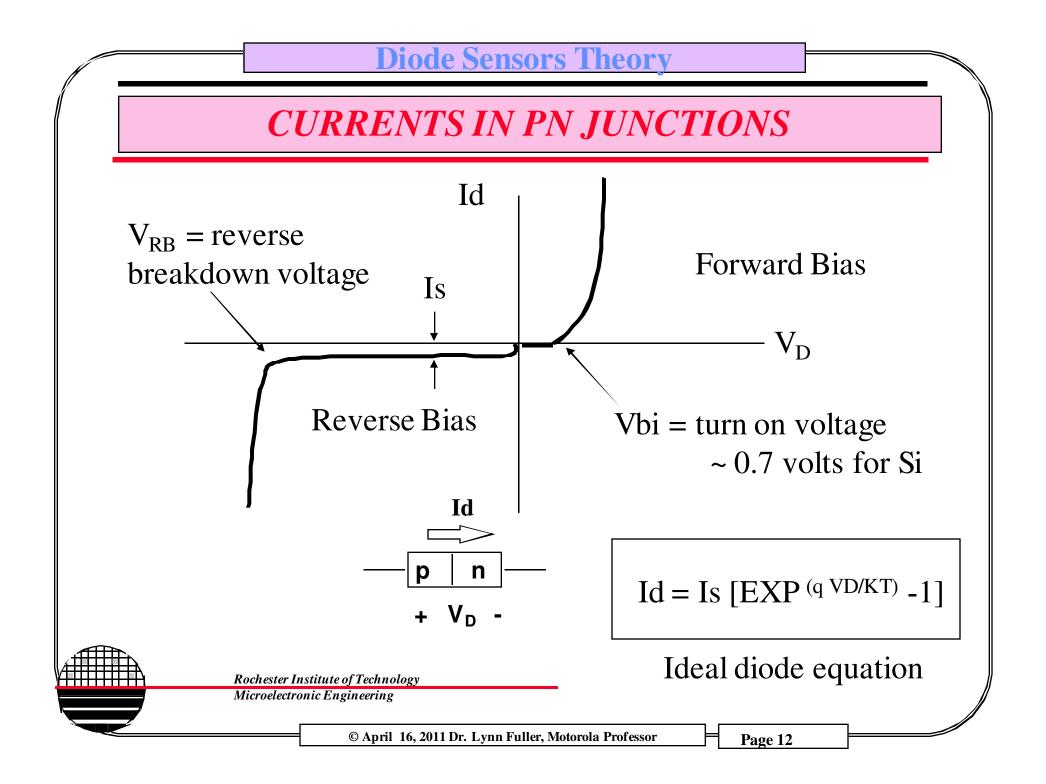
$$Cj =$$

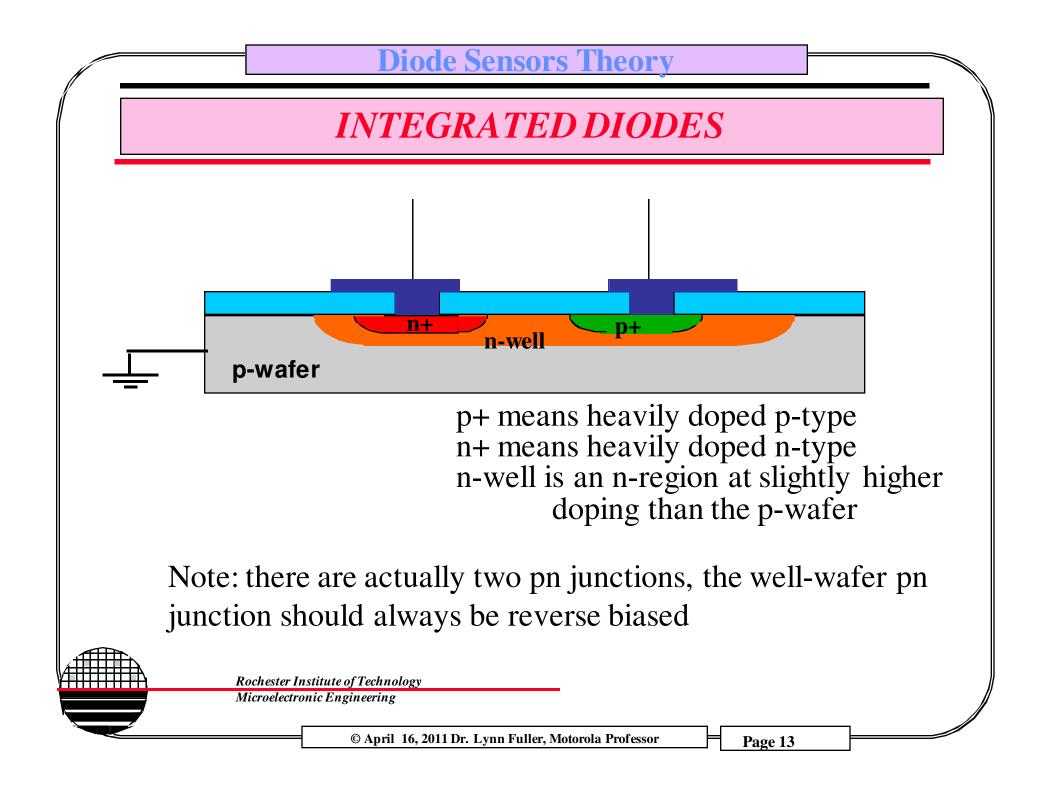
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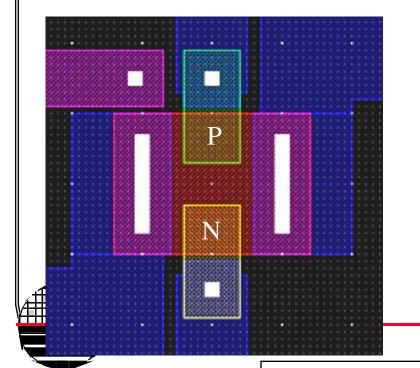


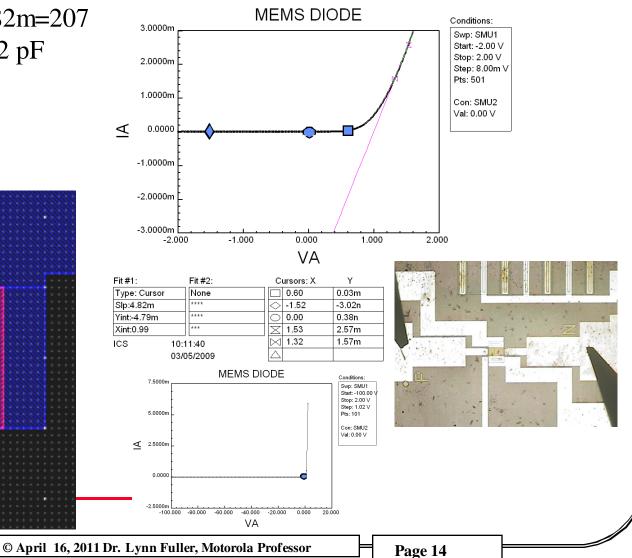




REAL DIODES

Series Resistance =1/4.82m=207 Junction Capacitance ~ 2 pF Is = 3.02E-9 amps BV = > 100 volts Size 80µ x 160µ





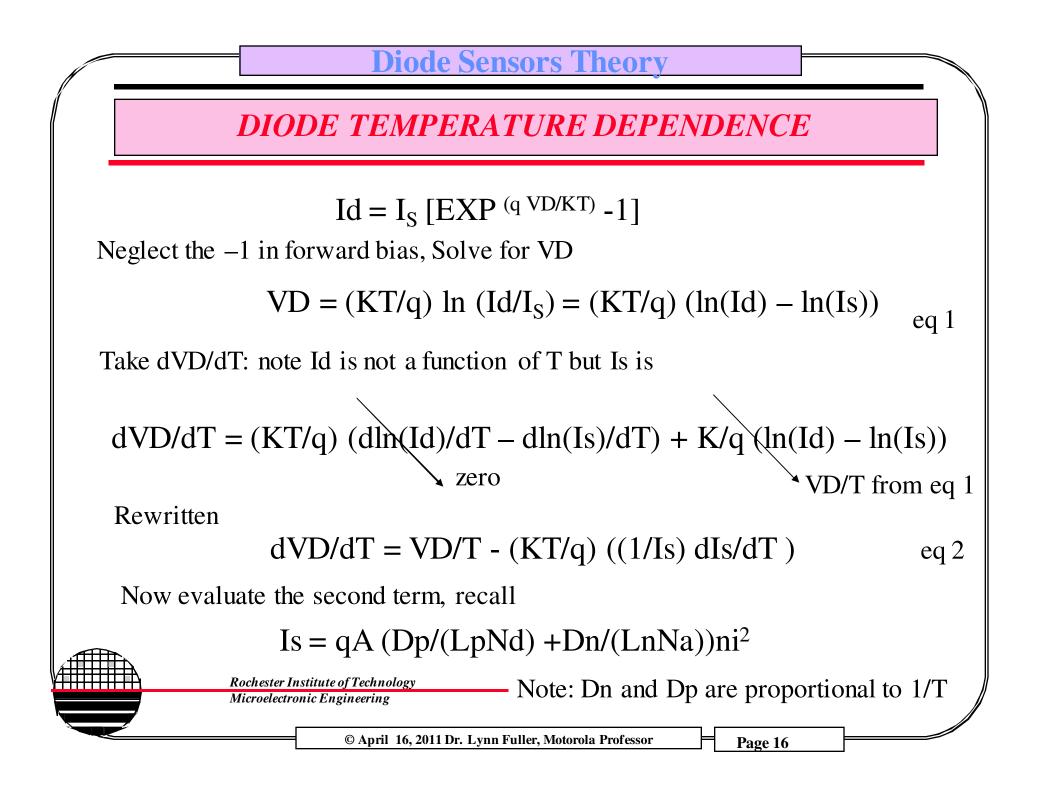
DIODE SPICE MODEL

		MEMS Diode
Model Parameter	Default Value	Extracted Value
Is reverse saturation current	1e-14 A	3.02E-9A
N emission coefficient	1	1
RS series resistance	0	207 ohms
VJ built-in voltage	1 V	0.6
CJ0 zero bias junction capacitance	0	2pF
M grading coefficient	0.5	0.5
BV Breakdown voltage	infinite	400
IBV Reverse current at breakdown	1E-10A	-

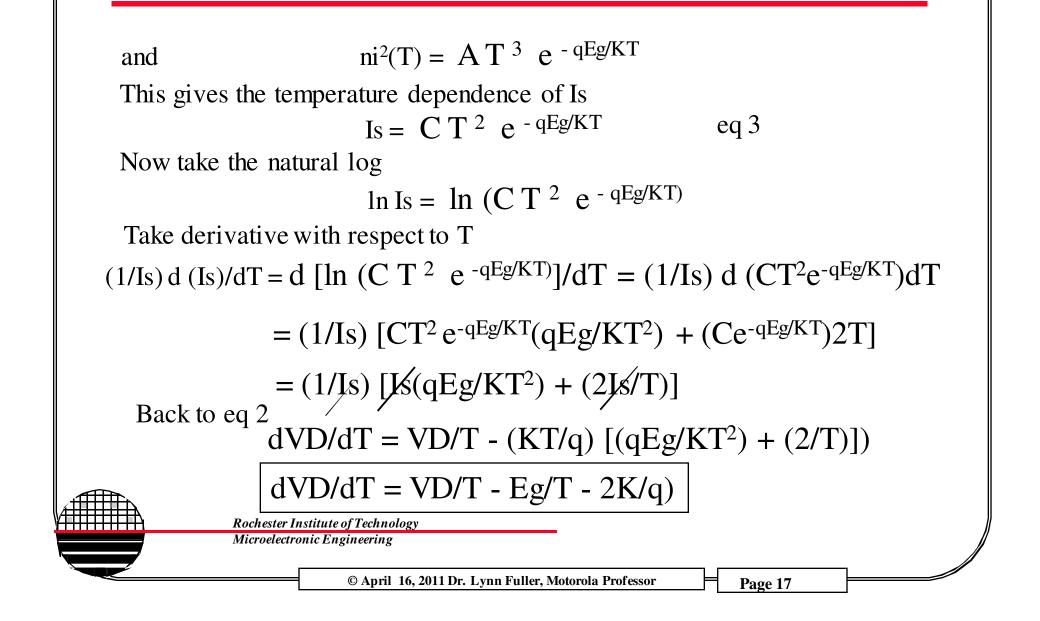
DXXX N(anode) N(cathode) Modelname .model Modelname D Is=1e-14 Cjo=.1pF Rs=.1 .model RITMEMS D IS=3.02E-9 N=1 RS=207 +VJ=0.6 CJ0=2e-12 M-0.5 BV=400

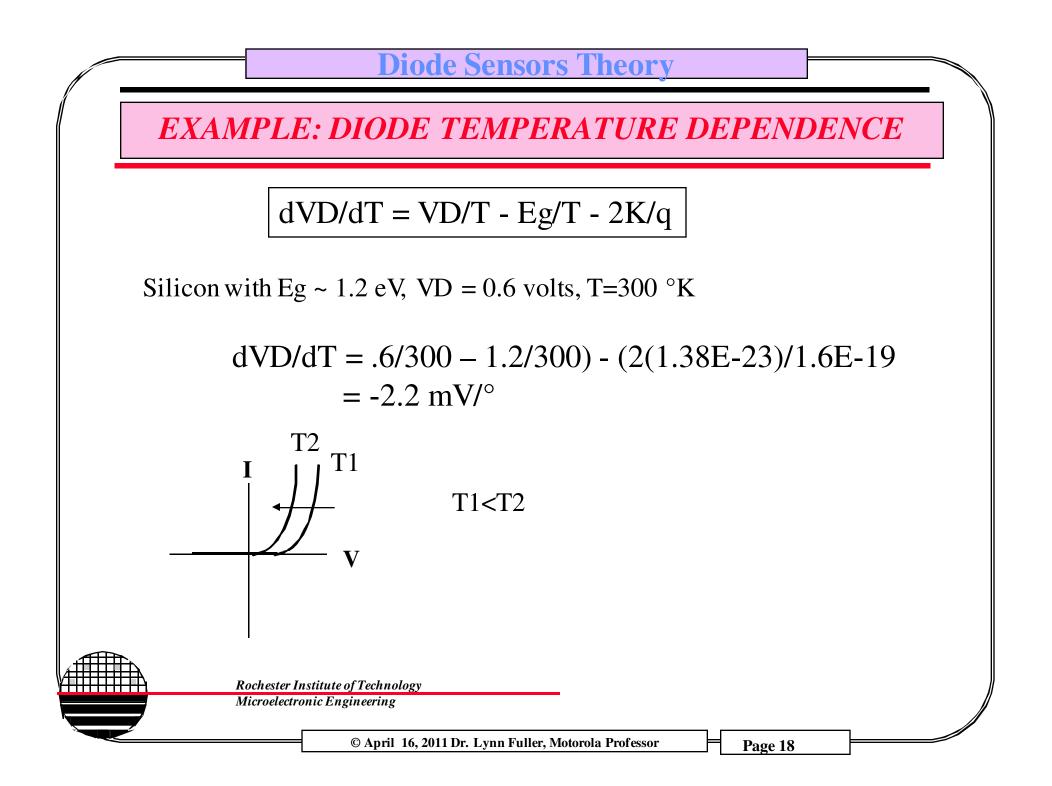
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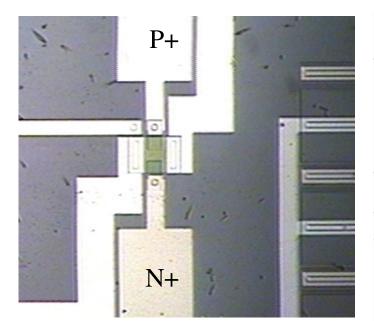


DIODE TEMPERATURE DEPENDENCE

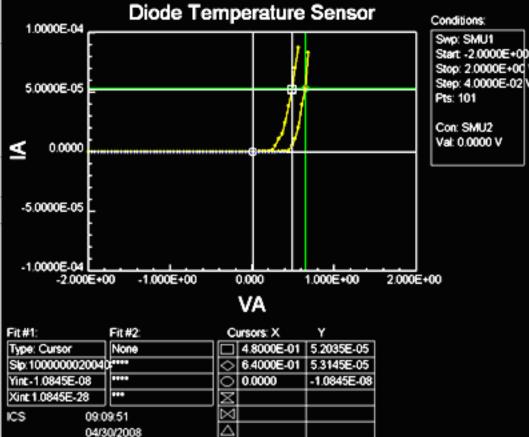




DIODE AS A TEMPERATURE SENSOR



Poly Heater Buried pn Diode, N+ Poly to Aluminum Thermocouple



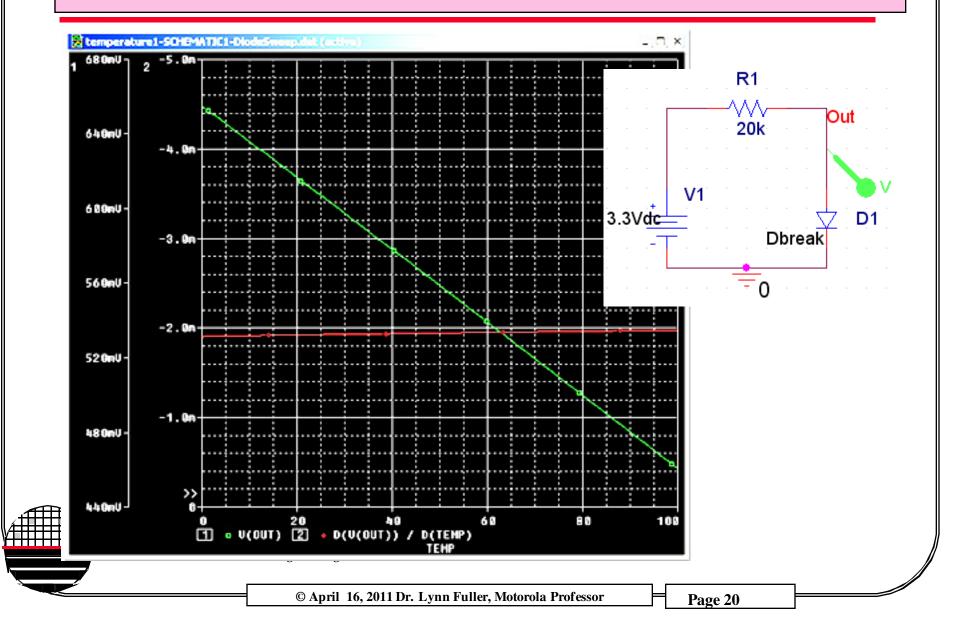
Compare with theoretical -2.2mV/°C

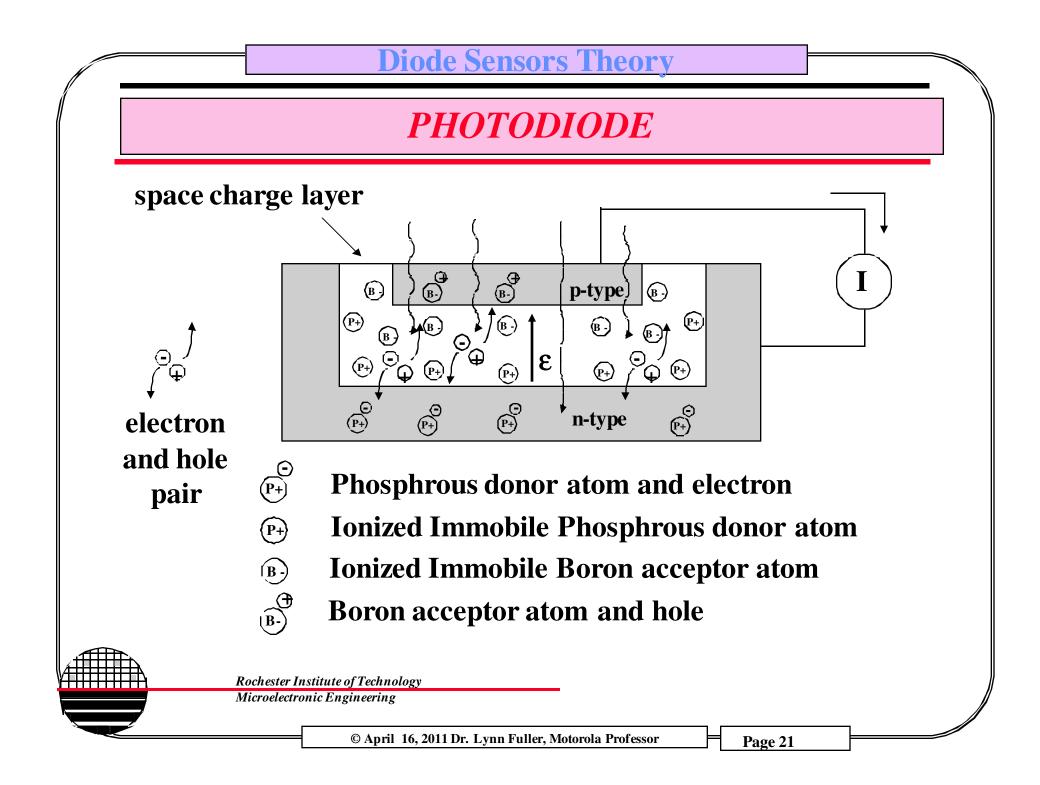
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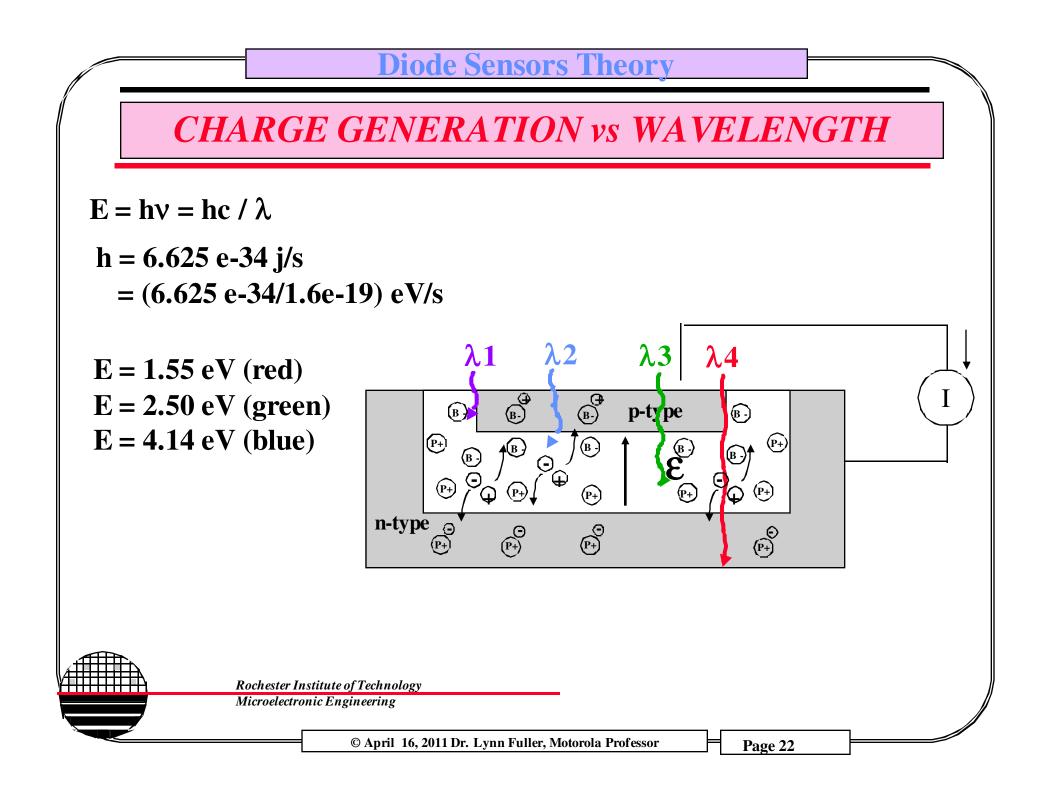
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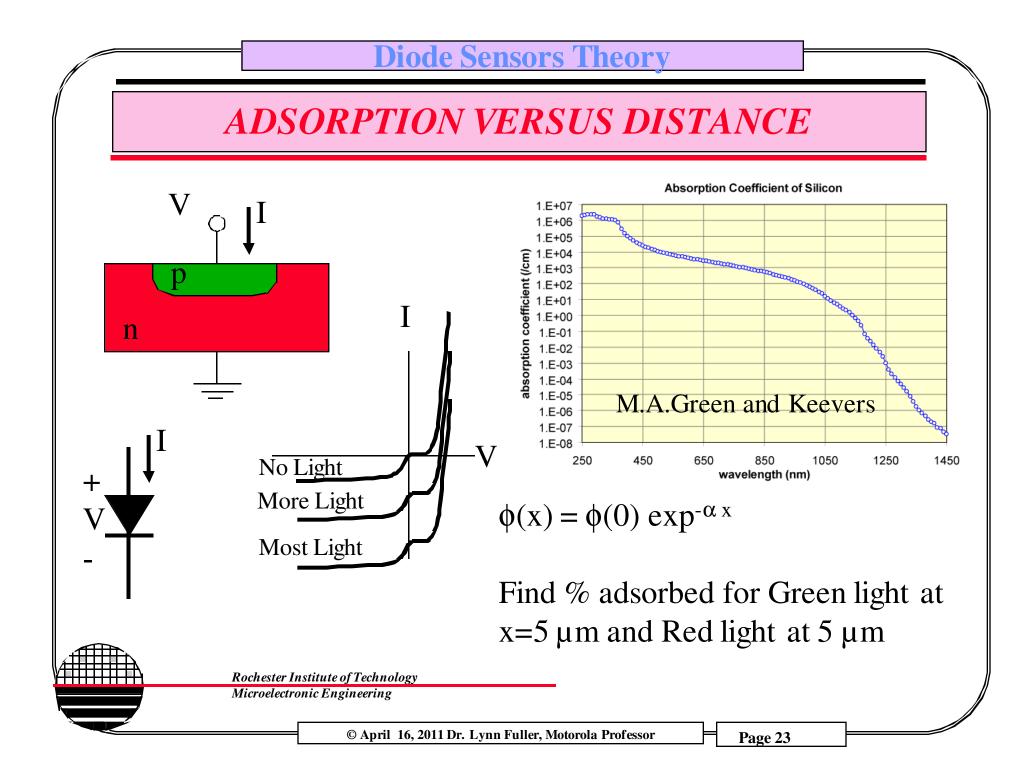
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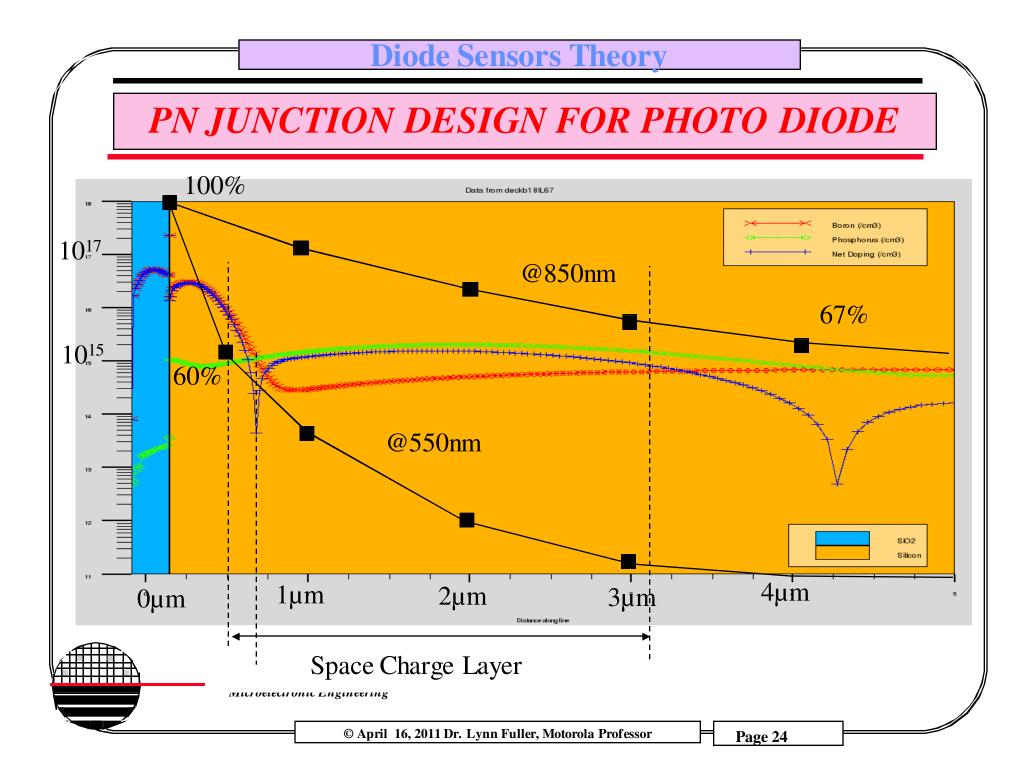
SPICE FOR DIODE TEMPERATURE SENSOR



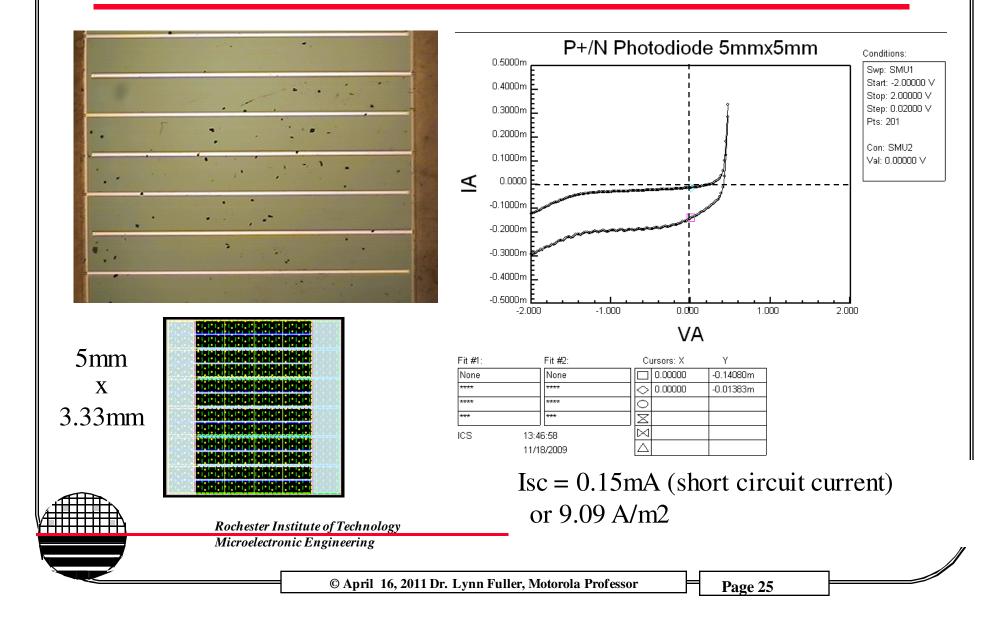




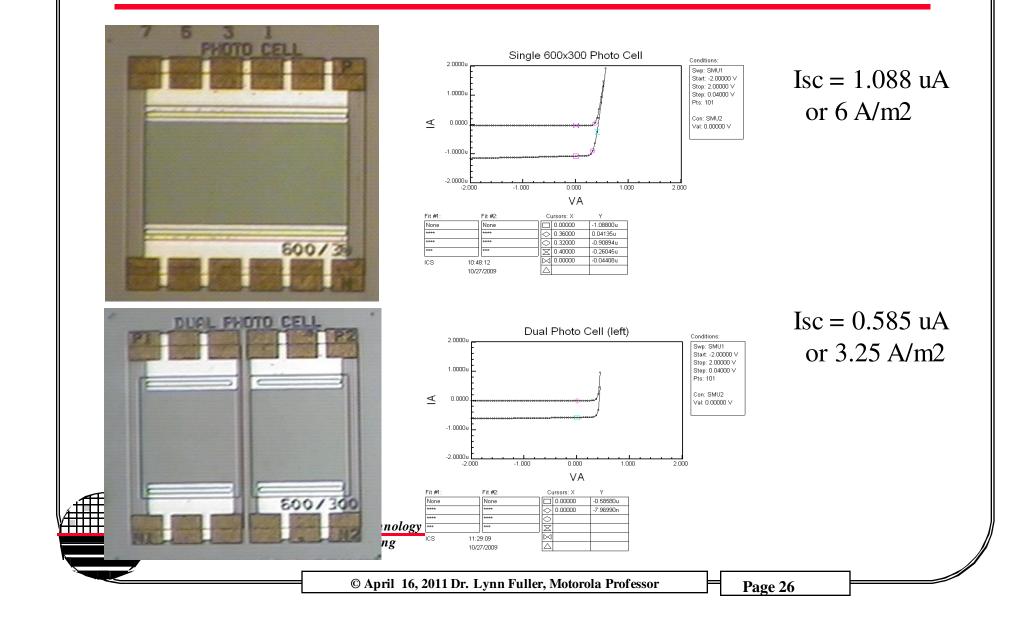




LARGE 5mm X 5mm PHOTODIODE



SINGLE AND DUAL PHOTO CELL



SOLAR CELL TUTORIAL

SOME TERMS AND DEFINITIONS:

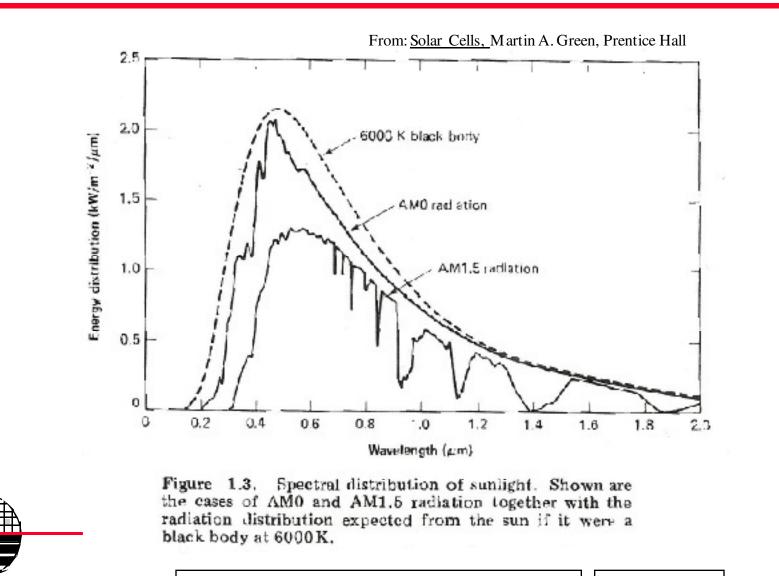
Air Mass – amount of air between sun and solar cell. In space AM=0 at the equator at noon AM=1, if the sun is arriving at an angle θ , AM=1/cos θ . AM1.5 is the standard for most solar cell work in USA and gives a sum total of 1000w/m2 over the entire spectrum of wavelengths from 0.2um to 2.0um

Efficiency is the ratio of the power out of a solar cell to the power falling on the solar cell (normally 1000w/m2 with the AM1.5 spectrum) Since Si solar cells can not absorb much of the infrared spectrum from the sun, and other factors, typical efficiencies are limited to 26-29% for basic silicon solar cells.

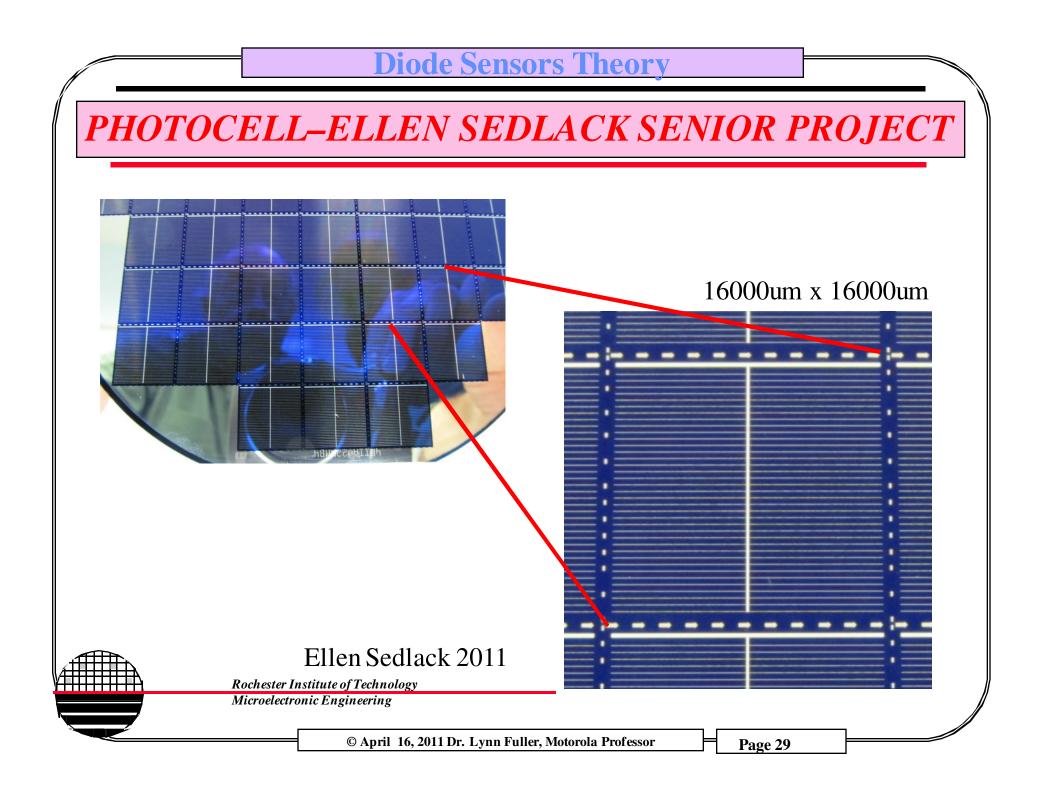
Quantum Efficiency – normalized ratio of electrons and holes collected to photons incident on the cell at a single wavelength, given in %.

FF – Fill Factor, a figure of merit, the "squareness " of the diode I-V characteristic in 4^{th} quadrant with light falling on the cell.

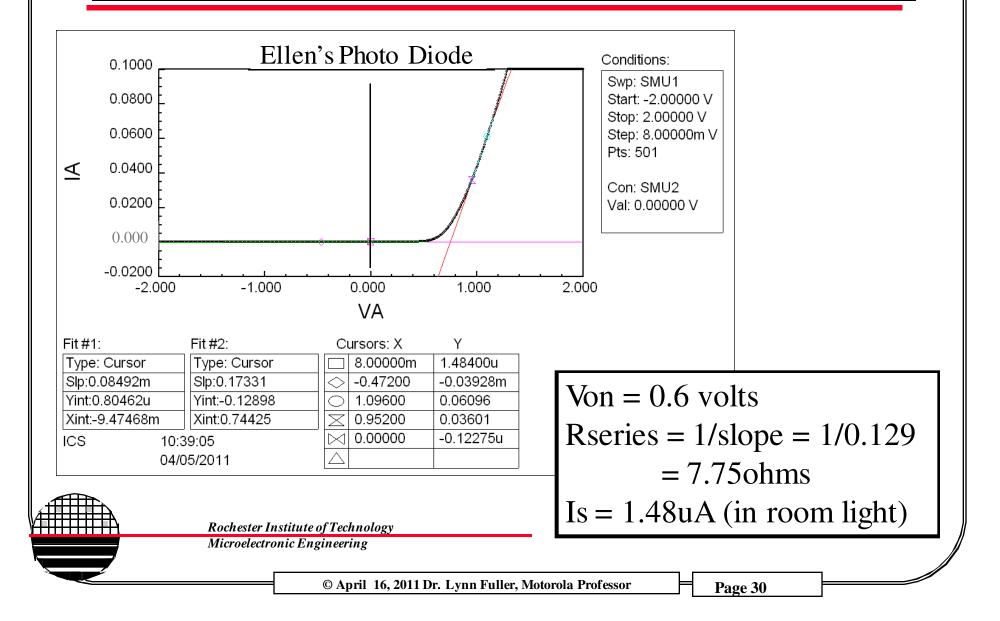
SOLAR CELL TUTORIAL



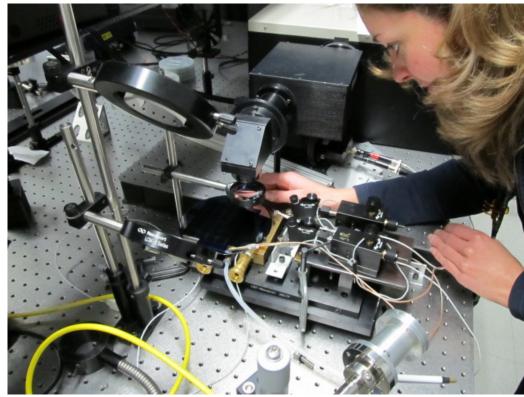
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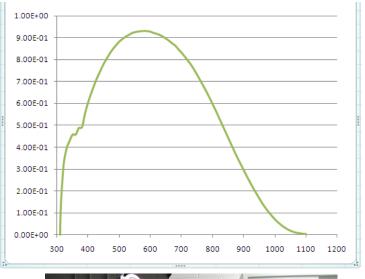
I-V CHARACTERISTICS OF PHOTO CELL



PHOTOCELL – QUANTUM EFFICIENCY



93% between 550nm and 650nm





Ellen Sedlack 2011

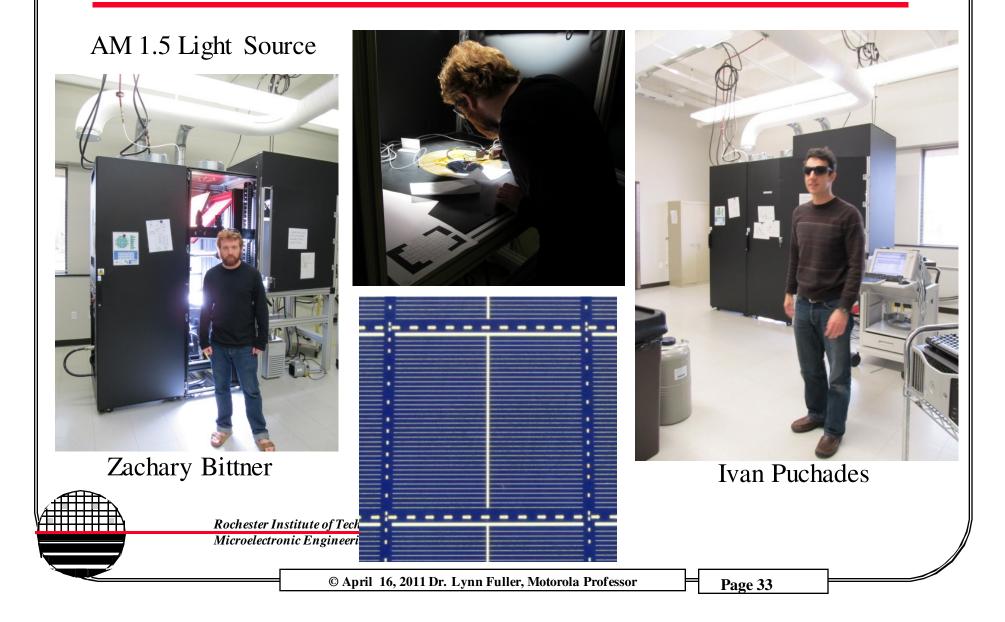
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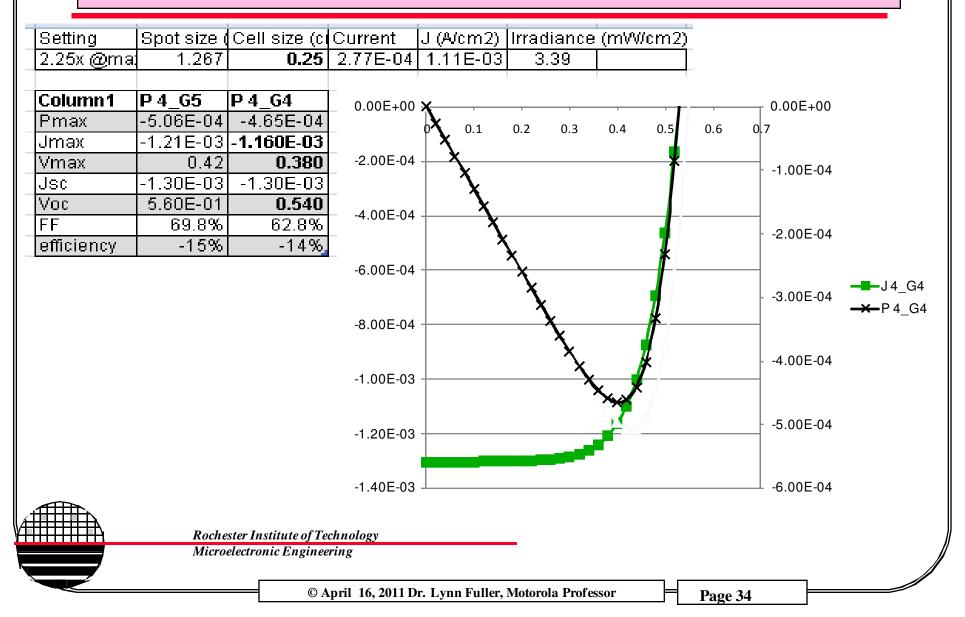
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Diode Sensors Theory SOLAR CELL TUTORIAL **Voc** - open circuit voltage **Isc** – short circuit voltage **Vmp** – Voltage at maximum power **Imp** – Current at maximum power Vmp Voc $\mathbf{FF} - \mathbf{FF} = \mathbf{VmpImp/VocIsc}$ No Light Diode I vs V Max Power Power = $I \times V$ ╋ Imp Most Light Isc **Rochester Institute of Technology** Microelectronic Engineering © April 16, 2011 Dr. Lynn Fuller, Motorola Professor Page 32

PHOTOCELL – POWER EFFICIENCY



POWER, EFFICIENCY, Isc, Voc



BANDGAP OF VARIOUS SEMICONDUCTORS

 $\mathbf{E} = \mathbf{h}\mathbf{v} = \mathbf{h}\mathbf{c} / \lambda$

What wavelengths will not generate e-h pairs in silicon. Thus silicon is transparent or light of this wavelength or longer is not adsorbed?

Semiconductor	Bandgap (eV) 300 K	Bandgap (eV) 0 K	λmax (µm) 300 K
BN	7.500		0.165
с	5.470	5.480	0.227
ZnS	3.680	3.840	0.337
GaN	3.360	3.500	0.369
ZnO	3.350	3.420	0.370
Alpha-SiC	2.996	3.030	0.414
CdS	2.420	2.560	0.512
GaP	2.260	2.340	0.549
BP	2.000		0.620
CdSe	1.700	1.850	0.729
AlSb	1.580	1.680	0.785
CdTe	1.560		0.795
GaAs	1.420	. 1.520	0.873
InP	1.350	1.420	0.919
Si	1.120	1.170	1.107
GaSb	0.720	0.810	1.722
Ge	0.660	0.740	1.879
PbS	0.410	0.286	3.024
InAs	0.360	0.420	3.444
РЬТе	0.310	0.190	4.000
InSb	0.170	0.230	7.294
Sn		0.082	15.122 @ 0 K

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Table of various semiconductors in order of increasing λ_{max} . From Sze (1981).

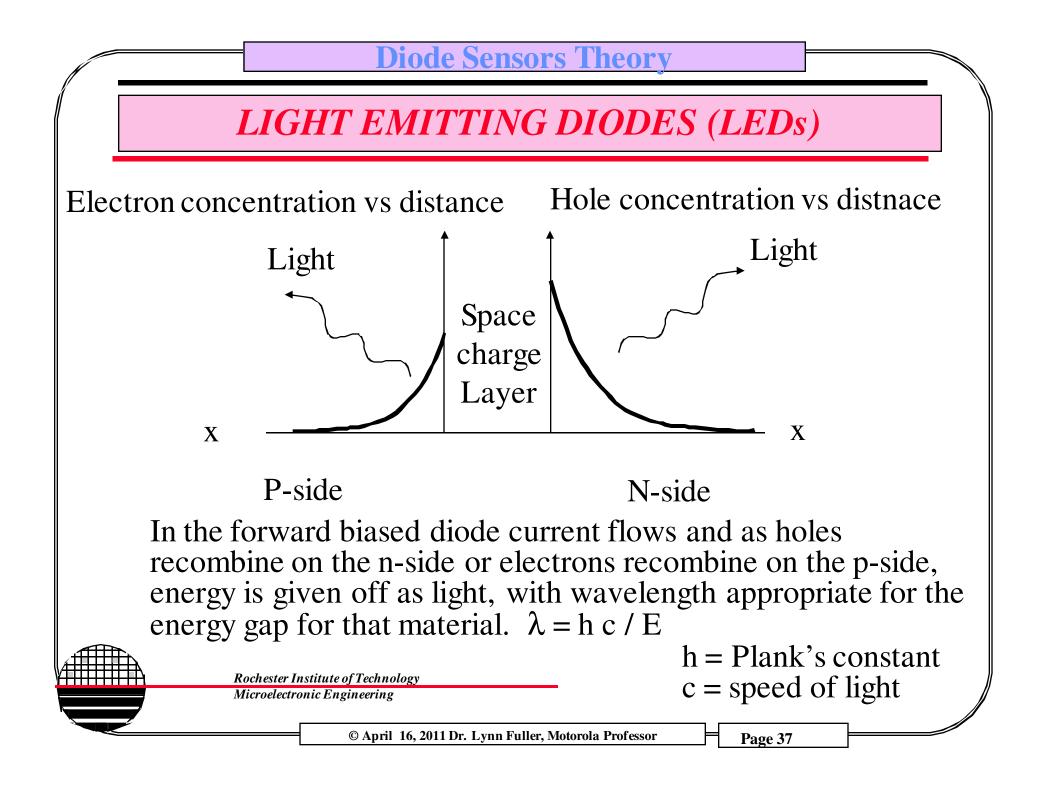
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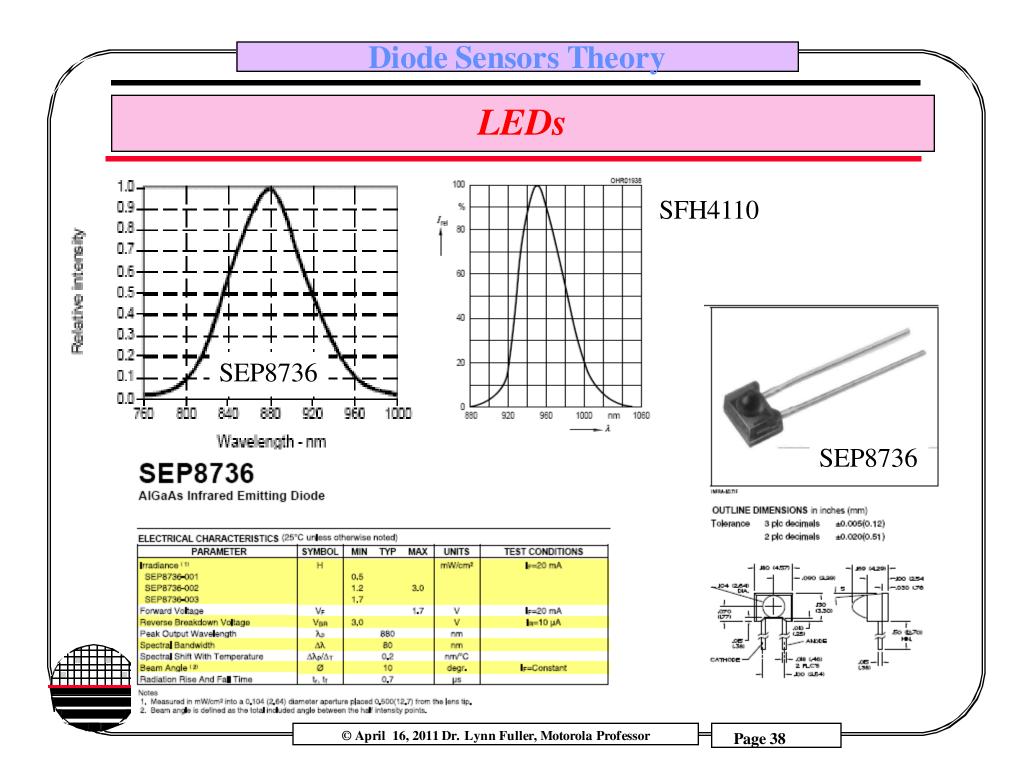
TYPES OF PHOTODETECTORS

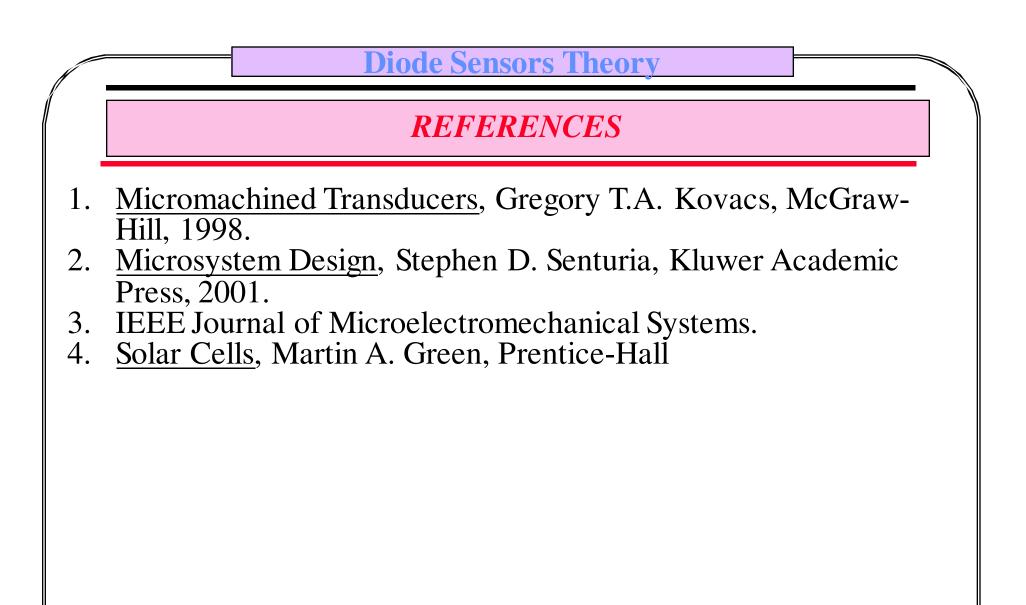
Device Type	Gain	Response Time (s)	Typical Temperature
Photomultiplier	> 106	10 ^{.7} to 10 ^{.9}	300 (sometimes cooled)
Photoconductor	1 to 106	10-3 to 10-8	4.2 to 300
Metal-Semiconductor-Metal Photodetector	1 or less	10-10 to 10-12	300
p-n Photodiode	1 or less	10-6 to 10-11	300 (sometimes cooled to 77 K)
p-i-n Photodiode	1 or less	10-6 to 10-9	300
Metal-Semiconductor Diode	1 or less	10 ⁻⁹ to 10 ⁻¹²	300
Avalanche Diode	10 ² to 10 ⁴	10-10	300
Bipolar Phototransistor	102	10-6 to 10-8	300
Bipolar Photo-Darlington	104	- 10-5 to 10-6	300
Field-Effect Phototransistor	10	10-7	300
CCD Cell (Metal-Insulator- Semiconductor Capacitor)	1 or less	10-5 to 10-8	300 (sometimes cooled)

Gains and response times of some typical photodetectors (some are optimistic!). After Sze (1981). Note that the CCD cell, and some extrinsic photoconductors, are integrating detectors, and thus the response time figures can be somewhat misleading.

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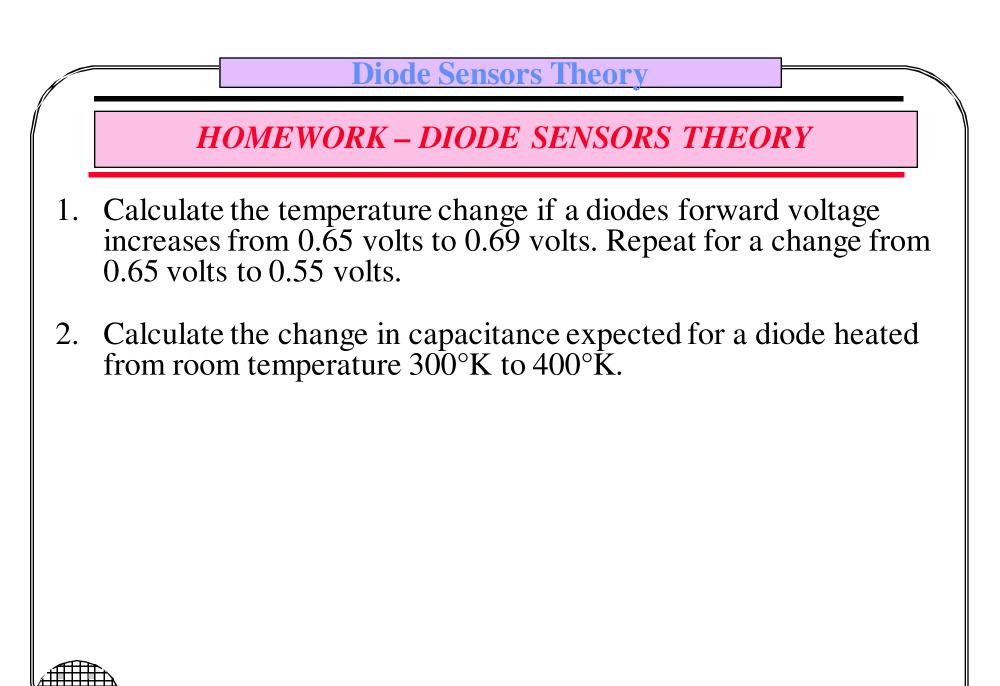




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